

1. General description

Planar passivated high commutation three quadrant triac in a SOT186A (TO-220F) "full pack" plastic package. This "series E" triac balances the requirements of commutation performance and gate sensitivity and is intended for interfacing with low power drivers including microcontrollers.

2. Features and benefits

- 3Q technology for improved noise immunity
- · Direct interfacing with low power drivers and microcontrollers
- Good immunity to false turn-on by dV/dt
- High commutation capability with sensitive gate
- High voltage capability
- Isolated mounting base package
- · Planar technology for voltage ruggedness and reliability
- Sensitive gate for easy logic level triggering
- Triggering in three quadrants only

3. Applications

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- General purpose motor controls
- Large and small appliances (White Goods)
- Loads such as contactors, circuit breakers, valves, dispensers and door locks
- Lower-power highly inductive, resistive and safety loads

4. Quick reference data

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Oymbol	i arameter	Conditions		- YP	IIIaA	Unit
V _{DRM}	repetitive peak off- state voltage		-	-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_h \le 110 \text{ °C}$; Fig. 1; Fig. 2; Fig. 3	-	-	2	A
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ t _p = 20 ms; <u>Fig. 4</u> ; <u>Fig. 5</u>	-	-	14	A
		full sine wave; $T_{j(init)}$ = 25 °C; t _p = 16.7 ms	-	-	15.4	A
Tj	junction temperature		-	-	125	°C
Static chara	acteristics					
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 7</u>	0.5	-	10	mA

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3Q Hi-Com Triac

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 7</u>	0.5	-	10	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 7</u>	0.5	-	10	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	-	10	mA
V _T	on-state voltage	I _T = 3 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.63	2	V
Dynamic ch	naracteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; R _{GT1(ext)} = 220 Ω	-	500	-	V/µs
	rate of change of commutating current	$V_D = 400 \text{ V}; T_j = 125 \text{ °C}; I_{T(RMS)} = 2 \text{ A};$ $dV_{com}/dt = 20 \text{ V}/\mu \text{s}; \text{ (snubberless condition); gate open circuit}$	2	-	-	A/ms
		V_D = 400 V; T _j = 125 °C; I _{T(RMS)} = 2 A; dV _{com} /dt = 10 V/µs; gate open circuit	2.3	-	-	A/ms

5. Pinning information

Die	Cumhal	Description	Oinsuelifie of excelling	Creation complexit
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2-T1
2	T2	main terminal 2		sym051
3	G	gate		Symoor
mb	n.c.	mounting base; isolated		
			TO-220F (SOT186A)	

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
BTA202X-600E	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A				

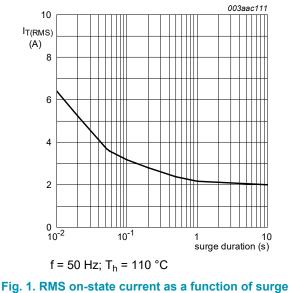


7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_h \le 110$ °C; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	2	A
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; t_p = 20 ms; Fig. 4; Fig. 5	-	14	A
		full sine wave; T _{j(init)} = 25 °C; t _p = 16.7 ms	-	15.4	А
l ² t	I ² t for fusing	t _p = 10 ms; SIN	-	0.98	A²s
dl _T /dt	rate of rise of on-state current	I _G = 0.2 A	-	100	A/µs
I _{GM}	peak gate current		-	2	А
P _{GM}	peak gate power		-	5	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C



duration; maximum values

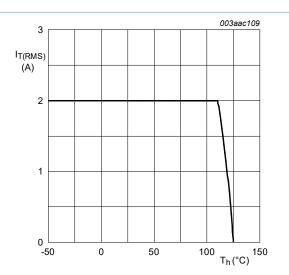
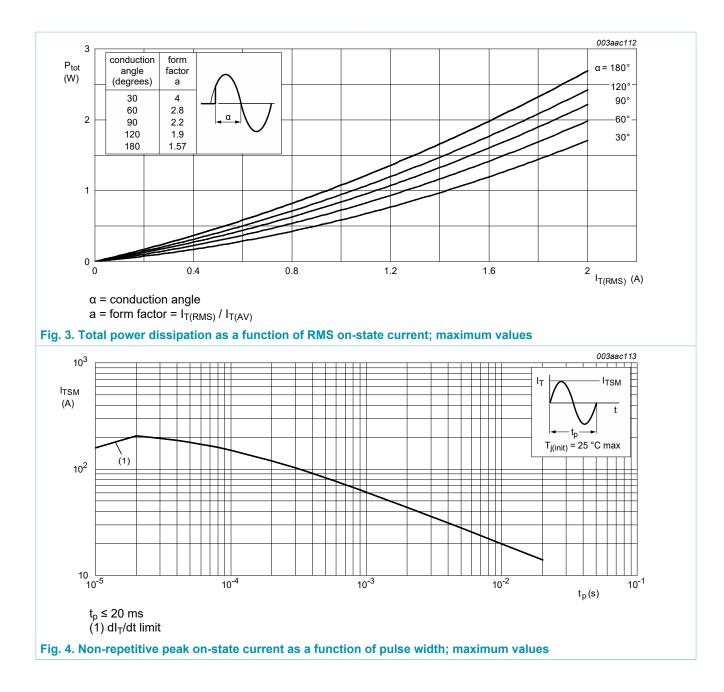


Fig. 2. RMS on-state current as a function of heatsink temperature; maximum values

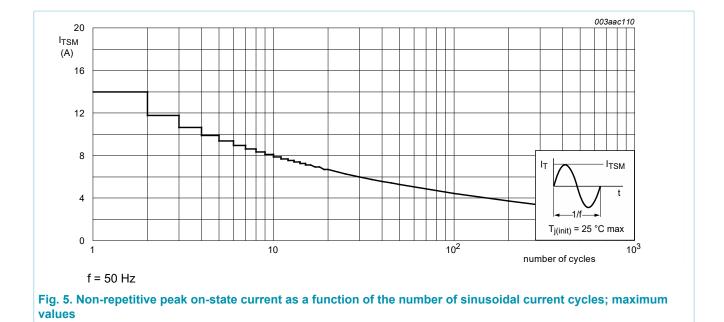
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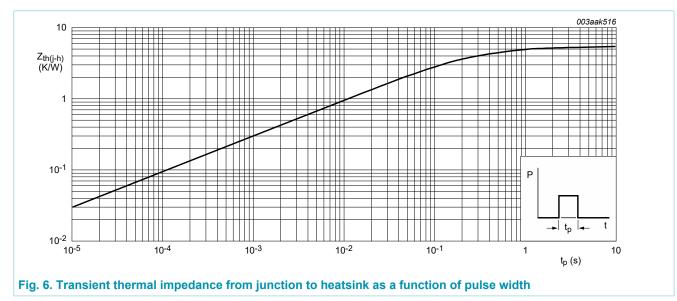
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8. Thermal characteristics

Table 5. Therm	al characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-h)}	thermal resistance from junction to heatsink	full cycle; with heatsink compound; Fig. 6	-	-	5.5	K/W
R _{th(j-a)}	thermal resistance from junction to ambient free air	in free air	-	55	-	K/W



9. Isolation characteristics

Table 6. Isolati	on characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{isol(RMS)}	RMS isolation voltage	from all terminals to external heatsink; sinusoidal waveform; clean and dust free; 50 Hz \leq f \leq 60 Hz; RH \leq 65 %; T _h = 25 °C	-	-	2500	V
C _{isol}	isolation capacitance	from main terminal 2 to external heatsink; f = 1 MHz	-	10	-	pF

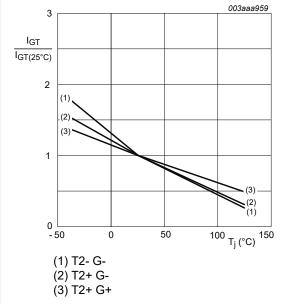
Table 5. Thermal characteristics

10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics				,	
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 7</u>	0.5	-	10	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 7</u>	0.5	-	10	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 7</u>	0.5	-	10	mA
I _L latching curre	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 8</u>	-	-	12	mA
		V_D = 12 V; I _G = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 8</u>	-	-	20	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 8</u>	-	-	12	mA
н	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	-	10	mA
V _T	on-state voltage	I _T = 3 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.63	2	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; <u>Fig. 11</u>	-	0.7	1	V
		V _D = 400 V; I _T = 0.1 A; T _j = 125 °C; Fig. 11	0.2	0.3	-	V
ID	off-state current	V _D = 600 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic ch	naracteristics		·			
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; R _{GT1(ext)} = 220 Ω	-	500	-	V/µs
dl _{com} /dt	rate of change of commutating current	V_D = 400 V; T _j = 125 °C; I _{T(RMS)} = 2 A; dV _{com} /dt = 20 V/µs; (snubberless condition); gate open circuit	2	-	-	A/m
		V_D = 400 V; T _j = 125 °C; I _{T(RMS)} = 2 A; dV _{com} /dt = 10 V/µs; gate open circuit	2.3	-	-	A/m

BTA202X-600E

3Q Hi-Com Triac





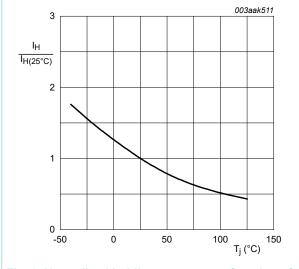
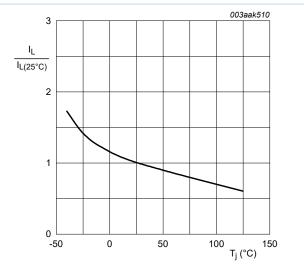
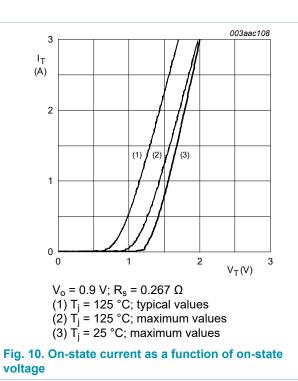


Fig. 9. Normalized holding current as a function of junction temperature

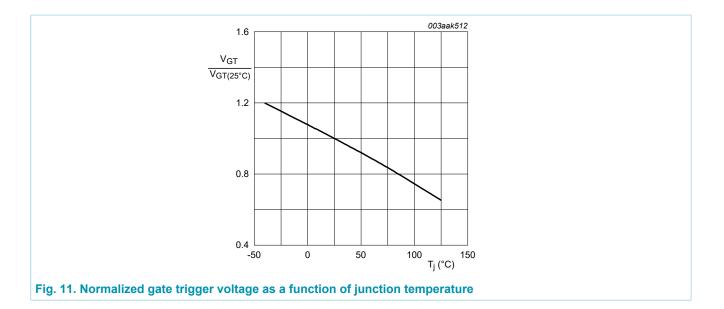






BTA202X-600E

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11. Package outline

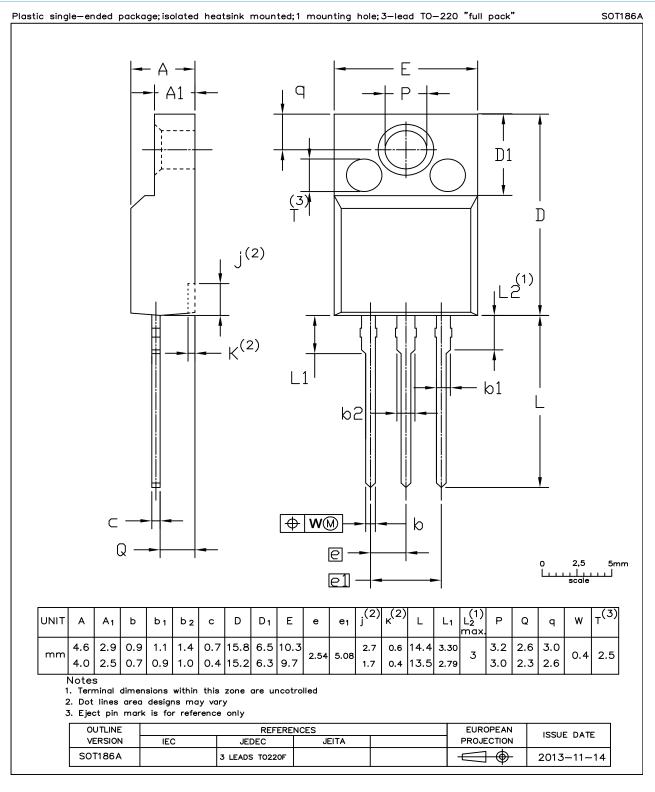


Fig. 12. Package outline TO-220F (SOT186A)

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12. Legal information

Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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BTA202X-600E

13. Contents

1.	General description1	
2.	Features and benefits1	
3.	Applications1	
4.	Quick reference data1	
5.	Pinning information2	
6.	Ordering information2	
7.	Limiting values 3	í
8.	Thermal characteristics	j
9.	Isolation characteristics6	í
10.	Characteristics7	,
11.	Package outline10	I
12.	Legal information11	

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ООО "ЛайфЭлектроникс"

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