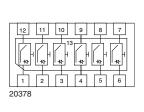
RoHS

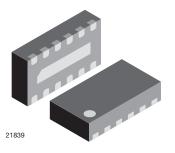
**GREEN** (5-2008)



## Vishay Semiconductors

## 6-Channel EMI-Filter with ESD-Protection

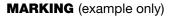




#### **FEATURES**

- Ultra compact LLP2513-13L package
- Low package profile of 0.6 mm
- 6-channel EMI-filter
- · Low leakage current
- Line resistance  $R_S = 100 \Omega$
- Typical cut off frequency f<sub>3dB</sub> = 240 MHz
- ESD-protection acc. IEC 61000-4-2
  - ± 10 kV contact discharge
  - ± 12 kV air discharge
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)

 Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>





Dot = pin 1 marking

YY = type code (see table below)

XX = date code

ORDERING INFORMATION					
DEVICE NAME	EVICE NAME ORDERING CODE		MINIMUM ORDER QUANTITY		
VEMI65AC-HCI	VEMI65AC-HCI-GS08	3000	15 000		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VEMI65AC-HCI	LLP2513-13L	9T	5.5 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

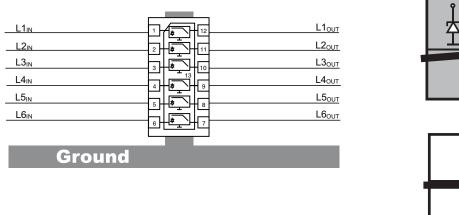
ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	All I/O pin to pin 13; acc. IEC 61000-4-5; t <sub>p</sub> = 8/20 µs; single shot	I <sub>PPM</sub>	2	Α	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	\/	± 10	kV	
	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 12		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C	
Storage temperature		T <sub>STG</sub>	- 55 to + 150	°C	



## Vishay Semiconductors

### **APPLICATION NOTE**

With the VEMI65AC-HCI 6 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behaviour is <u>Bi</u>directional and <u>Asymmetric</u> (BiAs).



EMI

**BiAs** 

20379

The 6 independent EMI-filter are placed between

pin 1 and pin 12,

pin 2 and pin 11,

pin 3 and pin 10,

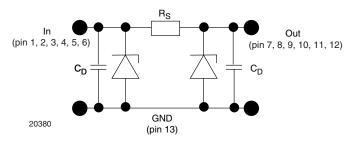
pin 4 and pin 9,

pin 5 and pin 8 and

pin 6 and pin 7.

They all are connected to a common ground pin 13 on the backside of the package.

The circuit diagram of one EMI-filter-channel shows two identical Z-diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level ( $V_{BR}$ ) and the diode capacitance ( $C_D$ ). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance  $R_S$  between input and output the device works as a low pass filter. Low frequency signals ( $f < f_{3dB}$ ) pass the filter while high frequency signals ( $f > f_{3dB}$ ) will be shorted to ground through the diode capacitances  $C_D$ .



Each filter is symmetrical so that both ports can be used as input or output.



# Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> All inputs (pin 1 to pin 6) to ground (pin 13) (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of channels which can be protected	N <sub>channel</sub>	-	-	6	channel	
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	5	V	
Reverse voltage	at I <sub>R</sub> = 1 μA	V <sub>R</sub>	5	-	-	V	
Reverse current	at V <sub>R</sub> = V <sub>RWM</sub>	I <sub>R</sub>	-	< 0.1	1	μΑ	
Reverse break down voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6	-	-	V	
Pos. clamping voltage	at I <sub>PP</sub> = 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	-	-	7	V	
	at $I_{PP} = I_{PPM} = 2$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	-	-	8	V	
Neg. clamping voltage	at I <sub>PP</sub> = - 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	- 1	-	-	V	
	at I <sub>PP</sub> = I <sub>PPM</sub> = -2 A applied at the input, measured at the output; acc. IEC 61000-4-5	V <sub>C-out</sub>	- 1.2	-	-	V	
Input capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>IN</sub>	-	20	-	pF	
	at V <sub>R</sub> = 2.5 V; f = 1 MHz	C <sub>IN</sub>	-	13	-	pF	
ESD-clamping voltage	at ± 10 kV ESD-pulse acc. IEC 61000-4-2	V <sub>CESD</sub>	-	7.5	-	V	
Line resistance	Measured between input and output; I <sub>S</sub> = 10 mA	R <sub>S</sub>	90	100	110	Ω	
Cut-off frequency	$V_{IN}$ = 0 V; measured in a 50 $\Omega$ system	f <sub>3dB</sub>	-	240	-	MHz	

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

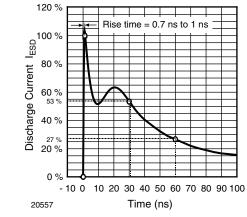


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$ /150 pF)

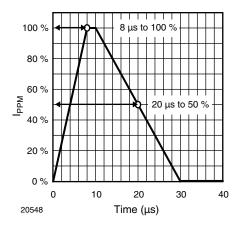


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5



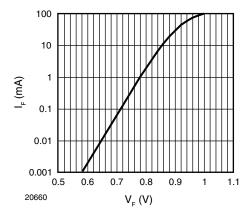


Fig. 3 - Typical Forward Current  $I_F$  vs. Forward Voltage  $V_F$ 

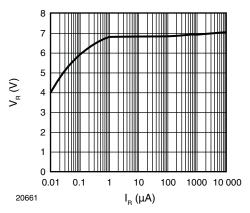


Fig. 4 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 

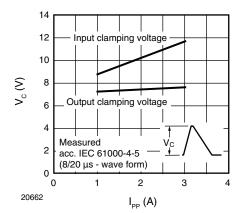


Fig. 5 - Typical Peak Clamping Voltage  $V_{C}$  vs. Peak Pulse Current  $I_{PP}$ 

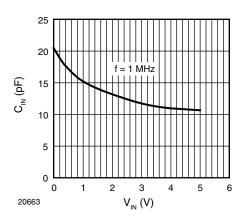


Fig. 6 - Typical Input Capacitance CIN vs. Input Voltage VIN

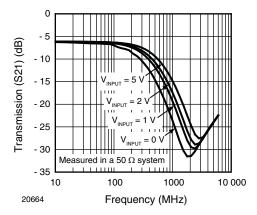
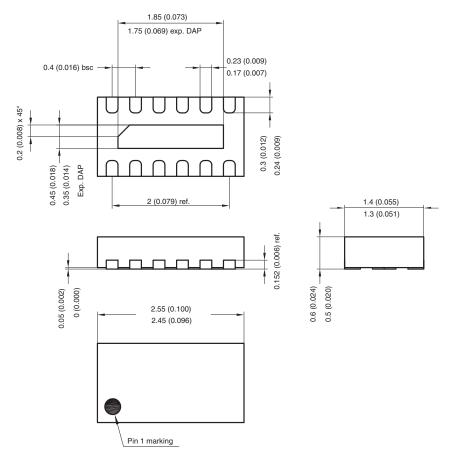


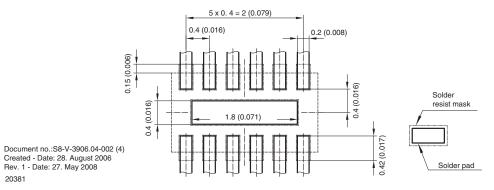
Fig. 7 - Typical Small Signal Transmission (S21) at  $\,$  Z $_{O}$  = 50  $\,$   $\Omega$ 

# Vishay Semiconductors

## PACKAGE DIMENSIONS in millimeters (inches): LLP2513-13L



#### Foot print recommendation:





## **Legal Disclaimer Notice**

Vishay

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Revision: 02-Oct-12 Document Number: 91000



OOO «ЛайфЭлектроникс" "LifeElectronics" LLC

ИНН 7805602321 КПП 780501001 P/C 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

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- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
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- Работу по проектам и поставку образцов.
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