

### FEATURES

#### High Speed

- 50 MHz Unity Gain Stable Operation
- 300 V/ms Slew Rate
- 120 ns Settling Time

Drives Unlimited Capacitive Loads

#### Excellent Video Performance

- 0.04% Differential Gain @ 4.4 MHz
- 0.198 Differential Phase @ 4.4 MHz

#### Good DC Performance

- 2 mV max Input Offset Voltage
- 15 mV/8C Input Offset Voltage Drift
- Available in Tape and Reel in Accordance with EIA-481A Standard

#### Low Power

- Only 10 mA Total Supply Current for Both Amplifiers
- $\pm 5$  V to  $\pm 15$  V Supplies

### PRODUCT DESCRIPTION

The AD827 is a dual version of Analog Devices' industry-standard AD847 op amp. Like the AD847, it provides high speed, low power performance at low cost. The AD827 achieves a 300 V/ $\mu$ s slew rate and 50 MHz unity-gain bandwidth while consuming only 100 mW when operating from  $\pm 5$  volt power supplies. Performance is specified for operation using  $\pm 5$  V to  $\pm 15$  V power supplies.

The AD827 offers an open-loop gain of 3,500 V/V into 500  $\Omega$  loads. It also features a low input voltage noise of  $15 \text{ nV}/\sqrt{\text{Hz}}$ , and a low input offset voltage of 2 mV maximum. Common-mode rejection ratio is a minimum of 80 dB. Power supply rejection ratio is maintained at better than 20 dB with input frequencies as high as 1 MHz, thus minimizing noise feedthrough from switching power supplies.

The AD827 is also ideal for use in demanding video applications, driving coaxial cables with less than 0.04% differential gain and 0.19° differential phase errors for 643 mV p-p into a 75  $\Omega$  reverse terminated cable.

The AD827 is also useful in multichannel, high speed data conversion systems where its fast (120 ns to 0.1%) settling time is of importance. In such applications, the AD827 serves as an input buffer for 8-bit to 10-bit A/D converters and as an output amplifier for high speed D/A converters.

### REV. C

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### CONNECTION DIAGRAMS

8-Lead Plastic (N) and Cerdip (Q) Packages



16-Lead Small Outline (R) Package



20-Lead LCC (E) Package



### APPLICATION HIGHLIGHTS

1. Performance is fully specified for operation using  $\pm 5$  V to  $\pm 15$  V supplies.
2. A 0.04% differential gain and 0.19° differential phase error at the 4.4 MHz color subcarrier frequency, together with its low cost, make it ideal for many video applications.
3. The AD827 can drive unlimited capacitive loads, while its 30 mA output current allows 50  $\Omega$  and 75  $\Omega$  reverse-terminated loads to be driven.
4. The AD827's 50 MHz unity-gain bandwidth makes it an ideal candidate for multistage active filters.
5. The AD827 is available in 8-lead plastic mini-DIP and cerdip, 20-lead LCC, and 16-lead SOIC packages. Chips and MIL-STD-883B processing are also available.

# AD827—SPECIFICATIONS (@ T<sub>A</sub> = +25°C, unless otherwise noted.)

Model	Conditions	V <sub>S</sub>	AD827J			AD827A/S			Unit
			Min	Typ	Max	Min	Typ	Max	
<b>DC PERFORMANCE</b>									
Input Offset Voltage <sup>1</sup>	T <sub>MIN</sub> to T <sub>MAX</sub>	±5 V	0.5	2		0.3	2	mV	
Offset Voltage Drift	T <sub>MIN</sub> to T <sub>MAX</sub>	±15 V						mV	
Input Bias Current	T <sub>MIN</sub> to T <sub>MAX</sub>	±5 V to ±15 V	15			15		μV/°C	
		±5 V to ±15 V	3.3	7		3.3	7	μA	
Input Offset Current	T <sub>MIN</sub> to T <sub>MAX</sub>	±5 V to ±15 V						μA	
			50	300		50	300	nA	
Offset Current Drift	T <sub>MIN</sub> to T <sub>MAX</sub>	±5 V to ±15 V						nA	
				400			400	nA	
Common-Mode Rejection Ratio	V <sub>CM</sub> = ±2.5 V	±5 V	78	95		80	95	dB	
		±15 V	78	95		80	95	dB	
		T <sub>MIN</sub> to T <sub>MAX</sub>	±5 V to ±15 V	75			75		dB
Power Supply Rejection Ratio	T <sub>MIN</sub> to T <sub>MAX</sub>	±5 V to ±15 V	75	86		75	86	dB	
			72			72		dB	
Open-Loop Gain	V <sub>O</sub> = ±2.5 V R <sub>LOAD</sub> = 500 Ω T <sub>MIN</sub> to T <sub>MAX</sub> R <sub>LOAD</sub> = 150 Ω V <sub>OUT</sub> = ±10 V R <sub>LOAD</sub> = 1 kΩ T <sub>MIN</sub> to T <sub>MAX</sub>	±5 V							
			2	3.5		2	3.5	V/mV	
			1			1		V/mV	
				1.6			1.6	V/mV	
			3	5.5		3	5.5	V/mV	
			1.5			1.5		V/mV	
<b>MATCHING CHARACTERISTICS</b>									
Input Offset Voltage Crosstalk	f = 5 MHz	±5 V		0.4			0.2	mV	
		±5 V		85			85	dB	
<b>DYNAMIC PERFORMANCE</b>									
Unity-Gain Bandwidth		±5 V		35			35	MHz	
		±15 V		50			50	MHz	
Full Power Bandwidth <sup>2</sup>	V <sub>O</sub> = 5 V p-p, R <sub>LOAD</sub> = 500 Ω	±5 V		12.7			12.7	MHz	
Slew Rate <sup>3</sup>	V <sub>O</sub> = 20 V p-p, R <sub>LOAD</sub> = 1 kΩ	±15 V		4.7			4.7	MHz	
Settling Time to 0.1%	A <sub>V</sub> = -1 -2.5 V to +2.5 V -5 V to +5 V	±5 V		65			65	ns	
		±15 V		120			120	ns	
Phase Margin	C <sub>LOAD</sub> = 10 pF R <sub>LOAD</sub> = 1 kΩ	±15 V		50			50	Degrees	
								%	
Differential Gain Error	f = 4.4 MHz	±15 V		0.04			0.04	%	
Differential Phase Error	f = 4.4 MHz	±15 V		0.19			0.19	Degrees	
Input Voltage Noise	f = 10 kHz	±15 V		15			15	nV/√Hz	
Input Current Noise	f = 10 kHz	±15 V		1.5			1.5	pA/√Hz	
Input Common-Mode Voltage Range		±5 V		+4.3			+4.3	V	
				-3.4			-3.4	V	
		±15 V		+14.3			+14.3	V	
				-13.4			-13.4	V	
Output Voltage Swing	R <sub>LOAD</sub> = 500 Ω R <sub>LOAD</sub> = 150 Ω R <sub>LOAD</sub> = 1 kΩ R <sub>LOAD</sub> = 500 Ω	±5 V	3.0	3.6		3.0	3.6	±V	
		±5 V	2.5	3.0		2.5	3.0	±V	
		±15 V	12	13.3		12	13.3	±V	
		±15 V	10	12.2		10	12.2	±V	
Short-Circuit Current Limit		±5 V to ±15 V		32			32	mA	
<b>INPUT CHARACTERISTICS</b>									
Input Resistance				300			300	kΩ	
Input Capacitance				1.5			1.5	pF	

Model	Conditions	V <sub>S</sub>	AD827J			AD827A/S			Unit
			Min	Typ	Max	Min	Typ	Max	
OUTPUT RESISTANCE	Open Loop		15			15			Ω
POWER SUPPLY									
Operating Range			±4.5		±18	±4.5		±18	V
Quiescent Current		±5 V		10	13		10	13	mA
	T <sub>MIN</sub> to T <sub>MAX</sub>				16			16.5/17.5	mA
		±15 V		10.5	13.5		10.5	13.5	mA
	T <sub>MIN</sub> to T <sub>MAX</sub>				16.5			17/18	mA
TRANSISTOR COUNT			92			92			

## NOTES

<sup>1</sup> Offset voltage for the AD827 is guaranteed after power is applied and the device is fully warmed up. All other specifications are measured using high speed test equipment, approximately 1 second after power is applied.

<sup>2</sup> Full Power Bandwidth = Slew Rate/2 π V<sub>PEAK</sub>.

<sup>3</sup> Gain = +1, rising edge.

All min and max specifications are guaranteed.

Specifications subject to change without notice.

## ABSOLUTE MAXIMUM RATINGS<sup>1</sup>

Supply Voltage ..... ±18 V

Internal Power Dissipation<sup>2</sup>

  Plastic (N) Package (Derate at 10 mW/°C) ..... 1.5 W

  Cerdip (Q) Package (Derate at 8.7 mW/°C) ..... 1.3 W

  Small Outline (R) Package (Derate at 10 mW/°C) ... 1.5 W

  LCC (E) Package (Derate at 6.7 mW/°C) ..... 1.0 W

Input Common-Mode Voltage ..... ±V<sub>S</sub>

Differential Input Voltage ..... 6 V

Output Short Circuit Duration<sup>3</sup> ..... Indefinite

Storage Temperature Range (N, R) ..... -65°C to +125°C

Storage Temperature Range (Q) ..... -65°C to +150°C

Operating Temperature Range

  AD827J ..... 0°C to 70°C

  AD827A ..... -40°C to +85°C

  AD827S ..... -55°C to +125°C

Lead Temperature Range

  (Soldering to 60 sec) ..... 300°C

## NOTES

<sup>1</sup> Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only, and functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum ratings for extended periods may affect device reliability.

<sup>2</sup> Maximum internal power dissipation is specified so that T<sub>J</sub> does not exceed 175°C at an ambient temperature of 25°C.

Thermal Characteristics:

MiniDIP: θ<sub>JA</sub> = 100°C/W; θ<sub>JC</sub> = 33°C/W

Cerdip: θ<sub>JA</sub> = 110°C/W; θ<sub>JC</sub> = 30°C/W

16-Lead Small Outline Package: θ<sub>JA</sub> = 100°C/W

20-Lead LCC: θ<sub>JA</sub> = 150°C/W; θ<sub>JC</sub> = 35°C/W

<sup>3</sup> Indefinite short circuit duration is only permissible as long as the absolute maximum power rating is not exceeded.

## ORDERING GUIDE

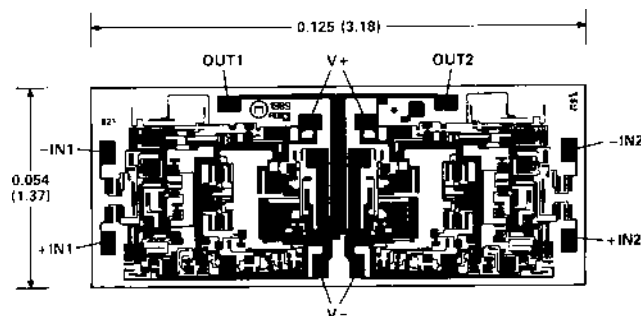
Model	Temperature Range	Package Description	Package Option
AD827JN	0°C to +70°C	8-Lead Plastic DIP	N-8
AD827JR	0°C to +70°C	16-Lead Plastic SO	R-16
AD827AQ	-40°C to +85°C	8-Lead Cerdip	Q-8
AD827SQ	-55°C to +125°C	8-Lead Cerdip	Q-8
AD827SQ/883B	-55°C to +125°C	8-Lead Cerdip	Q-8
5962-9211701MPA	-55°C to +125°C	8-Lead Cerdip	Q-8
AD827SE/883B	-55°C to +125°C	20-Lead LCC	E-20A
5962-9211701M2A	-55°C to +125°C	20-Lead LCC	E-20A
AD827JR-REEL	0°C to +70°C	Tape & Reel	
AD827JChips	0°C to +70°C	Die	
AD827SChips	-55°C to +125°C	Die	

## METALLIZATION PHOTOGRAPH

Contact factory for latest dimensions.

Dimensions shown in inches and (mm).

Substrate is connected to V+.



# AD827—Typical Performance Characteristics (@ +25°C & ±15 V, unless otherwise noted)

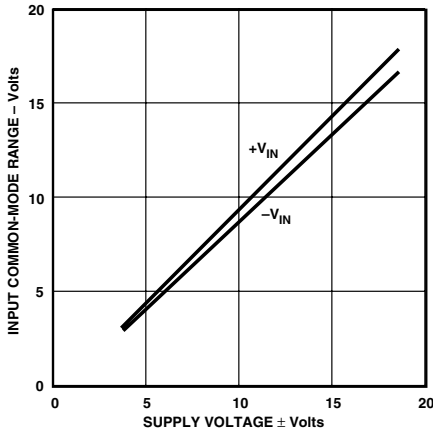


Figure 1. Input Common-Mode Range vs. Supply Voltage



Figure 2. Output Voltage Swing vs. Supply Voltage



Figure 3. Output Voltage Swing vs. Load Resistance

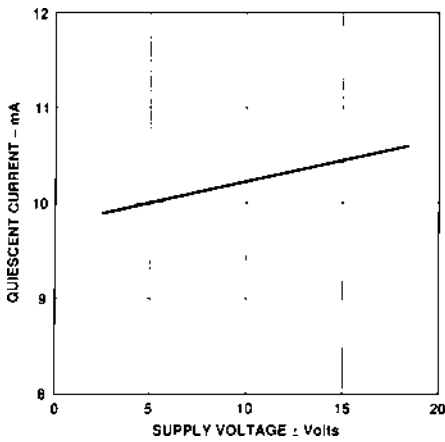


Figure 4. Quiescent Current vs. Supply Voltage

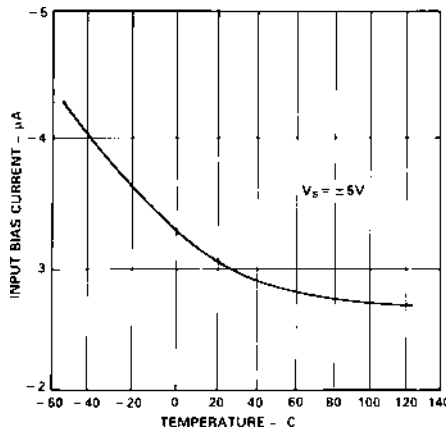


Figure 5. Input Bias Current vs. Temperature



Figure 6. Closed-Loop Output Impedance vs. Frequency, Gain = +1

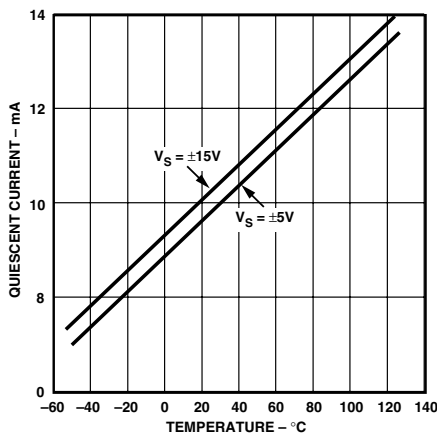


Figure 7. Quiescent Current vs. Temperature

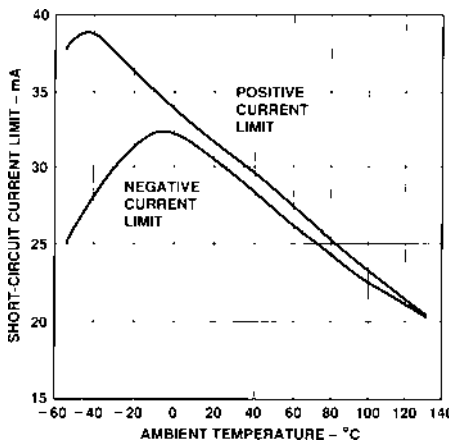


Figure 8. Short-Circuit Current Limit vs. Temperature

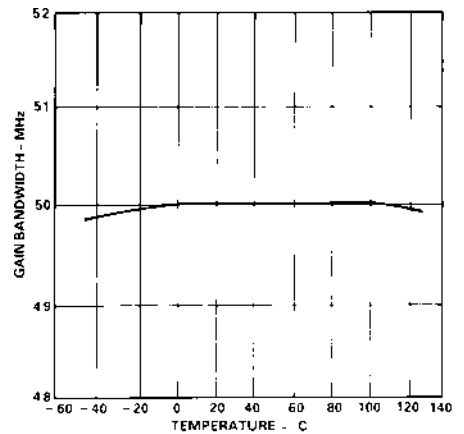


Figure 9. Gain Bandwidth vs. Temperature



Figure 10. Open-Loop Gain and Phase Margin vs. Frequency



Figure 11. Open-Loop Gain vs. Load Resistance



Figure 12. Power Supply Rejection Ratio vs. Frequency



Figure 13. Common-Mode Rejection Ratio vs. Frequency



Figure 14. Large Signal Frequency Response



Figure 15. Output Swing and Error vs. Settling Time

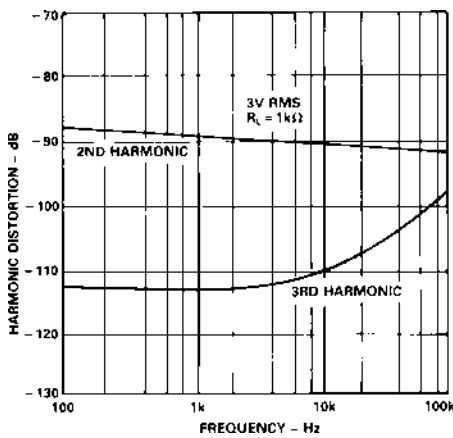


Figure 16. Harmonic Distortion vs. Frequency

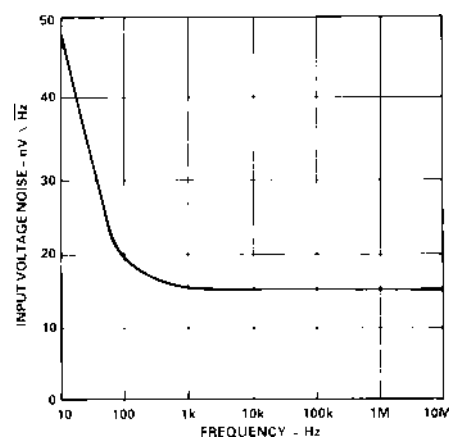


Figure 17. Input Voltage Noise Spectral Density

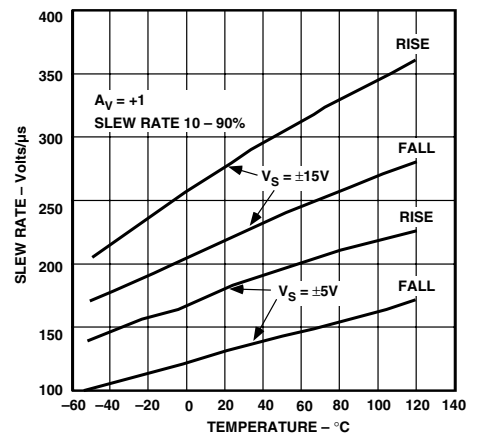


Figure 18. Slew Rate vs. Temperature

# AD827



Figure 19. Crosstalk vs. Frequency



$R_L = 500\Omega$  FOR  $\pm V_S = 5V$ ,  $1k\Omega$  FOR  $\pm V_S = 15V$   
USE GROUND PLANE  
PINOUT SHOWN IS FOR MINIDIP PACKAGE

Figure 20. Crosstalk Test Circuit

## INPUT PROTECTION PRECAUTIONS

An input resistor (resistor  $R_{IN}$  of Figure 21a) is recommended in circuits where the input common-mode voltage to the AD827 may exceed (on a transient basis) the positive supply voltage. This resistor provides protection for the input transistors by limiting the maximum current that can be forced into their bases.

For high performance circuits, it is recommended that a second resistor ( $R_B$  in Figures 21a and 22a) be used to reduce bias-current errors by matching the impedance at each input. This resistor reduces the error caused by offset voltages by more than an order of magnitude.



Figure 21a. Follower Connection



Figure 21b. Follower Large Signal Pulse Response



Figure 21c. Follower Small Signal Pulse Response

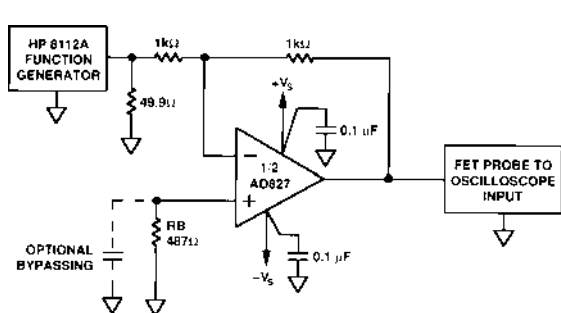


Figure 22a. Inverter Connection



Figure 22b. Inverter Large Signal Pulse Response



Figure 22c. Inverter Small Signal Pulse Response

## VIDEO LINE DRIVER

The AD827 functions very well as a low cost, high speed line driver for either terminated or unterminated cables. Figure 23 shows the AD827 driving a doubly terminated cable in a follower configuration.



Figure 23. A Video Line Driver

The termination resistor,  $R_T$ , (when equal to the cable's characteristic impedance) minimizes reflections from the far end of the cable. While operating from  $\pm 5$  V supplies, the AD827 maintains a typical slew rate of 200 V/ $\mu$ s, which means it can drive a  $\pm 1$  V, 30 MHz signal into a terminated cable.

Table I. Video Line Driver Performance Summary

$V_{IN}^*$	$V_{SUPPLY}$	$C_C$	-3 dB $B_W$	Over-shoot
0 dB or $\pm 500$ mV Step	$\pm 15$	20 pF	23 MHz	4%
0 dB or $\pm 500$ mV Step	$\pm 15$	15 pF	21 MHz	0%
0 dB or $\pm 500$ mV Step	$\pm 15$	0 pF	13 MHz	0%
0 dB or $\pm 500$ mV Step	$\pm 5$	20 pF	18 MHz	2%
0 dB or $\pm 500$ mV Step	$\pm 5$	15 pF	16 MHz	0%
0 dB or $\pm 500$ mV Step	$\pm 5$	0 pF	11 MHz	0%

\*-3 dB bandwidth numbers are for the 0 dBm signal input. Overshoot numbers are the percent overshoot of the 1 V step input.

A back-termination resistor ( $R_{BT}$ , also equal to the characteristic impedance of the cable) may be placed between the AD827 output and the cable input, in order to damp any reflected signals caused by a mismatch between  $R_T$  and the cable's characteristic impedance. This will result in a flatter frequency response, although this requires that the op amp supply  $\pm 2$  V to the output in order to achieve a  $\pm 1$  V swing at resistor  $R_T$ .

## A HIGH SPEED THREE OP AMP INSTRUMENTATION AMPLIFIER CIRCUIT

The instrumentation amplifier circuit shown in Figure 24 can provide a range of gains. Table II details performance.



Figure 24. A High Bandwidth Three Op Amp Instrumentation Amplifier

Table II. Performance Specifications for the Three Op Amp Instrumentation Amplifier

Gain	$R_G$	Small Signal Bandwidth @ 1 V p-p Output
1	Open	16.1 MHz
2	2 k	14.7 MHz
10	226 $\Omega$	4.9 MHz
100	20 $\Omega$	660 kHz

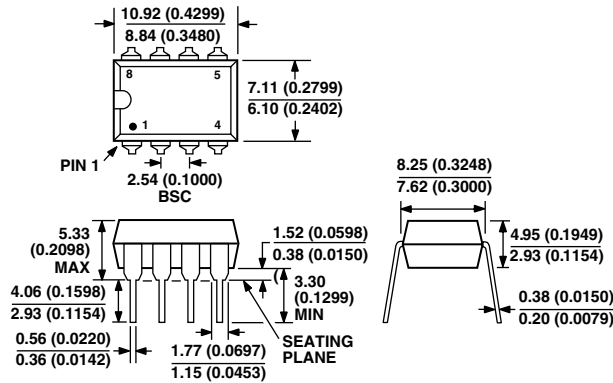




OUTLINE DIMENSIONS

8-Lead Plastic Dual-in-Line Package [PDIP]  
(N-8)

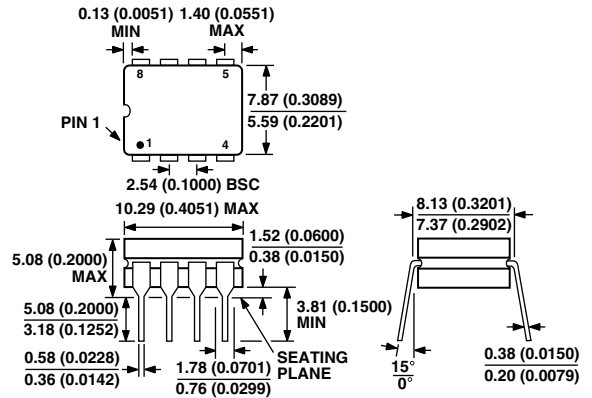
Dimensions shown in millimeters and (inches)



CONTROLLING DIMENSIONS ARE IN MILLIMETERS; INCH DIMENSIONS (IN PARENTHESES) ARE ROUNDED-OFF MILLIMETER EQUIVALENTS FOR REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN

8-Lead Ceramic DIP-Glass Hermetic Seal Package [CERDIP]  
(Q-8)

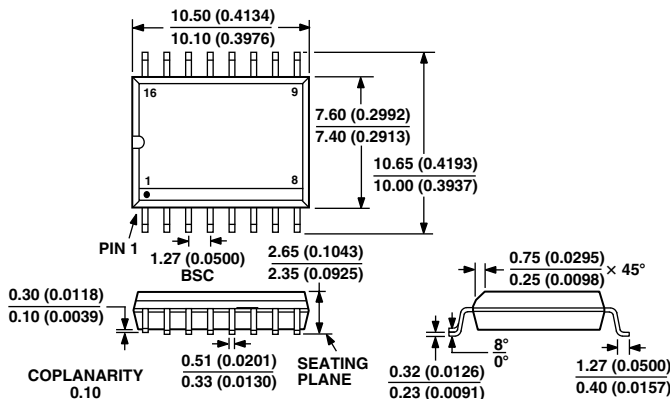
Dimensions shown in millimeters and (inches)



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16-Lead Standard Small Outline Package [SOIC]  
Wide Body  
(R-16)

Dimensions shown in millimeters and (inches)

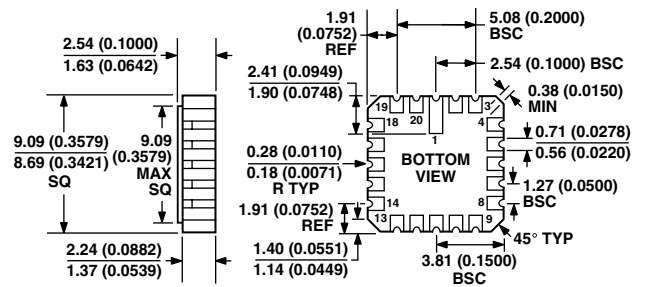


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COMPLIANT TO JEDEC STANDARDS MS-013AA

20-Terminal Ceramic Leadless Chip Carrier [LCC]  
(E-20A)

Dimensions shown in millimeters and (inches)



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# AD827

## Revision History

Location	Page
8/02—Data Sheet changed from REV. B to REV. C.	
Updated Outline Dimensions .....	9





Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

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- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
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