74AHC2G125; 74AHCT2G125

Dual buffer/line driver; 3-state Rev. 4 — 2 January 2019

1. General description

The 74AHC2G125 and 74AHCT2G125 are high-speed Si-gate CMOS devices. They provide a dual non-inverting buffer/line driver with 3-state output. The 3-state output is controlled by the output enable input ($n\overline{OE}$). A HIGH at $n\overline{OE}$ causes the output to assume a high-impedance OFF-state.

The AHC device has CMOS input switching levels and supply voltage range 2 V to 5.5 V.

The AHCT device has TTL input switching levels and supply voltage range 4.5 V to 5.5 V.

2. Features and benefits

- Symmetrical output impedance
- High noise immunity
- Low power dissipation
- Balanced propagation delays
- Multiple package options
- ESD protection:
 - HBM JESD22-A114E: exceeds 2000 V
 - MM JESD22-A115-A: exceeds 200 V
 - CDM JESD22-C101C: exceeds 1000 V
 - Specified from -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AHC2G125DP	-40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package;	SOT505-2
74AHCT2G125DP			8 leads; body width 3 mm; lead length 0.5 mm	
74AHC2G125DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package;	SOT765-1
74AHCT2G125DC			8 leads; body width 2.3 mm	

4. Marking

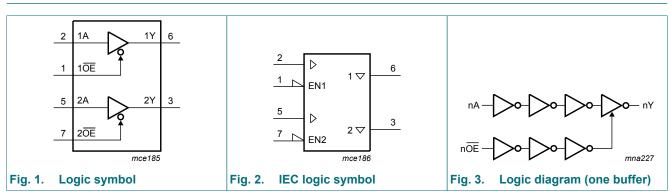
Table 2. Marking codes					
Type number	Marking[1]				
74AHC2G125DP	A25				
74AHCT2G125DP	C25				
74AHC2G125DC	A25				
74AHCT2G125DC	C25				

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

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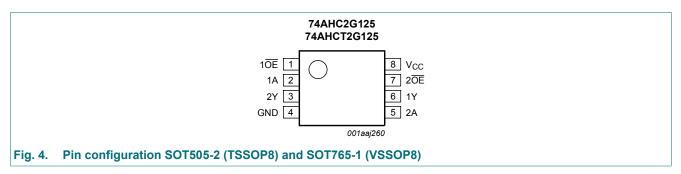
Dual buffer/line driver; 3-state

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

able 3. Pin description					
Symbol	Pin	Description			
1 <u>0E</u> , 2 <u>0E</u>	1, 7	output enable input (active LOW)			
1A, 2A	2, 5	data input			
GND	4	ground (0 V)			
1Y, 2Y	6, 3	data output			
V _{CC}	8	supply voltage			

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

	Input	Output
nŌE	nA	nY
L	L	L
L	Н	Н
Н	X	Z

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V [1]	-20	-	mA
I _{OK}	output clamping current	$V_{\rm O} < -0.5 \text{ V or } V_{\rm O} > V_{\rm CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I _O	output current	$-0.5 V < V_O < V_{CC} + 0.5 V$	-	±25	mA
I _{CC}	supply current		-	75	mA
I _{GND}	ground current		-75	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For TSSOP8 package: above 55 °C the value of P_{tot} derates linearly with 2.5 mW/K.

For VSSOP8 package: above 110 °C the value of Ptot derates linearly with 8 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	74	74AHC2G125			AHCT2G	125	Unit
			Min	Тур	Мах	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	V_{CC} = 3.3 V ± 0.3 V	-	-	100	-	-	-	ns/V
	fall rate	V_{CC} = 5.0 V ± 0.5 V	-	-	20	-	-	20	ns/V

10. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	ameter Conditions		25 °C			-40 °C to +85 °C		-40 °C to +125 °C	
			Min	Тур	Мах	Min	Max	Min	Max	
74AHC2	G125									
V _{IH}	HIGH-level	V _{CC} = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V _{CC} = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V _{IL}	LOW-level	V _{CC} = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V _{CC} = 5.5 V	-	-	1.65	-	1.65	-	1.65	V

Dual buffer/line driver; 3-state

Symbol	Parameter	Conditions		25 °C			°C to 5 °C	-	°C to 5 °C	Unit
			Min	Тур	Мах	Min	Мах	Min	Max	-
V _{OH}	HIGH-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I _O = -8.0 mA; V _{CC} = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I _O = 8.0 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
I _{OZ}	OFF-state output current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 5.5 V$	-	-	0.25	-	2.5	-	10	μA
l	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$ V_{I} = V_{CC} \text{ or } GND; I_{O} = 0 \text{ A}; $ $ V_{CC} = 5.5 \text{ V} $		-	1.0	-	10	-	40	μA
CI	input capacitance		-	1.5	10	-	10	-	10	pF
74AHCT	2G125									
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	-	-	2.0	-	2.0	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V _{OH}	HIGH-level	V_{I} = V_{IH} or V_{IL} ; V_{CC} = 4.5 V								
	output voltage	I _O = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -8.0 mA	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	V_{I} = V_{IH} or V_{IL} ; V_{CC} = 4.5 V								
	output voltage	I _O = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
I _{OZ}	OFF-state output current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 5.5 V$	-	-	0.25	-	2.5	-	10	μA
lı	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$V_{I} = V_{CC}$ or GND; $I_{O} = 0$ A; $V_{CC} = 5.5$ V	-	-	1.0	-	10	-	40	μA
ΔI _{CC}	additional supply current	per input pin; V _I = 3.4 V; other inputs at V _{CC} or GND; $I_O = 0 A$; V _{CC} = 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
CI	input capacitance		-	1.5	10	-	10	-	10	pF

11. Dynamic characteristics

Table 8. Dynamic characteristics

GND = 0 V; for test circuit see Fig. 7.

Symbol	Parameter	Conditions		25 °C			°C to 5 °C		°C to 5 °C	Unit
			Min	Typ [1]	Мах	Min	Мах	Min	Max	
74AHC2	G125									
t _{pd}	propagation	nA to nY; see Fig. 5 [2]								
	delay	V_{CC} = 3.0 V to 3.6 V; C _L = 15 pF	-	4.7	8.0	1.0	9.5	1.0	11.5	ns
		V_{CC} = 3.0 V to 3.6 V; C _L = 50 pF	-	6.6	11.5	1.0	13.0	1.0	14.5	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 15 pF	-	3.4	5.5	1.0	6.5	1.0	7.0	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 50 pF	-	4.8	7.5	1.0	8.5	1.0	9.5	ns
t _{en}	enable time	nOE to nY; see Fig. 6 [2]								
		V_{CC} = 3.0 V to 3.6 V; C _L = 15 pF	-	5.0	8.0	1.0	9.5	1.0	11.5	ns
		V_{CC} = 3.0 V to 3.6 V; C _L = 50 pF	-	6.9	11.5	1.0	13.0	1.0	14.5	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 15 pF	-	3.6	5.1	1.0	6.0	1.0	6.5	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 50 pF	-	4.9	7.5	1.0	8.5	1.0	9.5	ns
t _{dis}	disable time	nOE to nY; see Fig. 6 [2]								
		V_{CC} = 3.0 V to 3.6 V; C _L = 15 pF	-	6.0	9.7	1.0	11.5	1.0	12.5	ns
		V_{CC} = 3.0 V to 3.6 V; C _L = 50 pF	-	8.3	13.2	1.0	15.0	1.0	16.5	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 15 pF	-	4.1	6.8	1.0	8.0	1.0	8.5	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 50 pF	-	5.7	8.8	1.0	10.0	1.0	11.0	ns
C _{PD}	power dissipation capacitance	per buffer; C _L = 50 pF; f _i = 1 MHz; [3] V _I = GND to V _{CC}	-	9	-	-	-	-	-	pF
74AHCT	2G125	1		II						-
t _{pd}	propagation	nA to nY; see Fig. 5 [2]								
	delay	V_{CC} = 4.5 V to 5.5 V; C _L = 15 pF	-	3.4	5.5	1.0	6.5	1.0	6.5	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 50 pF	-	4.8	7.5	1.0	8.5	1.0	8.5	ns
t _{en}	enable time	nOE to nY; see Fig. 6 [2]								
		V_{CC} = 4.5 V to 5.5 V; C _L = 15 pF	-	3.9	5.1	1.0	6.0	1.0	6.0	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 50 pF	-	5.1	7.5	1.0	8.5	1.0	8.5	ns
t _{dis}	disable time	nOE to nY; see Fig. 6 [2]								
		V_{CC} = 4.5 V to 5.5 V; C _L = 15 pF	-	4.5	6.8	1.0	8.0	1.0	8.0	ns
		V_{CC} = 4.5 V to 5.5 V; C _L = 50 pF	-	6.1	8.8	1.0	10.0	1.0	10.0	ns
C _{PD}	power dissipation capacitance	per buffer; C_L = 50 pF; f_i = 1 MHz; [3] V_I = GND to V_{CC}	-	11	-	-	-	-	-	pF

Typical values are measured at T_{amb} = 25 °C and V_{CC} = 3.3 V and 5.0 V respectively. [1] [2] [3]

 t_{pd} is the same as t_{PLH} and t_{PHL} ; t_{en} is the same as t_{PZL} and t_{PZH} ; t_{dis} is the same as t_{PLZ} and t_{PHZ} . C_{PD} is used to determine the dynamic power dissipation P_D (µW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

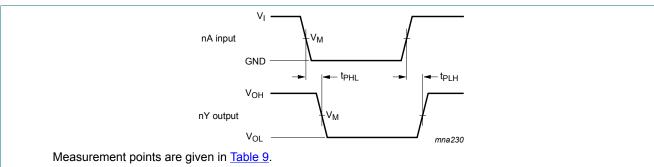
 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in Volts.

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Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 5. Input (nA) to output (nY) propagation delays

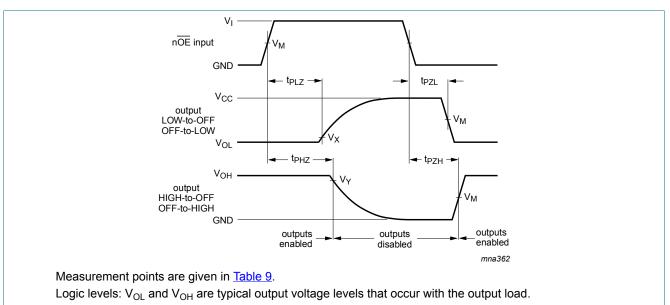


Fig. 6. Enable and disable times

Table 9. Measurement points

Туре	Input	Output			
	V _M	V _M	V _X	V _Y	
74AHC2G125	0.5V _{CC}	0.5V _{CC}	V _{OL} + 0.3 V	V _{OH} - 0.3 V	
74AHCT2G125	1.5 V	0.5V _{CC}	V _{OL} + 0.3 V	V _{OH} - 0.3 V	

74AHC2G125; 74AHCT2G125

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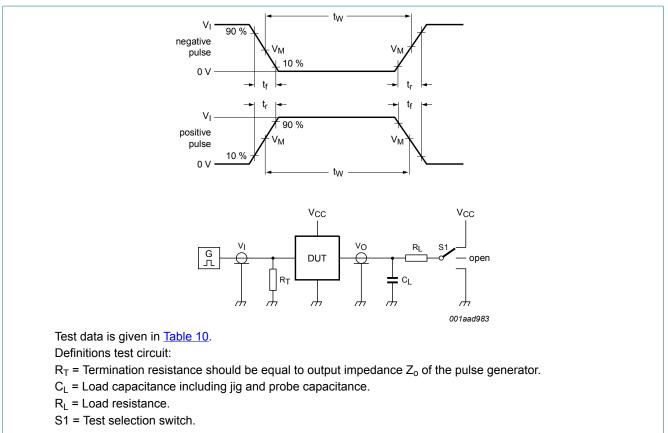


Fig. 7. Test circuit for measuring switching times

Table 10. Test data

Туре	Input I		Load		S1 position		
	VI	t _r , t _f	CL	RL	t _{PHL} , t _{PLH}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
74AHC2G125	V _{CC}	≤ 3 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}
74AHCT2G125	3 V	≤ 3 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}

12. Package outline

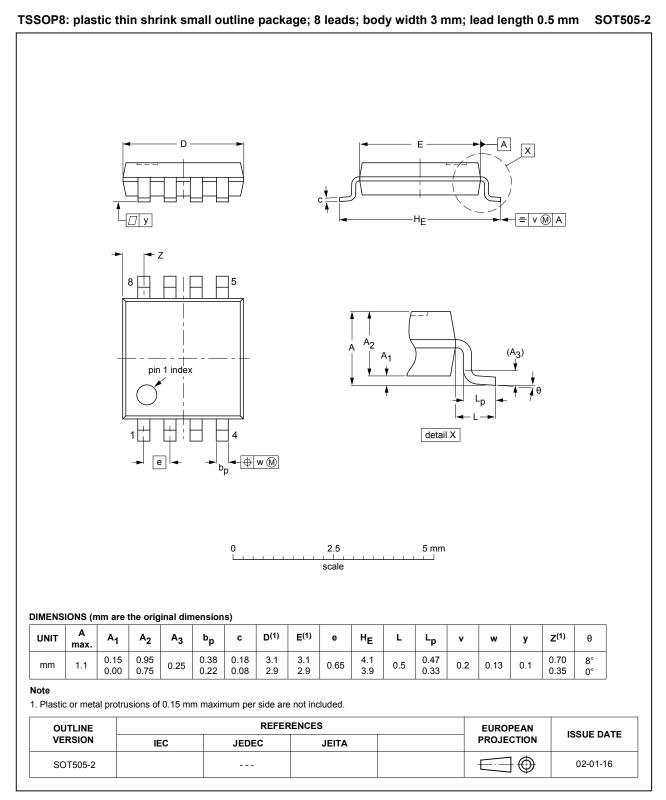
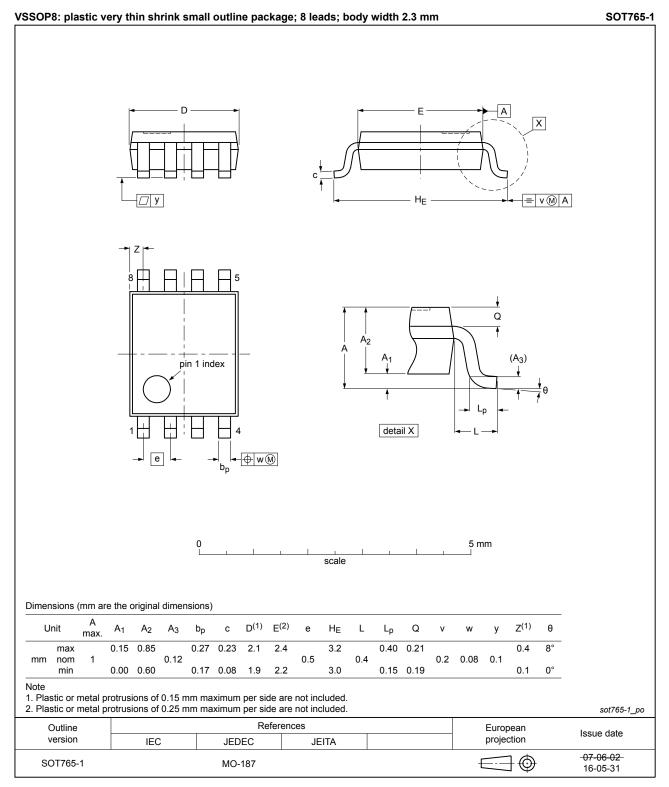


Fig. 8. Package outline SOT505-2 (TSSOP8)

74AHC2G125; 74AHCT2G125

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13. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AHC_AHCT2G125 v.4	20190102	Product data sheet	-	74AHC_AHCT2G125 v.3	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type numbers 74AHC2G125GD and 74AHCT2G125GD (SOT996-2) removed. Package outline drawing <u>SOT765-1</u> (VSSOP8) updated. 				
74AHC_AHCT2G125 v.3	20130506	Product data sheet	-	74AHC_AHCT2G125 v.2	
Modifications:	For type number 74AHC2G125GD and 74AHCT2G125GD XSON8U has changed to XSON8.				
74AHC_AHCT2G125 v.2	20081222	Product data sheet	-	74AHC_AHCT2G125 v.1	
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. Added type number 74AHC2G125GD and 74AHCT2G125GD (XSON8U package). 				
74AHC_AHCT2G125 v.1	20040113	Product specification	-	-	

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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ООО "ЛайфЭлектроникс"

ИНН 7805602321 КПП 780501001 Р/С 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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