MOSFETs Silicon N-Channel MOS (DTMOSIV)

TK6Q65W

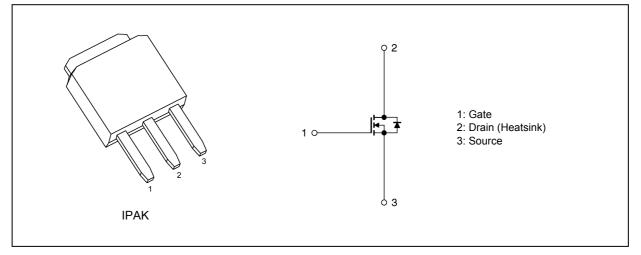
1. Applications

Switching Voltage Regulators

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 0.89 \Omega(typ.)$ by using Super Junction Structure : DTMOS
- (2) Easy to control Gate switching
- (3) Enhancement mode: V_{th} = 2.5 to 3.5 V(V_{DS} = 10 V, I_D = 0.18 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	650	V
Gate-source voltage		V _{GSS}	±30	
Drain current (DC)	(Note 1)	I _D	5.8	Α
Drain current (pulsed)	(Note 1)	I _{DP}	23.2	1
Power dissipation (7	Г _с = 25 °С)	PD	60	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	82	mJ
Avalanche current		I _{AR}	1.5	Α
Reverse drain current (DC)	(Note 1)	I _{DR}	5.8	1
Reverse drain current (pulsed)	(Note 1)	I _{DRP}	23.2	7
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2014-09

5. Thermal Characteristics

Characteristics		Max	Unit
Channel-to-case thermal resistance		2.09	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	125	

Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2: V_{DD} = 90 V, T_{ch} = 25 °C (initial), L = 65.2 mH, R_G = 25 Ω , I_{AR} = 1.5 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

6.1. Static Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V	_	_	±1	μA
Drain cut-off current	I _{DSS}	V _{DS} = 650 V, V _{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	650	_	—	V
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 0.18 mA	2.5	_	3.5	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 2.9 A	_	0.89	1.05	Ω

6.2. Dynamic Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 300 V, V _{GS} = 0 V, f = 1 MHz	—	390	_	pF
Reverse transfer capacitance	C _{rss}]	_	1.7	_	
Output capacitance	C _{oss}		_	12	_	
Effective output capacitance	C _{o(er)}	V_{DS} = 0 to 400 V, V_{GS} = 0 V	_	17.5	_	
Gate resistance	rg	V _{DS} = OPEN , f = 1 MHz	_	7.5	_	Ω
Switching time (rise time)	tr	See Figure 6.2.1	—	14	_	ns
Switching time (turn-on time)	t _{on}		_	34	_	
Switching time (fall time)	t _f	1	_	4	_	
Switching time (turn-off time)	t _{off}	1		45		
MOSFET dv/dt ruggedness	dv/dt	V _{DD} = 0 to 400 V, I _D = 2.9 A	50		_	V/ns

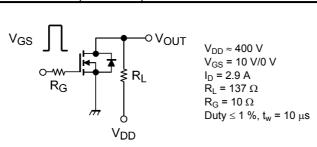


Fig. 6.2.1 Switching Time Test Circuit

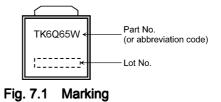
6.3. Gate Charge Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS}$ = 10 V, I _D = 5.8 A		11	_	nC
Gate-source charge 1	Q _{gs1}		_	3	_	
Gate-drain charge	Q _{gd}			5	_	

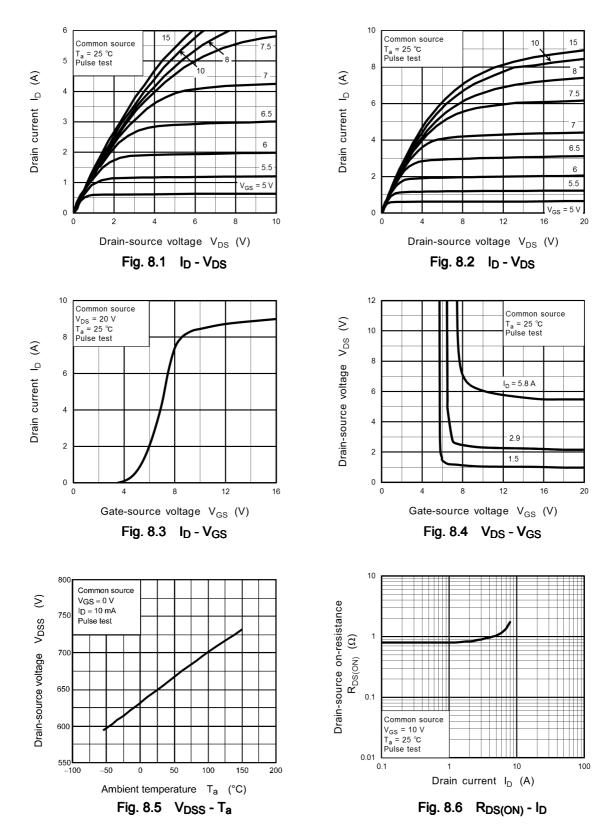
6.4. Source-Drain Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	V _{DSF}	I _{DR} = 5.8 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 2.9 A, V _{GS} = 0 V	_	260	_	ns
Reverse recovery charge	Q _{rr}	-dI _{DR} /dt = 100 A/μs	_	1.6	_	μC
Peak reverse recovery current	I _{rr}			13	_	А
Diode dv/dt ruggedness	dv/dt	I_{DR} = 2.9 A, V_{GS} = 0 V, V_{DD} = 400 V	15			V/ns

7. Marking



8. Characteristics Curves (Note)



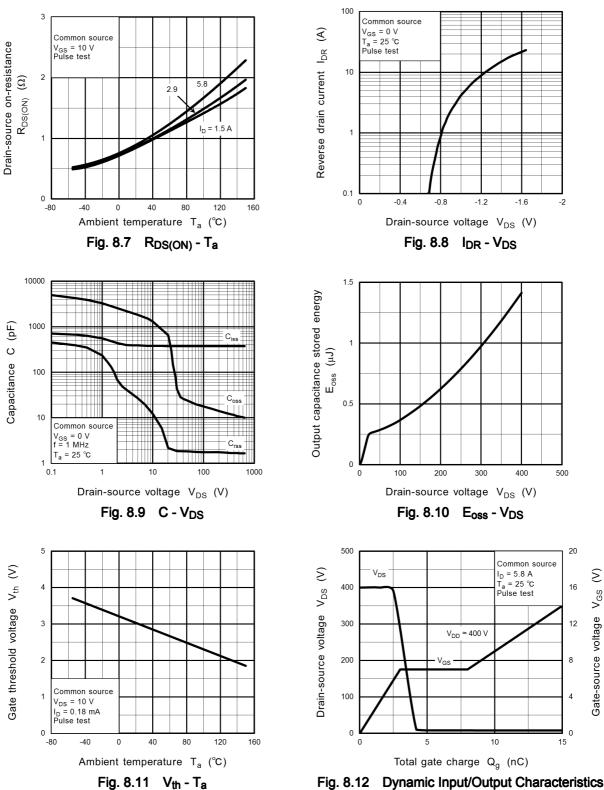
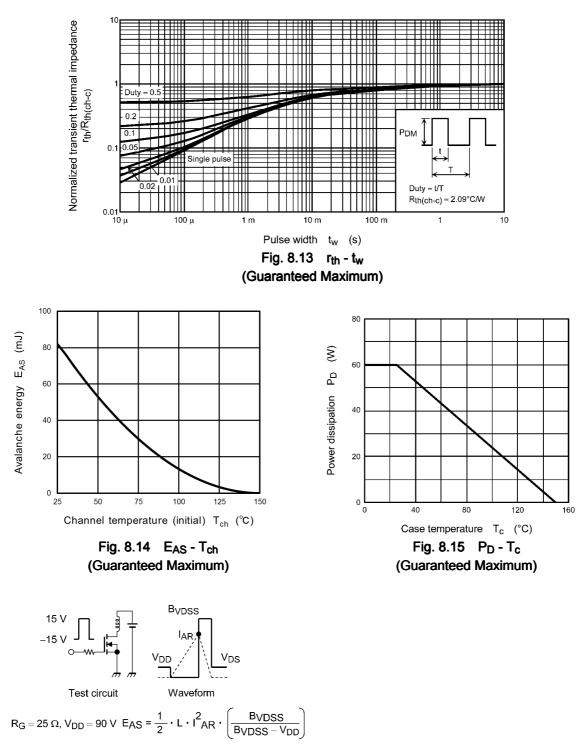
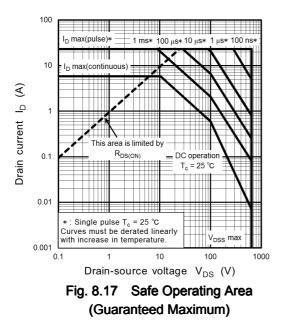


Fig. 8.12 Dynamic Input/Output Characteristics



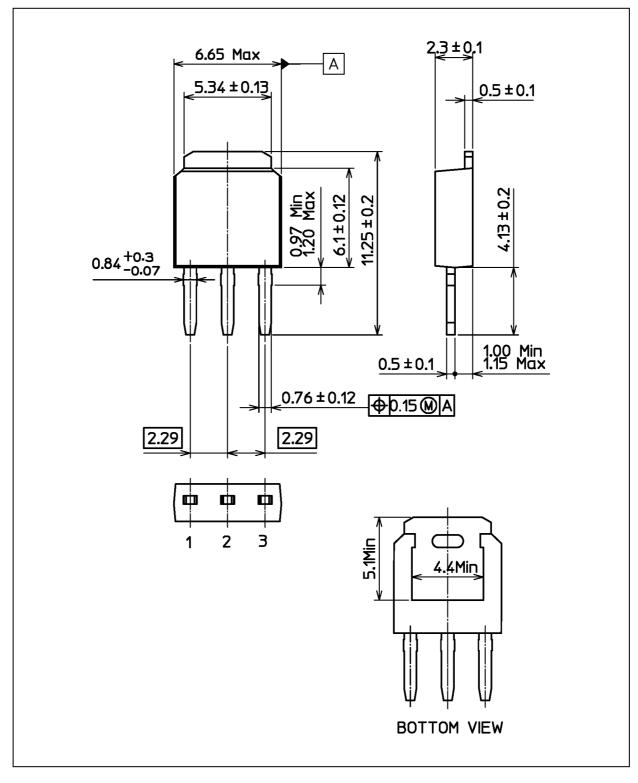




Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

Unit: mm



Weight: 0.337 g (typ.)

Package Name(s)

TOSHIBA: 2-7L1A

Nickname: IPAK

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