Specification Sheet

<Chip Monolithic Ceramic Capacitor, Low ESL Series > Murata Global P/N : LLK215C70G225ME01L (Low ESL 0805-14T,T=0.5,X7S,2.2uF,4V)

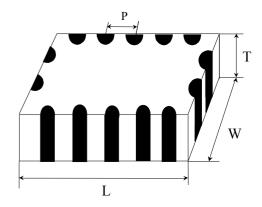
Corresponding products for RoHS directive

Dimensions(mm)

L	W	T	p	
2.0+/-0.1	1.25+/-0.1	0.5+0.05/-0.1	0.4+/-0.05	

Rated Value

-								
	TC code	TC	DC Rated Voltage (V)	CAP.		CAP.TOL		
	C7	X7S	4	2.2	μF	+/-20%		



Packaging

Specification	Packaging unit [pcs/reel]			
φ180 Plastic Tape Carrier Packaging	4,000			

Specification

Please refer to next page.

△Note

- (1) This specification sheet is applied for CHIP MONOLITHIC CERAMIC CAPACITOR used for General Electronics equipment for your design.
- (2) Please contact our sales representative or product engineers before using our products for the application listed below.
 - ① Aircraft equipment ② Aerospace equipment ③ Undersea equipment ④ Medical equipment
 - ⑤ Transportation equipment ⑥ Traffic signal equipment ⑦ Disaster prevention / crime prevention equipment
 - Application of similar complexity and/or requirements to the applications listed in the above.
- (3) Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used. Please confirm the solderability of Tin plating termination chip before use.
- (4) Use of Sn-Zn based solder will deteriorate reliability of MLCC. Please contact murata factory for the use of Sn-Zn based solder in advance.
- (5) This specification sheet has only typical specification because there is no space for detailed specifications. Therefore, please approve our product specification or transact the approval sheet for product specification before your ordering. Especially, please read rating and CAUTION (for storage, operating, rating, soldering, mounting, and handling) in them to prevent smoking and /or burning, etc.
- (6) You are able to read a detailed specification of our some products listed in this specification sheet in the web-site of MURATA (http://www.murata.com/) before to require our product specification or to transact the approval sheet.
- (7) Product specifications are subject to change without advance notice.

Please check with our sales representatives or product engineers before ordering.

If there are any questions, please contact our sales representatives or product engineers.

(8) Do not use under the condition that causes condensation.

Use dampproof countermeasure if using under the condition that causes condensation

Business Development Support Sec. I Planning & Market Promotion Department FUKUI MURATA MFG. CO., LTD.

		SPECIFICATIONS AND T	FEST METHODS					
No	Item	Specification	Test Method					
1	Operating Temperature Range	R7 / C7 : -55°C to +125°C						
2	Rated Voltage	See the previous pages.	The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage,V ^{P-P} or V ^{O-P} , whichever is larger, shall be maintained within the rated voltage range.					
3	Appearance	No defects or abnormalities.	Visual inspection.					
4	Dimensions	Within the specified dimension.	Using calipers.					
5	Dielectric Strength	No defects or abnormalities.	No failure shall be observed when 250% of the rated voltage is applied between the terminations for 1 to 5 seconds, provided the charge/discharge current is less than 50mA.					
6	Insulation Resistance	50Ω · F min. The insulation resistance shall be measured with a DC vo not exceeding the rated voltage at 25°C and 75%RH max within 1 minutes of charging.						
	Capacitance	Within the specified tolerance.	The capacitance/D.F. shall be measured at 25°C at the frequency and voltage shown in the table.					
8	Dissipation Factor (D.F.)	0.120max.	Capacitance Frequency Voltage					
	(D.1.)		C≦10μF (10V min.) 1± 0.1kHz 1.0 ± 0.2 Vms					
			C≦10μF (6.3V max.) 1± 0.1kHz 0.5± 0.1 Vms					
			C > 10μF 120± 24Hz 0.5± 0.1 Vrms					
	Temperature Characteristics	(°C) Temp. R7 -55 to +125 25°C Within ±15% C7 -55 to +125 25°C Within ±22%	each specified temperature stage. Step Temperature(°C) 1					
10	Adhesive Strength of Termination	No removal of the terminations or other defects shall occur.	Solder the capacitor to the test jig (glass epoxy board) using a eutectic solder. Then apply 5N force in parallel with the test jig for 10±1 sec. The soldering shall be done either with an iron or using the reflow method and shall be conducted with care so that the soldering is uniform and free of defects such as heat shock.					
11	Vibration	Appearance No defects or abnormalities. Capacitance Within the specified tolerance. D.F. 0.120max.	Solder the capacitor to the test jig (glass epoxy board) in the same manner and under the same conditions as (10). The capacitor shall be subjected to a simple harmonic motion having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55Hz. The frequency range, from 10 to 55Hz and return to 10Hz, shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).					
12	Solderability of Termination	75% of the terminations is to be soldered evenly and continuously.	d Immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Preheat at 80 to 120°C for 10 to 30 seconds. After preheating, immerse in eutectic solder solution for 2 ± 0.5 seconds at 230 ± 5 °C,or Sn-3.0Ag-0.5Cu solder solution for 2 ± 0.5 seconds at 245 ± 5 °C.					

	SPECIFICATIONS AND TEST METHODS								
No	Item		Specification	Test Method					
13	B Temperature Sudden Change	Appearance Capacitance Change D.F.	No marking defects. Within ±12.5% 0.120max.	Fix the capacitor to the supporting jig in the same manner under the same conditions as (10). Perform the five cycl according to the four heat treatments listed in the following table. Let sit for 24±2 hours at room temperature, then				s g	
		I.R.	50Ω·F min.	$\dashv \Gamma$	Step	1	2	3	4
		Dielectric Strength No failure			Temp.(°C)	Min. Operating Temp.+0/-3	Room Temp.	Max. Operating Temp.+3/-0	Room Temp.
					Time(min.)	30±3	2 to 3	30±3	2 to 3
				 Initial measurement Perform a heat treatment at 150+0/-10 °C for one houg and then let sit for 24±2 hours at room temperature. Perform the initial measurement. 					
14	High	Appearance	No marking defects.	Apply the rated voltage at 40±2°C and 90 to 95% humidit					ty for
	Temperature High Humidity (Steady State)	Capacitance Change	Within ±12.5%	500±12 hours. The charge/discharge current is less than					
		D.F.	0.2 max.		 Initial measurement Perform a heat treatment at 150+0/-10 °C for one ho 				
		I.R.	12.5Ω ·F min.	then let sit for 24±2 hours at room temperature.Perform the initial measurement.					
				Perform a		after test at treatment at 1 r 24±2 hours at			
15	Durability	Appearance	No marking defects.			he rated voltage			
		Capacitance Change	Within ±12.5%	maximum operating temperature ±3°C. Let sit for 24= room temperature, then measure. The charge/discharge current is less than 50mA.					2 hours at
		D.F.	0.2 max.	ın	ie charge/disc	narge current is	iess than	50MA.	
		I.R.	25Ω · F min.				for one houg and rature.Perform the		
						after test at treatment at 1 r 24±2 hours at			



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ИНН 7805602321 КПП 780501001 P/C 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

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- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
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- Регистрацию проекта у производителя компонентов.
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- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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