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February 2015



MMBTA28 / PZTA28 NPN Darlington Transistor

Description

This device is designed for applications requiring extremely high current gain at collector currents to 500 mA. Sourced from process 03.



Figure 1. MMBTA28 Device Package

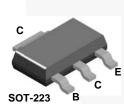


Figure 2. PZTA28 Device Package

Ordering Information

Part Number	Top Mark	Package	Packing Method
MMBTA28	3SS	SSOT 3L	Tape and Reel
PZTA28	A28	SOT-223 4L	Tape and Reel

Absolute Maximum Ratings^{(1), (2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	80	V
V _{CBO}	Collector-Base Voltage	80	V
V _{EBO}	Emitter-Base Voltage	12	V
Ι _C	Collector Current - Continuous	800	mA
T _{J,} T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Ма	Unit	
	Faiameter	MMBTA28 ⁽³⁾	PZTA28 ⁽⁴⁾	Unit
Б	Total Device Dissipation	350	1000	mW
PD	Derate Above 25°C	2.8	8.0	mW/°C
R _{θJA}	Thermal Resistance, Junction-to-Ambient	357	125	°C/W

Notes:

3. Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm; mounting pad for the collector lead minimum 6cm².

4. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

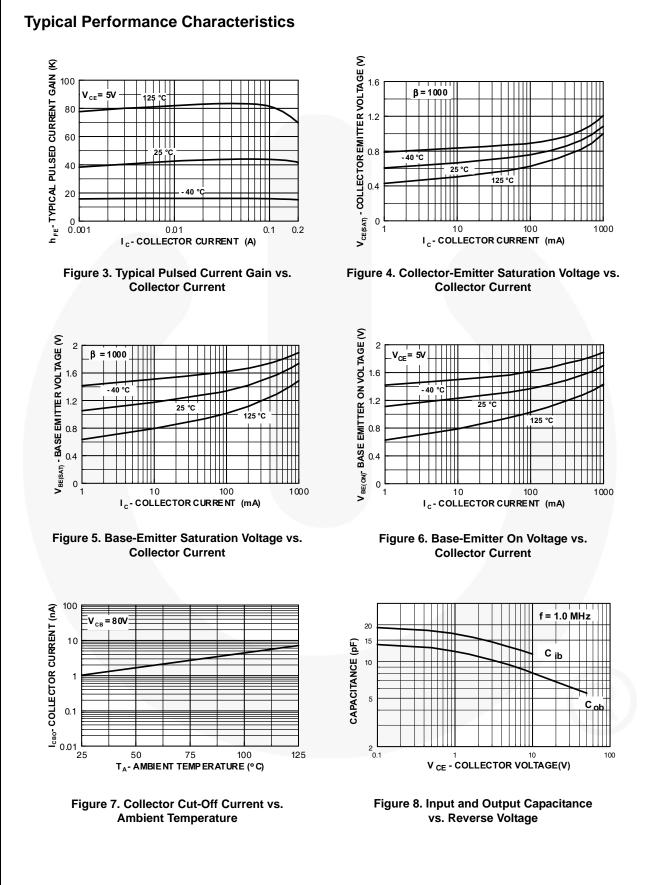
Electrical Characteristics⁽⁵⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

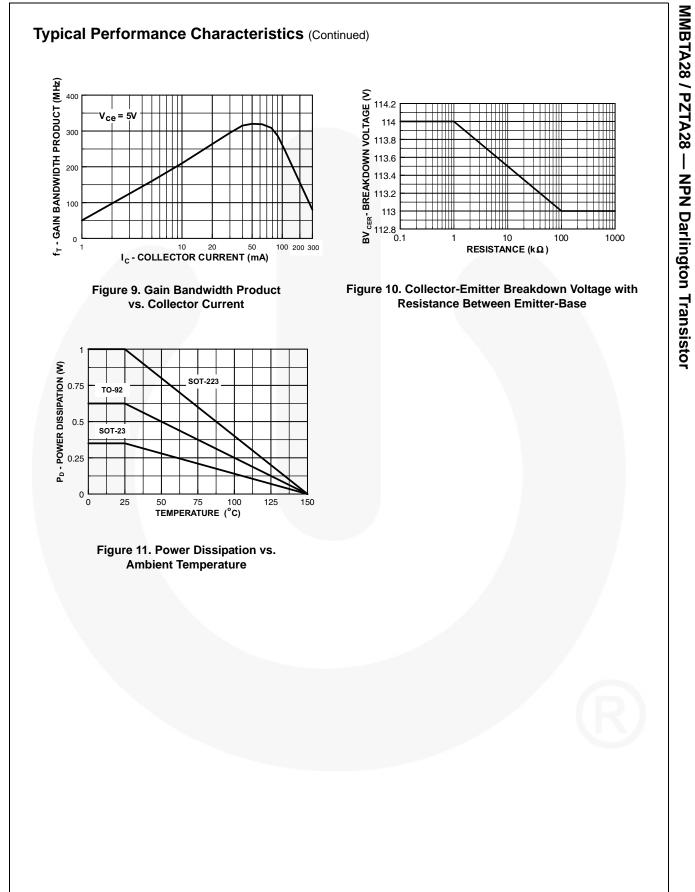
Symbol	Parameter	Conditions	Min.	Max.	Unit
V _{(BR)CES}	Collector-Emitter Breakdown Voltage	$I_{C} = 100 \ \mu A, \ V_{BE} = 0$	80		V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = 100 \ \mu \text{A}, \ I_{\rm E} = 0$	80		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_{\rm E} = 10 \ \mu {\rm A}, \ I_{\rm C} = 0$	12		V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = 60 \text{ V}, I_{E} = 0$		100	nA
I _{CES}	Collector Cut-Off Current	$V_{CE} = 60 \text{ V}, V_{BE} = 0$		500	nA
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = 10 \text{ V}, I_{C} = 0$		100	nA
h _{FE}	DC Current Gain	$I_{\rm C}$ = 10 mA, $V_{\rm CE}$ = 5.0 V	10000		
		$I_{\rm C}$ = 100 mA, $V_{\rm CE}$ = 5.0 V	10000		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 10 mA, I _B = 0.01 mA		1.2	V
		$I_{\rm C}$ = 100 mA, $I_{\rm B}$ = 0.1 mA		1.5	v
V _{BE(on)}	Base-Emitter On Voltage	$I_{\rm C}$ = 100 mA, $V_{\rm CE}$ = 5.0 V		2.0	V
f _T	Current Gain - Bandwidth Product	$I_{C} = 10 \text{ mA}, V_{CE} = 5.0 \text{ V},$ f = 100 MHz	125		MHz
C _{obo}	Output Capacitance	$V_{CB} = 1.0 \text{ V}, I_E = 0,$ f = 1.0 MHz		8.0	pF

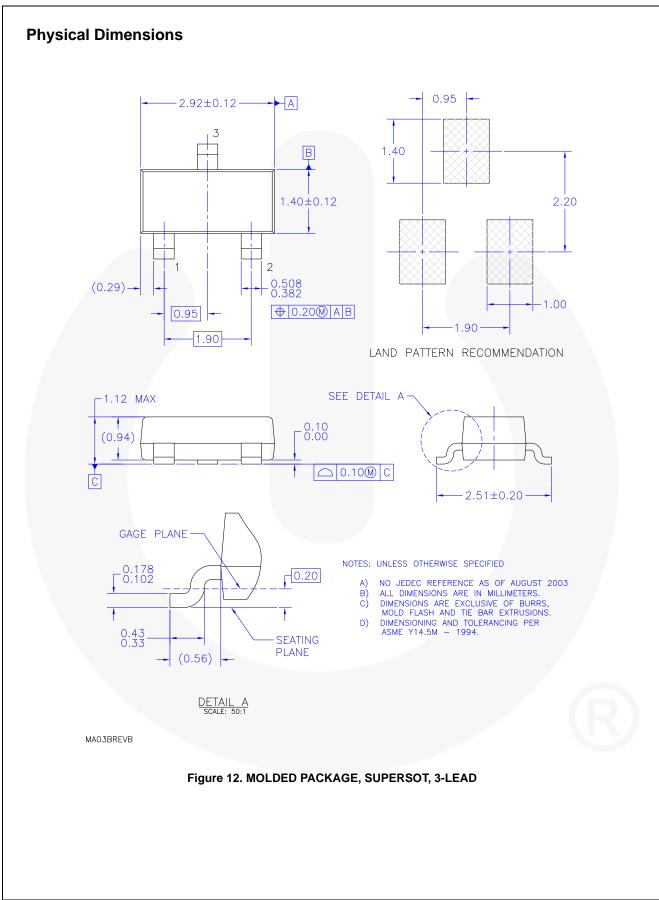
Note:

5. Pulse test: pulse width $\leq 300~\mu s,~duty~cycle \leq 2\%.$

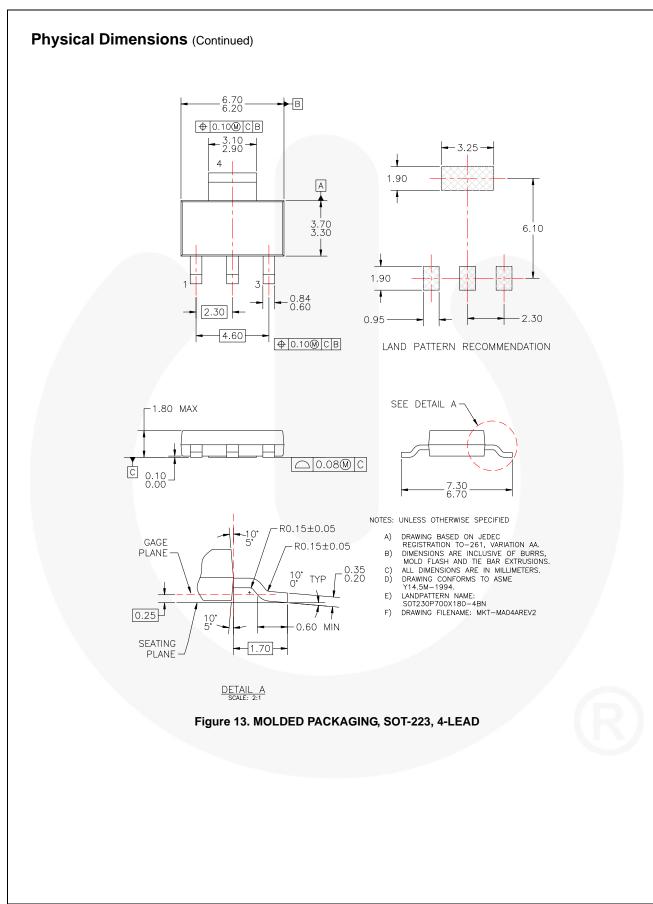


MMBTA28 / PZTA28 — NPN Darlington Transistor





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