

Dome Lens SMD LED



VLD.1535R...



VLD.1535G...

DESCRIPTION

The dome lens SMD LED series has been designed in a small untinted and clear molded package with lens for surface mounting as gullwing or reverse gullwing version. The VLD.1535... series is using recent ultrabright AllnGaP / Si chip technology with high luminous flux and large chip size allowing a high DC forward current up to 70 mA.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Product series: power
- Package: SMD dome lens
- Angle of half intensity: $\pm 22^\circ$

FEATURES

- Utilizing latest advanced AllnGaP technology
- Package type: surface-mount
- Package form: gullwing, reverse gullwing
- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.6
- High luminous flux and luminous intensity
- Luminous intensity and color categorized per packing unit
- Luminous intensity ratio per packing unit $I_{Vmax}/I_{Vmin} \leq 1.6$
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Preconditioning according to JEDEC® level 2a
- Suitable for reflow soldering according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

- Traffic signals and signs
- Interior and exterior lighting
- Indicator and backlighting purposes for audio, video, LCDs switches, symbols, illuminated advertising etc.

PARTS TABLE

PART	COLOR	LUMINOUS INTENSITY (mcd)			at I _F (mA)	WAVELENGTH (nm)			at I _F (mA)	FORWARD VOLTAGE (V)			at I _F (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLDS1535G-08	Super red	2800	5000	9000	50	626	630	637	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDS1535R-08	Super red	2800	5000	9000	50	626	630	637	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDR1535G-08	Red	3550	6500	11 200	50	619	624	631	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDR1535R-08	Red	3550	6500	11 200	50	619	624	631	50	1.9	2.2	2.7	50	AllnGaP on Si
VLDK1535G-08	Amber	4500	8000	14 000	50	611	616	621	50	1.9	2.25	2.7	50	AllnGaP on Si
VLDK1535R-08	Amber	4500	8000	14 000	50	611	616	621	50	1.9	2.25	2.7	50	AllnGaP on Si
VLDY1535G-08	Yellow	4500	8000	14 000	50	583	589	595	50	1.9	2.3	2.7	50	AllnGaP on Si
VLDY1535R-08	Yellow	4500	8000	14 000	50	583	589	595	50	1.9	2.3	2.7	50	AllnGaP on Si

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified) VLDS1535..., VLDR1535..., VLDK1535..., VLDY1535...

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage ⁽¹⁾	Short term application only	V _R	5	V
DC Forward current	T _{amb} ≤ 60 °C	I _F	70	mA
Power dissipation		P _V	200	mW
Junction temperature		T _j	125	°C
Operating temperature range		T _{amb}	-40 to +100	°C
Storage temperature range		T _{stg}	-40 to +100	°C
Thermal resistance junction-to-ambient	Mounted on PC board (pad size > 16 mm ²)	R _{thJA}	325	K/W

Note

⁽¹⁾ Driving the LED in reverse direction is suitable for a short term application only



OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) VLDS1535G, VLDS1535R, SUPER RED						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity ⁽¹⁾	$I_F = 50\text{ mA}$	I_V	2800	5000	9000	mcd
Luminous flux/luminous intensity		ϕ_V/I_V	-	1.2	-	mlm/mcd
Dominant wavelength ⁽¹⁾	$I_F = 50\text{ mA}$	λ_d	626	630	637	nm
Peak wavelength	$I_F = 50\text{ mA}$	λ_p	-	639	-	nm
Spectral bandwidth at 50 % $I_{rel\ max.}$	$I_F = 50\text{ mA}$	$\Delta\lambda$	-	18	-	nm
Angle of half intensity	$I_F = 50\text{ mA}$	ϕ	-	± 22	-	deg
Forward voltage ⁽¹⁾	$I_F = 50\text{ mA}$	V_F	1.9	2.2	2.7	V
Reverse current	$V_R = 5\text{ V}$	I_R	-	0.01	10	μA

Note

⁽¹⁾ Tolerances: $\pm 15\%$ for I_V , $\pm 0.1\text{ V}$ for V_F , $\pm 1\text{ nm}$ for λ_d

OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) VLDR1535G, VLDR1535R, RED						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity ⁽¹⁾	$I_F = 50\text{ mA}$	I_V	3550	6500	11 200	mcd
Luminous flux/luminous intensity		ϕ_V/I_V	-	1.2	-	mlm/mcd
Dominant wavelength ⁽¹⁾	$I_F = 50\text{ mA}$	λ_d	619	624	631	nm
Peak wavelength	$I_F = 50\text{ mA}$	λ_p	-	632	-	nm
Spectral bandwidth at 50 % $I_{rel\ max.}$	$I_F = 50\text{ mA}$	$\Delta\lambda$	-	18	-	nm
Angle of half intensity	$I_F = 50\text{ mA}$	ϕ	-	± 22	-	deg
Forward voltage ⁽¹⁾	$I_F = 50\text{ mA}$	V_F	1.9	2.2	2.7	V
Reverse current	$V_R = 5\text{ V}$	I_R	-	0.01	10	μA

Note

⁽¹⁾ Tolerances: $\pm 15\%$ for I_V , $\pm 0.1\text{ V}$ for V_F , $\pm 1\text{ nm}$ for λ_d

OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) VLDK1535G, VLDK1535R, AMBER						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity ⁽¹⁾	$I_F = 50\text{ mA}$	I_V	4500	8000	14 000	mcd
Luminous flux/luminous intensity		ϕ_V/I_V	-	1.2	-	mlm/mcd
Dominant wavelength ⁽¹⁾	$I_F = 50\text{ mA}$	λ_d	611	616	621	nm
Peak wavelength	$I_F = 50\text{ mA}$	λ_p	-	622	-	nm
Spectral bandwidth at 50 % $I_{rel\ max.}$	$I_F = 50\text{ mA}$	$\Delta\lambda$	-	18	-	nm
Angle of half intensity	$I_F = 50\text{ mA}$	ϕ	-	± 22	-	deg
Forward voltage ⁽¹⁾	$I_F = 50\text{ mA}$	V_F	1.9	2.25	2.7	V
Reverse current	$V_R = 5\text{ V}$	I_R	-	0.01	10	μA

Note

⁽¹⁾ Tolerances: $\pm 15\%$ for I_V , $\pm 0.1\text{ V}$ for V_F , $\pm 1\text{ nm}$ for λ_d



OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified) VLDY1535G, VLDY1535R, YELLOW						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity ⁽¹⁾	$I_F = 50\text{ mA}$	I_V	4500	8000	14 000	mcd
Luminous flux/luminous intensity		ϕ_V/I_V	-	1.2	-	mlm/mcd
Dominant wavelength ⁽¹⁾	$I_F = 50\text{ mA}$	λ_d	583	589	595	nm
Peak wavelength	$I_F = 50\text{ mA}$	λ_p	-	591	-	nm
Spectral bandwidth at 50 % $I_{rel\ max.}$	$I_F = 50\text{ mA}$	$\Delta\lambda$		17		nm
Angle of half intensity	$I_F = 50\text{ mA}$	ϕ		± 22		deg
Forward voltage ⁽¹⁾	$I_F = 50\text{ mA}$	V_F	1.9	2.3	2.7	V
Reverse current	$V_R = 5\text{ V}$	I_R		0.01	10	μA

Note

⁽²⁾ Tolerances: $\pm 15\%$ for I_V , $\pm 0.1\text{ V}$ for V_F , $\pm 1\text{ nm}$ for λ_d

COLOR CLASSIFICATION				
GROUP	DOMINANT WAVELENGTH (nm)			
	AMBER		YELLOW	
	MIN.	MAX.	MIN.	MAX.
2	611	616		
3	616	621	583	586
4			586	589
5			589	592
6			592	595

Note

• Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of $\pm 1\text{ nm}$

LUMINOUS INTENSITY CLASSIFICATION		
GROUP	LUMINOUS INTENSITY (mcd)	
STANDARD	MIN.	MAX.
CA	2800	3550
CB	3550	4500
DA	4500	5600
DB	5600	7100
EA	7100	9000
EB	9000	11 200
FA	11 200	14 000

Note

• Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of $\pm 15\%$.
 The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).
 In order to ensure availability, single brightness groups will not be orderable.
 In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.
 In order to ensure availability, single wavelength groups will not be orderable

TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)



Fig. 1 - Maximum Permissible Forward Current vs. Ambient Temperature

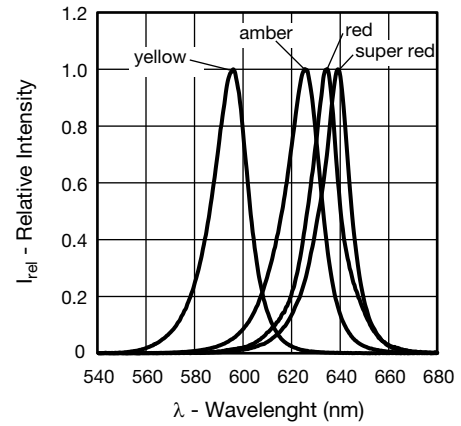


Fig. 4 - Relative Intensity vs. Wavelength

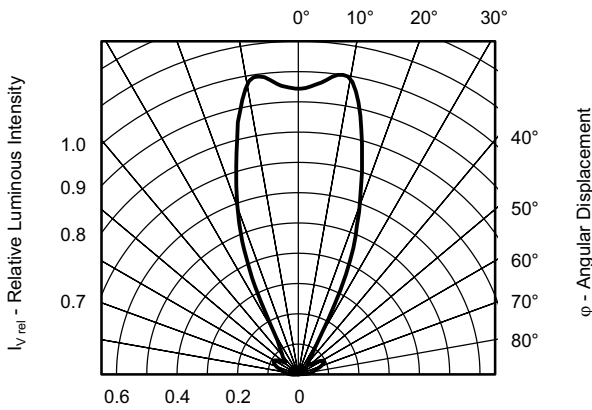


Fig. 2 - Relative Luminous Intensity vs. Angular Displacement



Fig. 5 - Forward Current vs. Forward Voltage

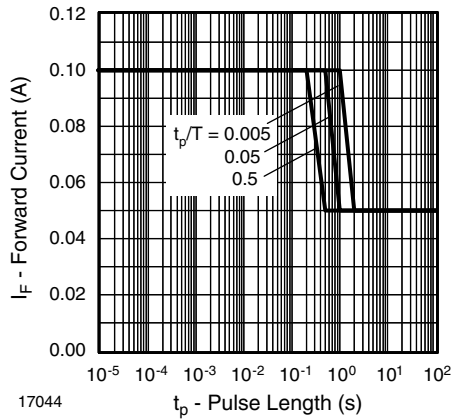


Fig. 3 - Forward Current vs. Pulse Length



Fig. 6 - Relative Luminous Intensity vs. Forward Current



Fig. 7 - Change of Forward Voltage vs. Ambient Temperature

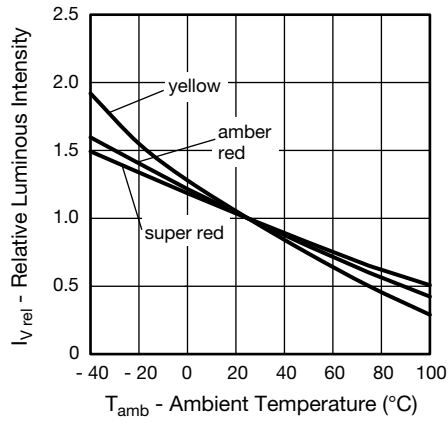


Fig. 8 - Relative Luminous Intensity vs. Ambient Temperature



Fig. 9 - Change of Dominant Wavelength vs. Ambient Temperature



PACKAGE DIMENSIONS in millimeters: **VLD.1535G..** (gullwing)



Technical drawings according to DIN specifications



Not indicated tolerances ± 0.2



Drawing-No.: 6.544-5408.01-4
Issue: 1; 13.09.12

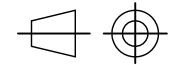


VLDS1535., VLDR1535., VLDK1535., VLDY1535..

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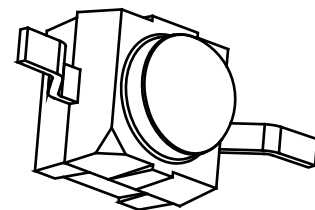
Vishay Semiconductors

PACKAGE DIMENSIONS in millimeters: **VLD.1535R..** (reverse gullwing)



Technical drawings according to DIN specifications

Not indicated tolerances ± 0.2



Drawing-No.: 6.544-5409.01-4
Issue: 1; 13.09.12

TAPING AND REEL DIMENSIONS in millimeters: **VLD.1535G..** (gullwing)

Reel



Leader and trailer tape



Terminal position in tape

Device	Lead I	Lead II
VLDY153.G...-08	Cathode	Anode
VLDK153.G...-08		
VLDR153.G...-08		
VLDS153.G...-08		



Drawing-No.: 9.800-5137.01-4
Issue: 1; 17.03.16



TAPING AND REEL DIMENSIONS in millimeters: **VLD.1535R..** (reverse gullwing)

Reel



Leader and trailer tape



Terminal position in tape

Device	Lead I	Lead II
VLDY153.R...-08	Cathode	Anode
VLDK153.R...-08		
VLDR153.R...-08		
VLDS153.R...-08		



Drawing-No.: 9.800-5138.01-4
Issue: 1; 17.03.16

COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3
0.1 N to 1.3 N
300 ± 10 mm/min
165° to 180° peel angle

LABEL

Standard bar code labels for finished goods
The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

SOLDERING PROFILE

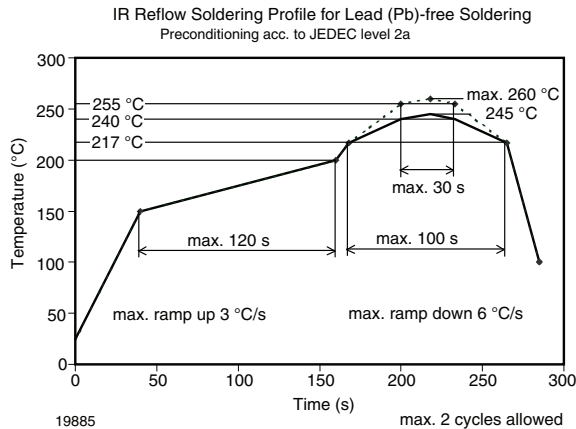


Fig. 10 - Vishay Lead (Pb)-free Reflow Soldering Profile (according to J-STD-020)

BAR CODE PRODUCT LABEL (example)



- A. 2D barcode
- B. PartNo = Vishay part number
- C. QTY = quantity
- D. SelCode = selection code (binning)
- E. PTC = code of manufacturing plant
- F. Batch = date code: year / week / plant code
- G. Region code
- H. SL = sales location
- I. Terminations finishing
- J. Lead (Pb)-free symbol
- K. Halogen-free symbol
- L. RoHS symbol

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



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FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

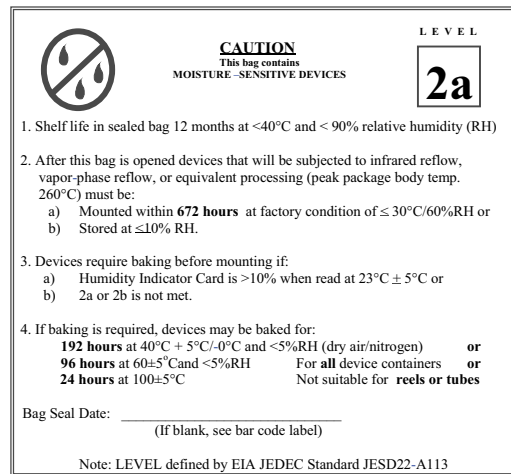
- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

- 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or
- 96 h at 60 °C + 5 °C and < 5 % RH for all device containers or
- 24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

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Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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