

# MC74VHCT02A

## Quad 2-Input NOR Gate

The MC74VHCT02A is an advanced high speed CMOS 2-input NOR gate fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation.

The VHCT inputs are compatible with TTL levels. This device can be used as a level converter for interfacing 3.3 V to 5.0 V, because it has full 5 V CMOS level output swings.

The VHCT02A input structures provide protection when voltages between 0 V and 5.5 V are applied, regardless of the supply voltage. The output structures also provide protection when  $V_{CC} = 0$  V. These input and output structures help prevent device destruction caused by supply voltage – input/output voltage mismatch, battery backup, hot insertion, etc.

The internal circuit is composed of three stages, including a buffer output which provides high noise immunity and stable output. The inputs tolerate voltages up to 7 V, allowing the interface of 5 V systems to 3 V systems.

### Features

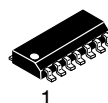
- High Speed:  $t_{PD} = 3.6$  ns (Typ) at  $V_{CC} = 5$  V
- Low Power Dissipation:  $I_{CC} = 2$   $\mu$ A (Max) at  $T_A = 25^\circ$ C
- TTL-Compatible Inputs:  $V_{IL} = 0.8$  V;  $V_{IH} = 2.0$  V
- Power Down Protection Provided on Inputs
- Balanced Propagation Delays
- Designed for 2 V to 5.5 V Operating Range
- Low Noise:  $V_{OLP} = 0.8$  V (Max)
- Pin and Function Compatible with Other Standard Logic Families
- Latchup Performance Exceeds 300 mA
- ESD Performance: HBM > 2000 V; Machine Model > 200 V
- Chip Complexity: 40 FETs or 10 Equivalent Gates
- These Devices are Pb-Free and are RoHS Compliant



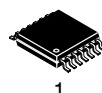
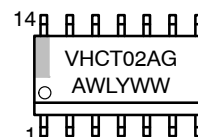
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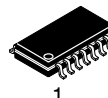
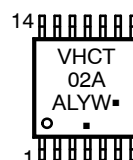
### MARKING DIAGRAMS



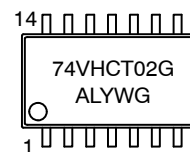
SOIC-14  
D SUFFIX  
CASE 751A



TSSOP-14  
DT SUFFIX  
CASE 948G



SOEIAJ-14  
M SUFFIX  
CASE 965



A = Assembly Location  
WL, L = Wafer Lot  
Y = Year  
WW, W = Work Week  
G or ■ = Pb-Free Package  
(Note: Microdot may be in either location)

### ORDERING INFORMATION

| Device           | Package               | Shipping <sup>†</sup> |
|------------------|-----------------------|-----------------------|
| MC74VHCT02ADR2G  | SOIC-14<br>(Pb-Free)  | 2500/Tape&Reel        |
| MC74VHCT02ADTR2G | TSSOP14<br>(Pb-Free)  | 2500/Tape&Reel        |
| MC74VHCT02AMG    | SOEIAJ14<br>(Pb-Free) | 50 Units / Rail       |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MC74VHCT02A

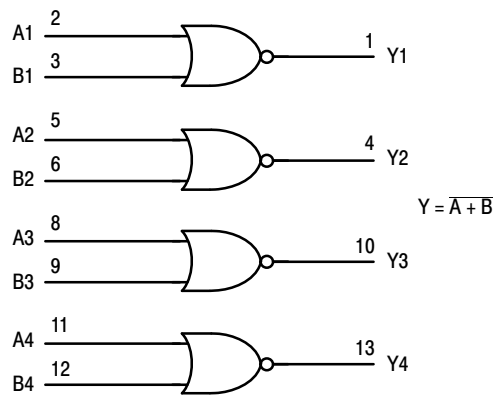


Figure 1. LOGIC DIAGRAM

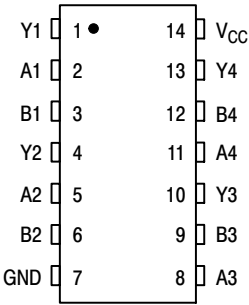


Figure 2. PIN ASSIGNMENT

FUNCTION TABLE

| Inputs |   | Output |
|--------|---|--------|
| A      | B | Y      |
| L      | L | H      |
| L      | H | L      |
| H      | L | L      |
| H      | H | L      |

# MC74VHCT02A

## MAXIMUM RATINGS\*

| Symbol           | Parameter                                                        | Value                          | Unit |
|------------------|------------------------------------------------------------------|--------------------------------|------|
| V <sub>CC</sub>  | DC Supply Voltage                                                | – 0.5 to + 7.0                 | V    |
| V <sub>in</sub>  | DC Input Voltage                                                 | – 0.5 to + 7.0                 | V    |
| V <sub>out</sub> | DC Output Voltage                                                | – 0.5 to V <sub>CC</sub> + 0.5 | V    |
| I <sub>IK</sub>  | Input Diode Current                                              | – 20                           | mA   |
| I <sub>OK</sub>  | Output Diode Current                                             | ± 20                           | mA   |
| I <sub>out</sub> | DC Output Current, per Pin                                       | ± 25                           | mA   |
| I <sub>CC</sub>  | DC Supply Current, V <sub>CC</sub> and GND Pins                  | ± 50                           | mA   |
| P <sub>D</sub>   | Power Dissipation in Still Air, SOIC Packages†<br>TSSOP Package† | 500<br>450                     | mW   |
| T <sub>stg</sub> | Storage Temperature                                              | – 65 to + 150                  | °C   |

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V<sub>in</sub> and V<sub>out</sub> should be constrained to the range GND ≤ (V<sub>in</sub> or V<sub>out</sub>) ≤ V<sub>CC</sub>. Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or V<sub>CC</sub>). Unused outputs must be left open.

\* Absolute maximum continuous ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute-maximum-rated conditions is not implied.

† Derating — SOIC Packages: – 7 mW/°C from 65° to 125°C  
TSSOP Package: – 6.1 mW/°C from 65° to 125°C

## RECOMMENDED OPERATING CONDITIONS

| Symbol                          | Parameter                                              | Min  | Max             | Unit |
|---------------------------------|--------------------------------------------------------|------|-----------------|------|
| V <sub>CC</sub>                 | DC Supply Voltage                                      | 4.5  | 5.5             | V    |
| V <sub>in</sub>                 | DC Input Voltage                                       | 0    | 5.5             | V    |
| V <sub>out</sub>                | DC Output Voltage                                      | 0    | V <sub>CC</sub> | V    |
| T <sub>A</sub>                  | Operating Temperature                                  | – 40 | + 85            | °C   |
| t <sub>r</sub> , t <sub>f</sub> | Input Rise and Fall Time V <sub>CC</sub> = 5.0V ± 0.5V | 0    | 20              | ns/V |

## DC ELECTRICAL CHARACTERISTICS

| Symbol           | Parameter                                                                                 | Test Conditions                                                                                            | V <sub>CC</sub><br>(V) | T <sub>A</sub> = 25°C |            |                    | T <sub>A</sub> ≤ 85°C |                    | T <sub>A</sub> ≤ 125°C |                    | Unit |
|------------------|-------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------|------------------------|-----------------------|------------|--------------------|-----------------------|--------------------|------------------------|--------------------|------|
|                  |                                                                                           |                                                                                                            |                        | Min                   | Typ        | Max                | Min                   | Max                | Min                    | Max                |      |
| V <sub>IH</sub>  | Minimum High-Level Input Voltage                                                          |                                                                                                            | 3.0<br>4.5<br>5.5      | 1.2<br>2.0<br>2.0     |            |                    | 1.2<br>2.0<br>2.0     |                    | 1.2<br>2.0<br>2.0      |                    | V    |
| V <sub>IL</sub>  | Maximum Low-Level Input Voltage                                                           |                                                                                                            | 3.0<br>4.5<br>5.5      |                       |            | 0.53<br>0.8<br>0.8 |                       | 0.53<br>0.8<br>0.8 |                        | 0.53<br>0.8<br>0.8 | V    |
| V <sub>OH</sub>  | Minimum High-Level Output Voltage<br>V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OH</sub> = – 50μA                           | 3.0<br>4.5             | 2.9<br>4.4            | 3.0<br>4.5 |                    | 2.9<br>4.4            |                    | 2.9<br>4.4             |                    | V    |
|                  |                                                                                           | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OH</sub> = – 4mA<br>I <sub>OH</sub> = – 8mA | 3.0<br>4.5             | 2.58<br>3.94          |            |                    | 2.48<br>3.80          |                    | 2.34<br>3.66           |                    |      |
| V <sub>OL</sub>  | Maximum Low-Level Output Voltage<br>V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>  | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OL</sub> = 50μA                             | 3.0<br>4.5             |                       | 0.0<br>0.0 | 0.1<br>0.1         |                       | 0.1<br>0.1         |                        | 0.1<br>0.1         | V    |
|                  |                                                                                           | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OL</sub> = 4mA<br>I <sub>OL</sub> = 8mA     | 3.0<br>4.5             |                       |            | 0.36<br>0.36       |                       | 0.44<br>0.44       |                        | 0.52<br>0.52       |      |
| I <sub>IN</sub>  | Maximum Input Leakage Current                                                             | V <sub>IN</sub> = 5.5 V or GND                                                                             | 0 to 5.5               |                       |            | ± 0.1              |                       | ± 1.0              |                        | ± 1.0              | μA   |
| I <sub>CC</sub>  | Maximum Quiescent Supply Current                                                          | V <sub>IN</sub> = V <sub>CC</sub> or GND                                                                   | 5.5                    |                       |            | 2.0                |                       | 20                 |                        | 40                 | μA   |
| I <sub>CCT</sub> | Quiescent Supply Current                                                                  | Input: V <sub>IN</sub> = 3.4V                                                                              | 5.5                    |                       |            | 1.35               |                       | 1.50               |                        | 1.65               | mA   |
| I <sub>OPD</sub> | Output Leakage Current                                                                    | V <sub>OUT</sub> = 5.5V                                                                                    | 0.0                    |                       |            | 0.5                |                       | 5.0                |                        | 10                 | μA   |

# MC74VHCT02A

## AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0\text{ns}$ )

| Symbol                   | Parameter                                    | Test Conditions                                    | $T_A = 25^\circ\text{C}$ |     |      | $T_A \leq 85^\circ\text{C}$ |      | $T_A \leq 125^\circ\text{C}$ |      | Unit |
|--------------------------|----------------------------------------------|----------------------------------------------------|--------------------------|-----|------|-----------------------------|------|------------------------------|------|------|
|                          |                                              |                                                    | Min                      | Typ | Max  | Min                         | Max  | Max                          | Max  |      |
| $t_{PLH}$ ,<br>$t_{PHL}$ | Maximum Propagation Delay, Input A or B to Y | $V_{CC} = 3.0 \pm 0.3\text{V}$ $C_L = 15\text{pF}$ |                          | 5.6 | 7.9  |                             | 9.5  |                              | 12.5 | ns   |
|                          |                                              | $C_L = 50\text{pF}$                                |                          | 8.1 | 11.4 |                             | 13.0 |                              | 17.5 |      |
|                          |                                              | $V_{CC} = 5.0 \pm 0.5\text{V}$ $C_L = 15\text{pF}$ |                          | 3.6 | 5.5  |                             | 6.5  |                              | 9.0  |      |
| $C_{in}$                 | Maximum Input Capacitance                    | $C_L = 50\text{pF}$                                |                          | 5.1 | 7.5  |                             | 8.5  |                              | 11.0 | pF   |
|                          |                                              |                                                    |                          | 4   | 10   |                             | 10   |                              | 10   |      |

|          |                                        |                                                       |    |
|----------|----------------------------------------|-------------------------------------------------------|----|
| $C_{PD}$ | Power Dissipation Capacitance (Note 1) | Typical @ $25^\circ\text{C}$ , $V_{CC} = 5.0\text{V}$ | pF |
|          |                                        | 20                                                    |    |

1.  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:  $I_{CC(OPR)} = C_{PD} \cdot V_{CC} \cdot f_{in} + I_{CC}/4$  (per gate).  $C_{PD}$  is used to determine the no-load dynamic power consumption;  $P_D = C_{PD} \cdot V_{CC}^2 \cdot f_{in} + I_{CC} \cdot V_{CC}$ .

## NOISE CHARACTERISTICS (Input $t_r = t_f = 3.0\text{ns}$ , $C_L = 50\text{pF}$ , $V_{CC} = 5.0\text{V}$ )

| Symbol    | Characteristic                           | $T_A = 25^\circ\text{C}$ |       | Unit |
|-----------|------------------------------------------|--------------------------|-------|------|
|           |                                          | Typ                      | Max   |      |
| $V_{OLP}$ | Quiet Output Maximum Dynamic $V_{OL}$    | 0.3                      | 0.8   | V    |
| $V_{OLV}$ | Quiet Output Minimum Dynamic $V_{OL}$    | - 0.3                    | - 0.8 | V    |
| $V_{IHD}$ | Minimum High Level Dynamic Input Voltage |                          | 3.5   | V    |
| $V_{ILD}$ | Maximum Low Level Dynamic Input Voltage  |                          | 1.5   | V    |

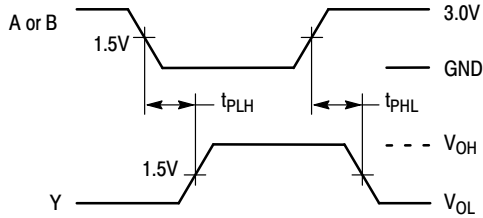
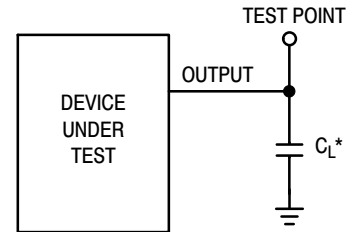


Figure 3. Switching Waveforms



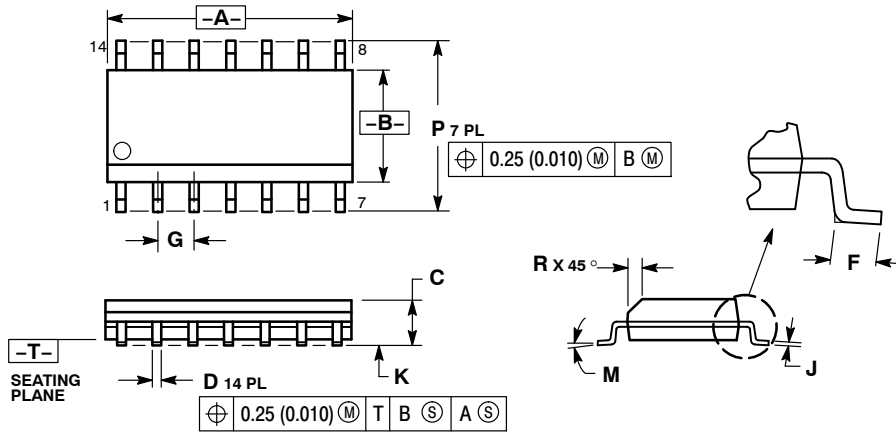
\*Includes all probe and jig capacitance

Figure 4. Test Circuit

# MC74VHCT02A

## PACKAGE DIMENSIONS

SOIC-14  
CASE 751A-03  
ISSUE J

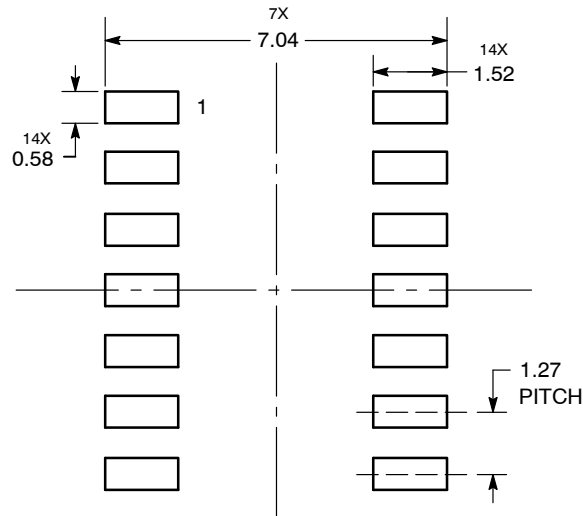


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 8.55        | 8.75 | 0.337     | 0.344 |
| B   | 3.80        | 4.00 | 0.150     | 0.157 |
| C   | 1.35        | 1.75 | 0.054     | 0.068 |
| D   | 0.35        | 0.49 | 0.014     | 0.019 |
| F   | 0.40        | 1.25 | 0.016     | 0.049 |
| G   | 1.27 BSC    |      | 0.050 BSC |       |
| J   | 0.19        | 0.25 | 0.008     | 0.009 |
| K   | 0.10        | 0.25 | 0.004     | 0.009 |
| M   | 0°          | 7°   | 0°        | 7°    |
| P   | 5.80        | 6.20 | 0.228     | 0.244 |
| R   | 0.25        | 0.50 | 0.010     | 0.019 |

### SOLDERING FOOTPRINT\*



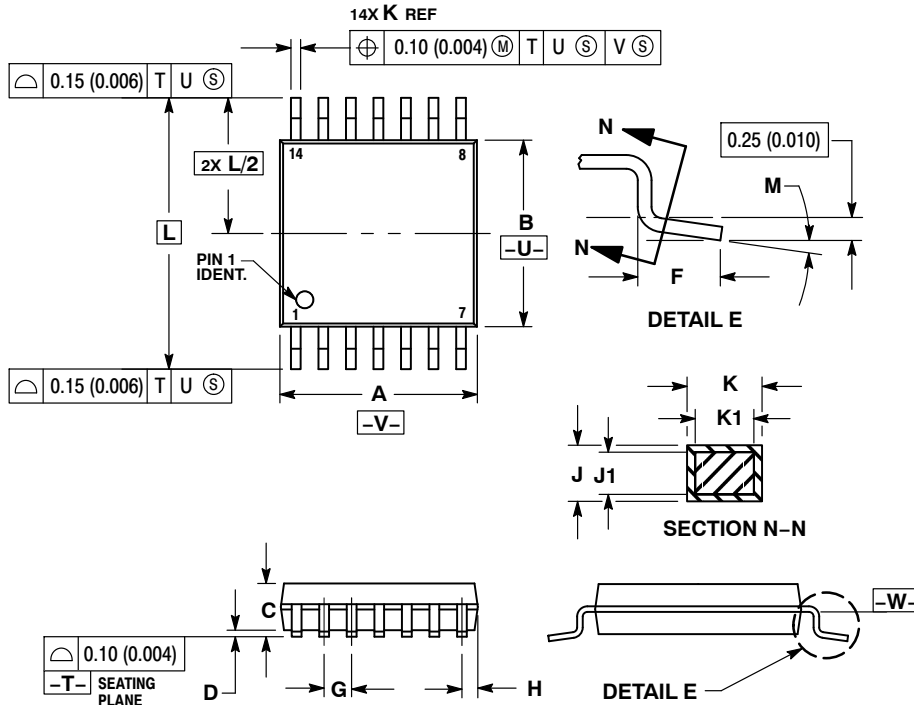
DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MC74VHCT02A

## PACKAGE DIMENSIONS

**TSSOP-14**  
**DT SUFFIX**  
CASE 948G-01  
ISSUE B

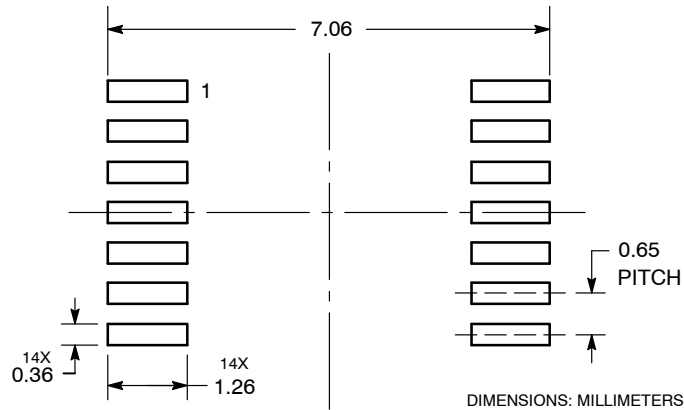


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE -W-.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 4.90        | 5.10 | 0.193     | 0.200 |
| B   | 4.30        | 4.50 | 0.169     | 0.177 |
| C   | ---         | 1.20 | ---       | 0.047 |
| D   | 0.05        | 0.15 | 0.002     | 0.006 |
| F   | 0.50        | 0.75 | 0.020     | 0.030 |
| G   | 0.65 BSC    | ---  | 0.026 BSC | ---   |
| H   | 0.50        | 0.60 | 0.020     | 0.024 |
| J   | 0.09        | 0.20 | 0.004     | 0.008 |
| J1  | 0.09        | 0.16 | 0.004     | 0.006 |
| K   | 0.19        | 0.30 | 0.007     | 0.012 |
| K1  | 0.19        | 0.25 | 0.007     | 0.010 |
| L   | 6.40 BSC    | ---  | 0.252 BSC | ---   |
| M   | 0°          | 8°   | 0°        | 8°    |

### SOLDERING FOOTPRINT\*

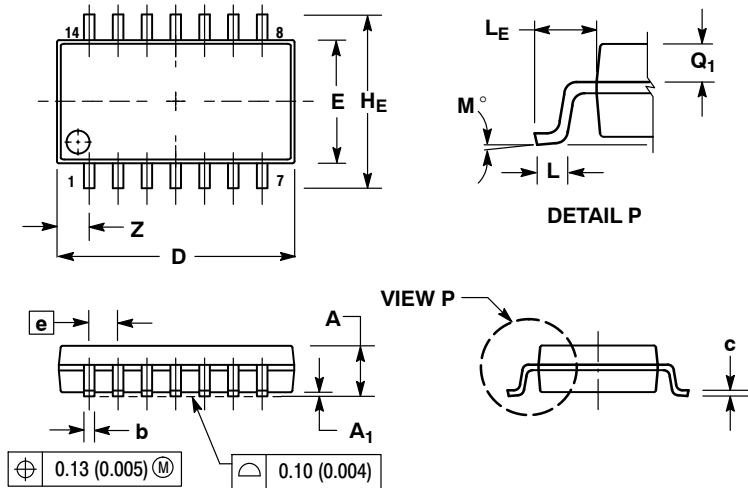


\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MC74VHCT02A

## PACKAGE DIMENSIONS

SOEIAJ-14  
CASE 965-01  
ISSUE B



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
5. THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 (0.018).

| DIM            | MILLIMETERS |       | INCHES    |       |
|----------------|-------------|-------|-----------|-------|
|                | MIN         | MAX   | MIN       | MAX   |
| A              | ---         | 2.05  | ---       | 0.081 |
| A <sub>1</sub> | 0.05        | 0.20  | 0.002     | 0.008 |
| b              | 0.35        | 0.50  | 0.014     | 0.020 |
| c              | 0.10        | 0.20  | 0.004     | 0.008 |
| D              | 9.90        | 10.50 | 0.390     | 0.413 |
| E              | 5.10        | 5.45  | 0.201     | 0.215 |
| e              | 1.27 BSC    |       | 0.050 BSC |       |
| H <sub>E</sub> | 7.40        | 8.20  | 0.291     | 0.323 |
| L              | 0.50        | 0.85  | 0.020     | 0.033 |
| L <sub>E</sub> | 1.10        | 1.50  | 0.043     | 0.059 |
| M              | 0°          | 10°   | 0°        | 10°   |
| Q <sub>1</sub> | 0.70        | 0.90  | 0.028     | 0.035 |
| Z              | ---         | 1.42  | ---       | 0.056 |

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- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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