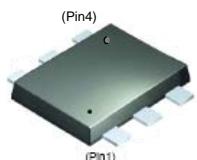


# 2N7002V/VA

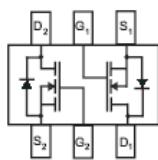
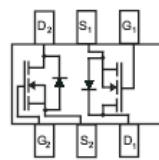
## N-Channel Enhancement Mode Field Effect Transistor

### Features

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Lead Free By Design/RoHS Compliant



\* Pin1 and Pin4 are exchangeable.

2N7002V  
Marking : AB2N7002VA  
Marking : AC

### Absolute Maximum Ratings \* $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain-Source Voltage	60	V
$V_{DGR}$	Drain-Gate Voltage $R_{GS} \leq 1.0\text{M}\Omega$	60	V
$V_{GSS}$	Gate-Source Voltage Continuous Pulsed	$\pm 20$ $\pm 40$	V
$I_D$	Drain Current Continuous Pulsed	280 1.5	mA A
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to +150	°C

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### Thermal Characteristics

Symbol	Parameter	Value	Units
$P_D$	Total Device Dissipation Derating above $T_A = 25^\circ\text{C}$	250 2.0	mW mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient *	500	°C/W

\* Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch, Minimum land pad size.

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
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**Off Characteristics** (Note1)

$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=10\mu\text{A}$	60	78	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$ $V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}, @T_C=125^\circ\text{C}$	- 7	0.001 500	1.0 500	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	0.2	$\pm 100$	nA

**On Characteristics** (Note1)

$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.76	2.5	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=5\text{V}, I_{\text{D}}=0.05\text{A},$ $V_{\text{GS}}=10\text{V}, I_{\text{D}}=0.5\text{A}, @T_J=125^\circ\text{C}$	- -	1.6 2.53	7.5 13.5	$\Omega$
$I_{\text{D}(\text{ON})}$	On-State Drain Current	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=7.5\text{V}$	0.5	1.43	-	A
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=0.2\text{A}$	80	356.5	-	mS

**Dynamic Characteristics**

$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	37.8	50	pF
$C_{\text{oss}}$	Output Capacitance		-	12.4	25	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	6.5	7.0	pF

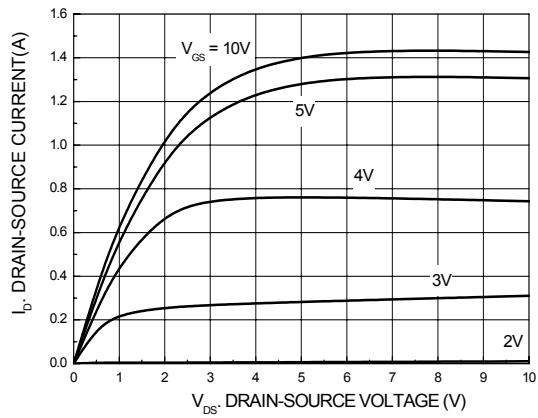
**Switching Characteristics**

$t_{\text{D}(\text{ON})}$	Turn-On Delay Time	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=0.2\text{A}, V_{\text{GEN}}=10\text{V}$ $R_L=150\Omega, R_{\text{GEN}}=25\Omega$	-	5.85	20	ns
$t_{\text{D}(\text{OFF})}$	Turn-Off Delay Time		-	12.5	20	

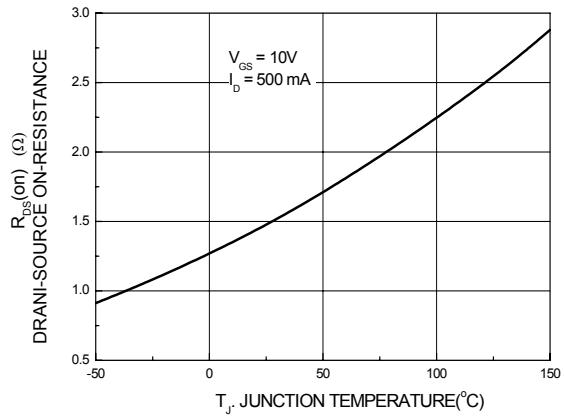
Note1 : Short duration test pulse used to minimize self-heating effect.

## Typical Performance Characteristics

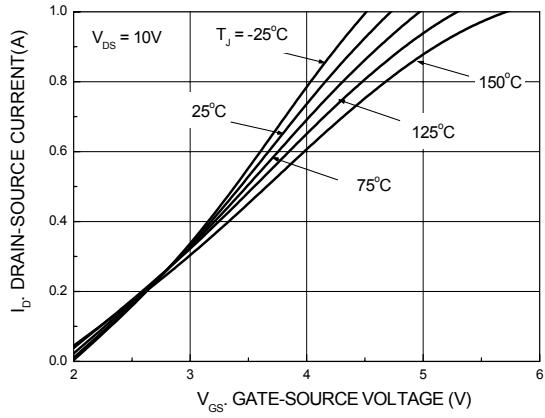
**Figure 1. On-Region Characteristics**



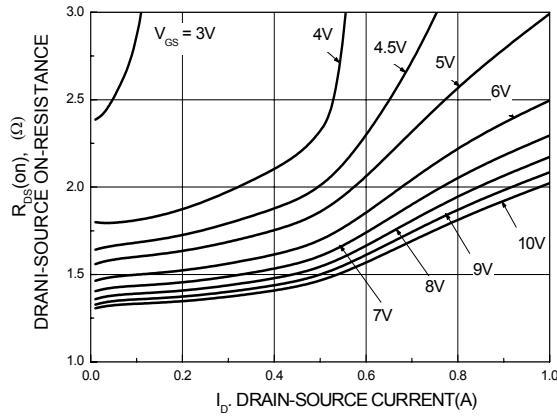
**Figure 3. On-Resistance Variation with Temperature**



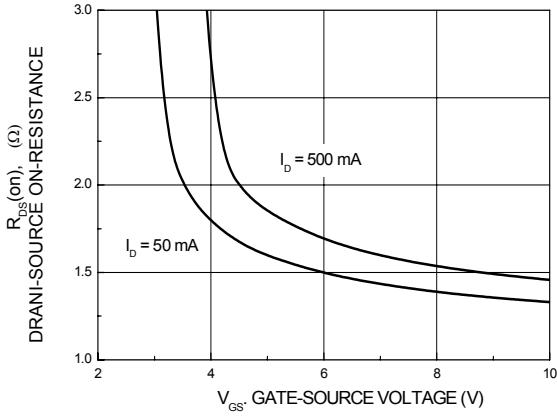
**Figure 5. Transfer Characteristics**



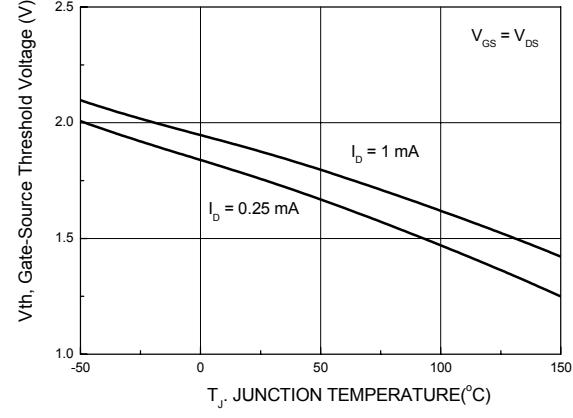
**Figure 2. On-Resistance Variation with Gate Voltage and Drain Current**



**Figure 4. On-Resistance Variation with Gate-Source Voltage**

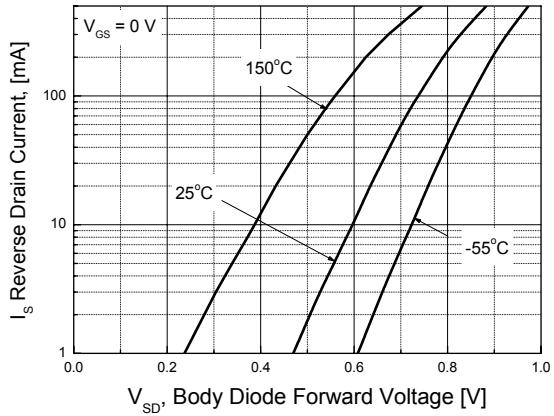


**Figure 6. Gate Threshold Variation with Temperature**

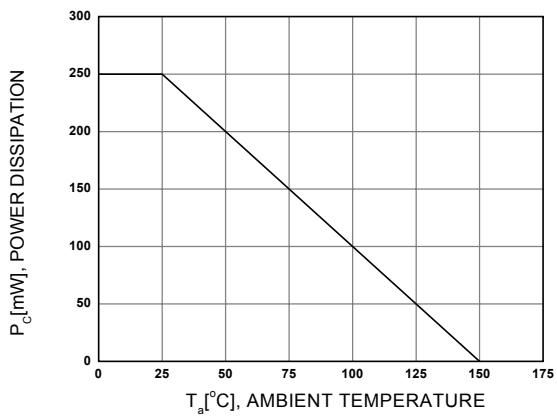


## Typical Performance Characteristics

**Figure 7. Reverse Drain Current Variation with Diode Forward Voltage and Temperature**

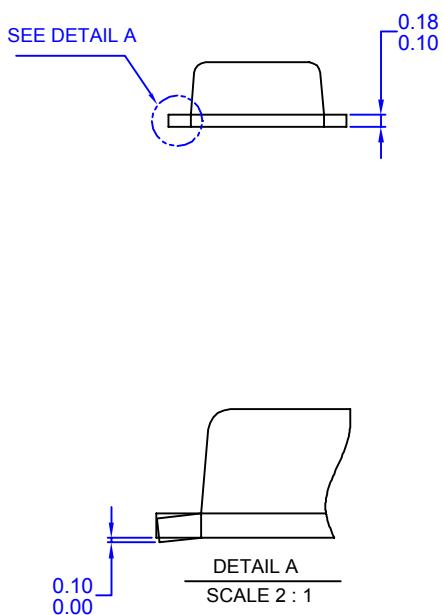
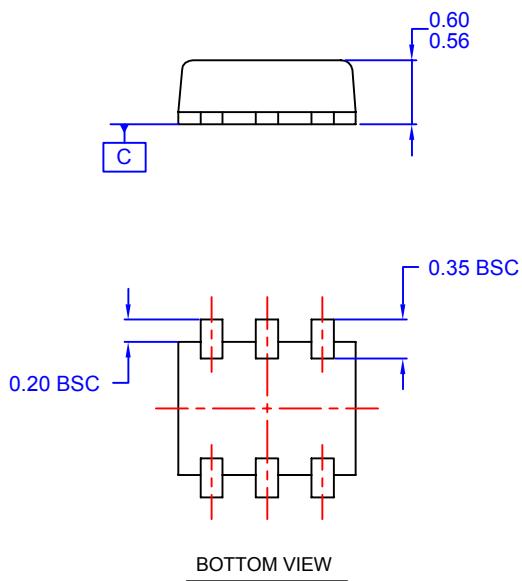
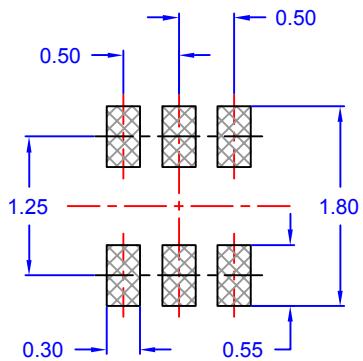
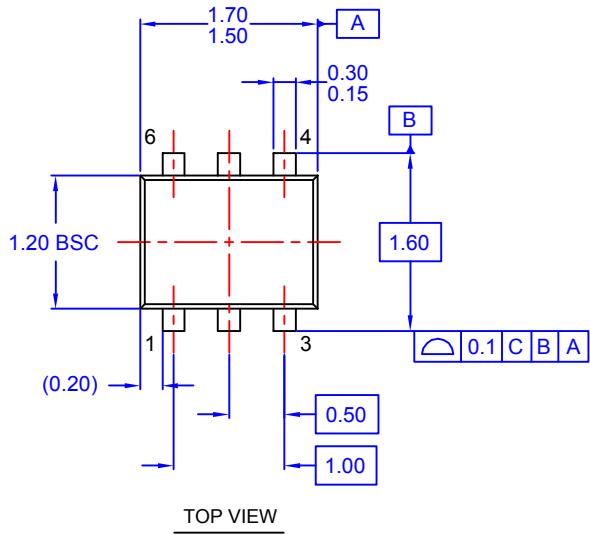


**Figure 8. Power Derating**



## Package Dimensions

SOT-563F

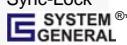


Dimensions in Millimeters



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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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Rev. I47

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"LifeElectronics" LLC

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