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FA	IRCH	ILD				Novembo	er 1998
	6329L trated L c	oad Switch					
-				Features			
General Description This device is particularly suited for compact power management in portable electronic equipment where 2.5V to 8V input and 2.5A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOT [™] -6			ompact power oment where	 V_{DROP}=0.2V @ V_{IN}=5V, I_L=2.8A. R_(ON) = 0.07Ω V_{DROP}=0.2V @ V_{IN}=2.5V, I_L=1.9A. R_(ON) = 0.105Ω. Control MOSFET (Q1) includes Zener protection for ESD ruggedness (>6KV Human Body Model). 			
			all N-Channel ge P-Channel				
power package	•	az) in one uny c		 High performation on-resistance 	ance trench technology	/ for extremel	y low
					6 package design usin nal and electrical capa		l frame fo
Ţ							
SOT-	23	SuperSOT [™] -6	SuperSOT [™] -8	SO-8	SOT-223	SOIC-	16
	3 pin 1	29	V_{IN} , R_1 4 $V_{ON/OFF}$ 5 R_1 , C_1 6		EQUIV Vout, C1 Notr, C1 Notr, C1 N/OFF	ALENT CIRCUIT	—o out
Sup	erSOT [™] -6	5	See Appl	ication Circuit			
b a a b - 4							
ADSOIU1 Symbol	Parameter	π Katings T_{A}	= 25°C unless otherwise no	ted	FDC6329L		Units

Symbol	Parameter	FDC6329L	Units
V _{IN}	Input Voltage Range (Note 1)	2.5 - 8	V
V _{ON/OFF}	On/Off Voltage Range	1.5 - 8	V
I _L	Load Current - Continuous (Note 2)	2.5	A
	- Pulsed	10	
P _D	Maximum Power Dissipation (Note 2)	0.7	W
Tj,T _{stg}	Operating and Storage Temperature Range	-55 to 150	°C
ESD	Electrostatic Discharge Rating MIL-STD-883D Human Body Model (100pf/1500Ohm)	6	kV
THERMA	L CHARACTERISTICS		
R _{eja}	Thermal Resistance, Junction-to-Ambient (Note 2)	180	°C/W
R _{eJC}	Thermal Resistance, Junction-to-Case (Note 2)	60	°C/W

Electrical Characteristics (T _A = 25°C unless otherwise noted)						
Symbol	Parameter	Conditions	Min	Тур	Max	Units
OFF CHA	RACTERISTICS					•
I _{FL}	Forward Leakage Current	$V_{IN} = 8 V, V_{ONOFF} = 0 V$			1	μA
ON CHAR	ACTERISTICS (Note 3)					
VDROP	Conduction Voltage	$V_{IN} = 5 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}, I_{L} = 2.8 \text{ A}$		0.12	0.2	V
		$V_{IN} = 2.5 \text{ V}, V_{ONOFF} = 3.3 \text{ V}, I_{L} = 1.9 \text{ A}$		0.14	0.2	
R _(ON)	Q2 - Static On-Resistance	$V_{GS} = -5 V, I_{D} = -2.5 A$		0.047	0.07	Ω
		$V_{GS} = -2.5 \text{ V}, \ I_{D} = -2.0 \text{ A}$		0.073	0.105	
I _L	Load Current	$V_{\text{DROP}} = 0.2 \text{ V}, V_{\text{IN}} = 5 \text{ V}, V_{\text{ON/OFF}} = 3.3 \text{ V}$	2.8			А
		$V_{\text{DROP}} = 0.2 \text{ V}, V_{\text{IN}} = 2.5 \text{V}, V_{\text{ONOFF}} = 3.3 \text{ V}$	1.9			

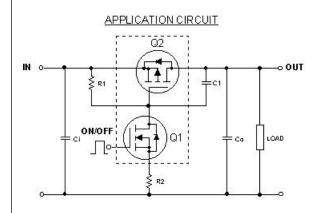
Notes:

1. Range of V_{in} can be up to 8V, but R_1 and R_2 must be scaled such that V_{GS} of Q2 does not exceed -8V.

2. R_{bM} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{bM} is guaranteed by design while R_{bM} is determined by the user's board design.

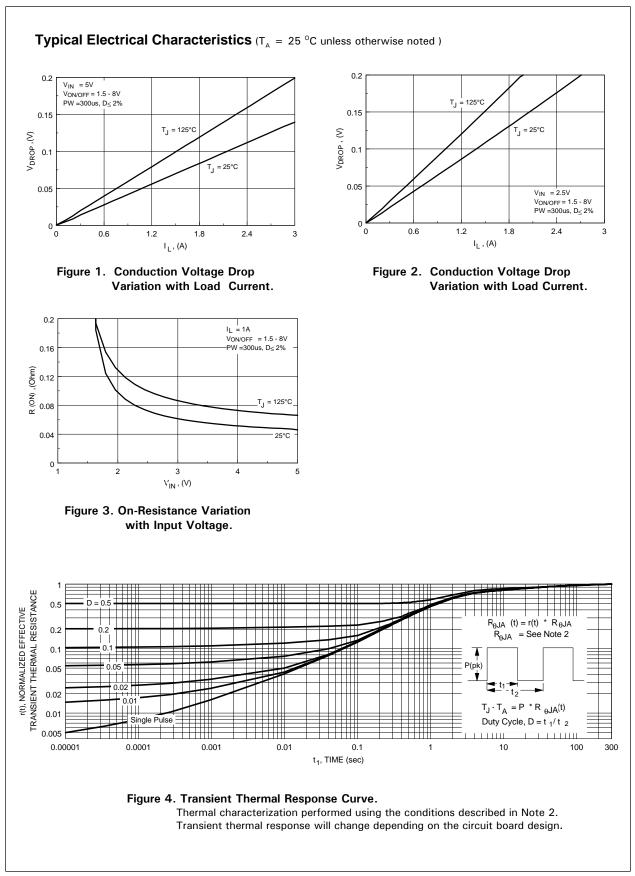
3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2.0%.

FDC6329L Load Switch Application



External Component Recommendation:

For applications where Co \leq 1µF. For slew rate control, select R2 in the range of 1k - 4.7k Ω . For additional in-rush current control, C1 \leq 1000pF can be added. Select R1 so that the R1/R2 ratio ranges from 10 - 100. R1 is required to turn Q2 off.



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