NX5P2924

Logic controlled high-side power switch Rev. 1 — 24 February 2014

Product data sheet

1. **General description**

The NX5P2924 is a high-side load switch which features a low ON resistance N-channel MOSFET with controlled slew rate that supports 2.5 A of continuous current. Designed for operation from 0.8 V to 5.5 V, it is used in power domain isolation applications to reduce power dissipation and extend battery life. The enable logic includes integrated logic level translation making the device compatible with lower voltage processors and controllers. The NX5P2924 is ideal for portable, battery operated applications due to low ground current.

Features and benefits 2.

- Wide supply voltage range from 0.8 V to 5.5 V
- Very low ON resistance:
 - 14 mΩ (typical) at a supply voltage of 1.2 V
 - 14 mΩ (typical) at a supply voltage of 1.8 V
- High noise immunity
- 1.2 V control logic; Integrated EN pull-down resistor
- High current handling capability (2.5 A continuous current)
- Turn-on slew rate limiting
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 4000 V
 - CDM AEC-Q100-011 revision B exceeds 1000 V
- Specified from -40 °C to +85 °C

Applications

- Cell phone
- Digital cameras and audio devices
- Portable and battery-powered equipment



Logic controlled high-side power switch

4. Ordering information

Table 1. Ordering information

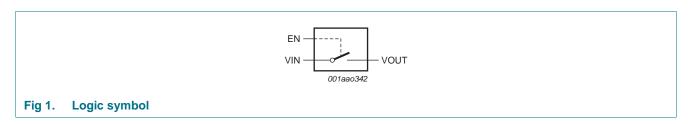
Type number	Package							
	Temperature range	Name	Description	Version				
NX5P2924UK	–40 °C to +85 °C	WLCSP6	wafer level chip-scale package; 6 bumps; 0.87 x 1.37 x 0.5 mm	NX5P2924				

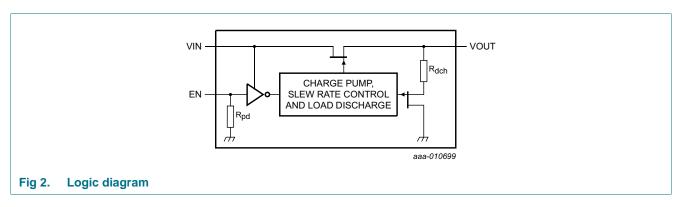
5. Marking

Table 2. Marking codes

Type number	Marking code
NX5P2924UK	24

6. Functional diagram

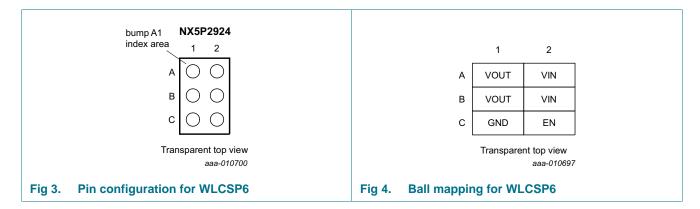




Logic controlled high-side power switch

7. Pinning information

7.1 Pinning



7.2 Pin description

Table 3. Pin description

Symbol	Pin	Description
VIN	A2, B2	input voltage
GND	C1	ground (0 V)
EN	C2	enable input (active HIGH)
VOUT	A1, B1	output voltage

8. Functional description

Table 4. Function table [1]

Input EN	Switch
L	switch OFF
Н	switch ON

^[1] H = HIGH voltage level; L = LOW voltage level.

Logic controlled high-side power switch

9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{I}	input voltage	input EN	[<u>1</u>] -0.5	+6.0	V
		input VIN	[2] -0.5	+6.0	V
V _{SW}	switch voltage	output VOUT	[2] -0.5	$V_{I(VIN)}$	V
I _{IK}	input clamping current	input EN: $V_{I(EN)} < -0.5 \text{ V}$	-50	-	mA
I _{SK}	switch clamping current	input VIN: $V_{I(VIN)} < -0.5 \text{ V}$	-50	-	mA
		output VOUT: $V_{O(VOUT)} < -0.5 \text{ V}$	-50	-	mA
		output VOUT: $V_{O(VOUT)} > V_{I(VIN)} + 0.5 \text{ V}$	-	50	mA
I _{SW}	switch current	V _{SW} > -0.5 V	-	±2500	mA
T _{j(max)}	maximum junction temperature		-40	+125	°C
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation		<u>[3]</u> _	470	mW

^[1] The minimum input voltage rating may be exceeded if the input current rating is observed.

10. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V_{I}	input voltage		8.0	5.5	V
T _{amb}	ambient temperature		-40	+85	°C

^[2] The minimum and maximum switch voltage ratings may be exceeded if the switch clamping current rating is observed.

^[3] The (absolute) maximum power dissipation depends on the junction temperature T_j. Higher power dissipation is allowed with lower ambient temperatures. The conditions to determine the specified values are T_{amb} = 85 °C and the use of a two layer PCB.

Logic controlled high-side power switch

11. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		<u>[1]</u> 139	K/W

^[1] R_{th(j-a)} is dependent upon board layout. To minimize R_{th(j-a)}, ensure that all pins have a solid connection to larger copper layer areas. In multi-layer PCBs, the second layer should be used to create a large heat spreader area below the device. Avoid using solder-stop varnish under the device.

12. Static characteristics

Table 8. Static characteristics

 $V_{I(VIN)} = 1.0 \text{ V}$ to 5.5 V, unless otherwise specified; Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Ta	_{mb} = 25	°C	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}$		Unit	
			Min	Typ[1]	Max	Min	Max		
	HIGH-level input	EN input; V _{I(VIN)} = 0.8 V	-	0.7	-	-	-	V	
	voltage	EN input; $V_{I(VIN)} = 1.0 \text{ V}$ to 1.2 V	0.9	-	-	0.9	-	V	
		EN input; $V_{I(VIN)} = 1.2 \text{ V}$ to 2.5 V	1.2	-	-	1.2	-	V	
		EN input; $V_{I(VIN)} = 2.5 \text{ V to } 5.5 \text{ V}$	1.2	-	-	1.2	-	V	
V _{IL}	LOW-level input	EN input; $V_{I(VIN)} = 0.8 \text{ V}$	-	0.25	-	-	-	V	
	voltage	EN input; $V_{I(VIN)} = 1.0 \text{ V}$ to 1.2 V	-	-	0.3	-	0.3	V	
		EN input; $V_{I(VIN)} = 1.2 \text{ V}$ to 2.5 V	-	-	0.4	-	0.4	V	
		EN input; $V_{I(VIN)} = 2.5 \text{ V}$ to 5.5 V	-	-	0.6	-	0.6	V	
R _{dch}	discharge	VOUT output; V _{I(VIN)} = 0.8 V	-	4.00	-	-	-	kΩ	
	resistance	VOUT output; V _{I(VIN)} = 1.0 V	-	1.40	-	-	-	kΩ	
		VOUT output; V _{I(VIN)} = 1.2 V	-	1.30	-	-	-	kΩ	
		VOUT output; V _{I(VIN)} = 1.8 V	-	1.27	1.50	-	-	kΩ	
		VOUT output; $V_{I(VIN)} = 3.3 \text{ V}$	-	1.25	1.50	-	-	kΩ	
		VOUT output; $V_{I(VIN)} = 5.5 \text{ V}$	-	1.25	1.50	-	-	kΩ	
I _(VIN)	supply current	VOUT open							
		EN = HIGH; $V_{I(VIN)}$ = 1.0 V; see Figure 5 and Figure 6	-	25	-	-	35	μΑ	
		EN = HIGH; $V_{I(VIN)}$ = 1.8 V; see Figure 5 and Figure 6	-	30	-	-	50	μΑ	
		EN = HIGH; $V_{I(VIN)}$ = 3.6 V; see Figure 5 and Figure 6	-	45	-	-	65	μΑ	
		EN = HIGH; $V_{I(VIN)}$ = 5.5 V; see Figure 5 and Figure 6	-	75	-	-	105	μΑ	
		EN = LOW; $V_{I(VIN)}$ = 1.0 V; see Figure 7 and Figure 8	-	0.1	-	-	8.0	μΑ	
		EN = LOW; $V_{I(VIN)}$ = 1.8 V; see Figure 7 and Figure 8	-	0.1	-	-	1.0	μΑ	
		EN = LOW; $V_{I(VIN)}$ = 3.6 V; see Figure 7 and Figure 8	-	0.1	-	-	1.2	μΑ	
		EN = LOW; $V_{I(VIN)}$ = 5.5 V; see Figure 7 and Figure 6	-	0.1	-	-	1.5	μΑ	

Logic controlled high-side power switch

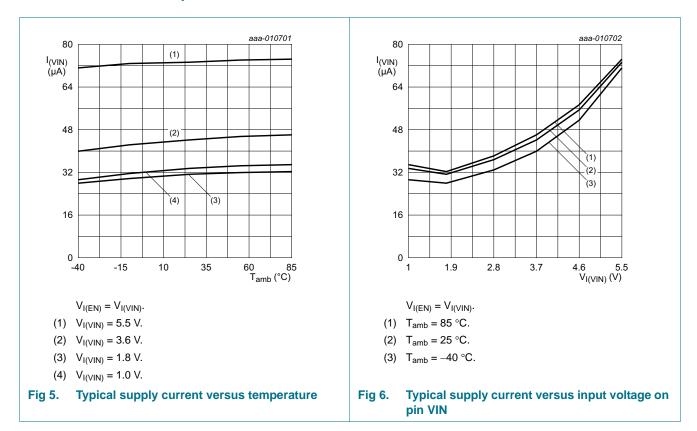
Table 8. Static characteristics ...continued

 $V_{I(VIN)} = 1.0 \text{ V to } 5.5 \text{ V}$, unless otherwise specified; Voltages are referenced to GND (ground = 0 V). ...continued

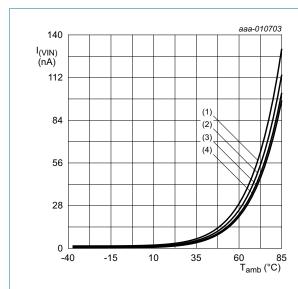
Symbol	Parameter	Conditions	T _{amb} = 25 °C		$T_{amb} = -40 ^{\circ}C$	to +85 °C	Unit	
			Min	Typ[1]	Max	Min	Max	
$I_{S(OFF)}$	OFF-state leakage current	EN = LOW; $V_{I(VIN)} = 1.8 \text{ V}$; $V_{I(VOUT)} = 0 \text{ V}$; see Figure 9 and 10	-	-0.5	-	-3.5	-	μΑ
		EN = LOW; $V_{I(VIN)}$ = 3.6 V; $V_{I(VOUT)}$ = 0 V; see <u>Figure 9</u> and <u>10</u>	-	-0.5	-	-5.0	-	μΑ
		EN = LOW; $V_{I(VIN)}$ = 5.5 V; $V_{I(VOUT)}$ = 0 V; see <u>Figure 9</u> and <u>10</u>	-	-0.5	-	−7.5	-	μΑ
R_{pd}	pull-down resistance	EN input	-	220	-	160	330	kΩ
Cı	input capacitance	EN	-	3	-	-	-	pF
C _{S(ON)}	ON-state capacitance	VIN; VOUT	-	-	0.5	-	0.5	nF

^[1] All typical values are measured at $V_{I(VIN)}$ = 3.6 V and T_{amb} = 25 °C unless otherwise specified.

12.1 Graphs



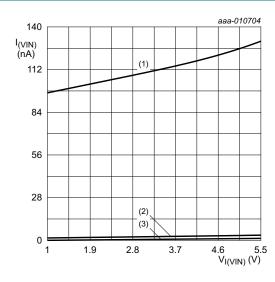
Logic controlled high-side power switch



$$V_{I(EN)} = GND.$$

- (1) $V_{I(VIN)} = 5.5 \text{ V}.$
- (2) $V_{I(VIN)} = 3.6 \text{ V}.$
- (3) $V_{I(VIN)} = 1.8 \text{ V}.$
- (4) $V_{I(VIN)} = 1.0 \text{ V}.$

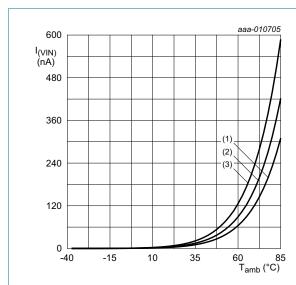
Fig 7. Typical supply current versus temperature



$$V_{I(EN)} = GND.$$

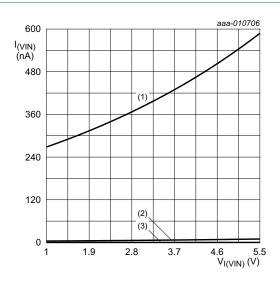
- (1) $T_{amb} = 85 \,^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -40 \, ^{\circ}C$.

Fig 8. Typical supply current versus input voltage on pin VIN



- (1) $V_{I(VIN)} = 1.8 \text{ V}.$
- (2) $V_{I(VIN)} = 3.6 \text{ V}.$
- (3) $V_{I(VIN)} = 5.5 \text{ V}.$

Fig 9. Typical OFF-state leakage current versus temperature



- (1) $T_{amb} = 85 \, ^{\circ}C$.
- (2) $T_{amb} = 25 \, ^{\circ}C$.
- (3) $T_{amb} = -40 \, ^{\circ}C$.

Fig 10. Typical OFF-state leakage current versus input voltage on pin VIN

Logic controlled high-side power switch

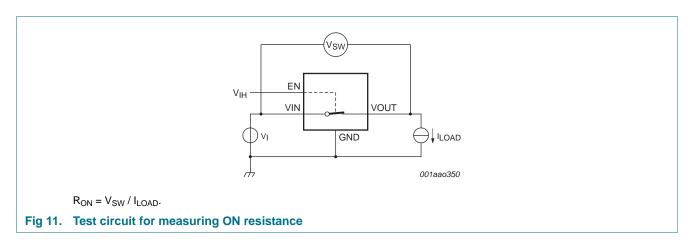
12.2 ON resistance

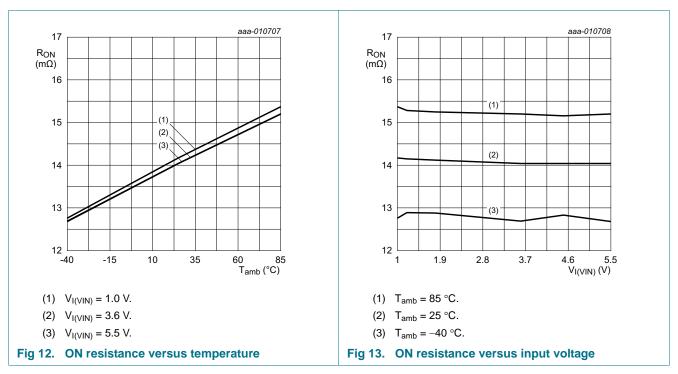
Table 9. ON resistance

At recommended operating conditions; voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions	T _{amb} = 25 °C		$T_{amb} = -40$	Unit		
			Min	Тур	Max	Min	Max	
R _{ON}	ON resistance	$V_{I(EN)}$ = 1.5 V; I_{LOAD} = 200 mA; see <u>Figure 11</u> , <u>Figure 12</u> and <u>Figure 13</u>						
		$V_{I(VIN)} = 0.8 \text{ V to } 5.5 \text{ V}$	-	14	-	-	20	mΩ

12.3 ON resistance test circuit and graphs





Logic controlled high-side power switch

13. Dynamic characteristics

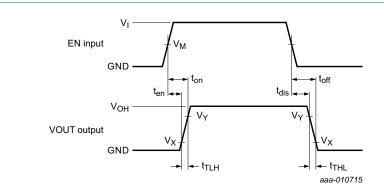
Table 10. Dynamic characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V); for test circuit, see Figure 15.

Symbol	Parameter	eter Conditions	Ta	_{imb} = 25	°C	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}$		Unit
4			Min	Тур	Max	Min	Max	
t _{en}	enable time	EN to VOUT; see <u>Figure 14</u> , <u>16</u> , <u>17</u> , <u>18</u> and <u>20</u>						
		$V_{I(VIN)} = 0.8 V$	-	600	-	-	-	μS
		$V_{I(VIN)} = 1.0 V$	-	240	-	-	-	μS
		V _{I(VIN)} = 3.6 V	-	90	-	-	-	μS
		$V_{I(VIN)} = 5.5 V$	-	90	-	-	-	μS
t _{dis}	disable time	EN to VOUT; see Figure 14, 19 and 21						
		$V_{I(VIN)} = 0.8 V$	-	210	-	-	-	μS
		$V_{I(VIN)} = 1.0 V$	-	20	-	-	-	μS
		$V_{I(VIN)} = 3.6 \text{ V}$	-	5	-	-	-	μS
		$V_{I(VIN)} = 5.5 V$	-	4	-	-	-	μS
t _{on}	turn-on time	EN to VOUT; see <u>Figure 14</u> , <u>16</u> , <u>17</u> , <u>18</u> and <u>20</u>						
		$V_{I(VIN)} = 0.8 V$	-	1000	-	-	-	μS
		$V_{I(VIN)} = 1.0 \text{ V}$	-	350	-	-	-	μS
		$V_{I(VIN)} = 3.6 \text{ V}$	-	240	-	-	-	μS
		$V_{I(VIN)} = 5.5 V$	-	290	-	-	-	μS
t _{off}	turn-off time	EN to VOUT; see Figure 14, 19 and 21						μS
		$V_{I(VIN)} = 0.8 V$	-	220.0	-	-	-	μS
		$V_{I(VIN)} = 1.0 V$	-	22.3	-	-	-	μS
		V _{I(VIN)} = 3.6 V	-	7.2	-	-	-	μS
		$V_{I(VIN)} = 5.5 V$	-	6.0	-	-	-	μS
t _{TLH}	LOW to HIGH	VOUT; see Figure 14						
	output transition time	$V_{I(VIN)} = 0.8 V$	-	400	-	-	-	μS
	transition time	$V_{I(VIN)} = 1.0 V$	-	110	-	20	-	μS
		V _{I(VIN)} = 3.6 V	-	150	-	50	-	μS
		$V_{I(VIN)} = 5.5 V$	-	200	-	70	-	μS
t _{THL}	HIGH to LOW	VOUT; see Figure 14						
	output transition time	$V_{I(VIN)} = 0.8 \text{ V}$	-	10.0	-	-	-	μS
	uansiuon ume	$V_{I(VIN)} = 1.0 \text{ V}$	-	2.3	-	-	-	μS
		$V_{I(VIN)} = 3.6 \text{ V}$	-	2.2	-	-	-	μS
		$V_{I(VIN)} = 5.5 \text{ V}$	-	2.0	-	-	-	μS

Logic controlled high-side power switch

13.1 Waveforms, graphs and test circuit



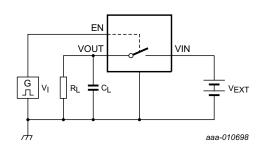
Measurement points are given in Table 11.

Logic level: $V_{\mbox{\scriptsize OH}}$ is the typical output voltage that occurs with the output load.

Fig 14. Switching times

Table 11. Measurement points

Supply voltage	EN Input	Output	
V _{I(VIN)}	V _M	V _X	V _Y
1.0 V to 5.5 V	$0.5 \times V_{I(EN)}$	$0.1 \times V_{OH}$	$0.9 \times V_{OH}$



Test data is given in Table 12.

Definitions test circuit:

R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

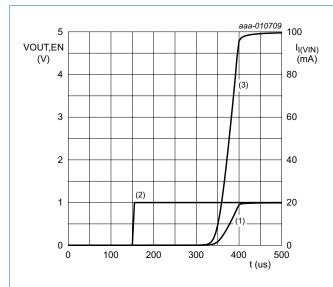
 V_{EXT} = External voltage for measuring switching times.

Fig 15. Test circuit for measuring switching times

Table 12. Test data

Supply voltage	Input	Load	
V _{EXT}	V _{I(EN)}	CL	R _L
1.0 V to 5.5 V	1.5 V	0.1 μF	10 Ω

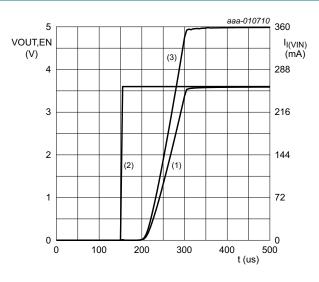
Logic controlled high-side power switch



 $V_{I(VIN)}$ = 1 V; R_L = 10 $\Omega;$ C_L = 0.1 $\mu F;$ T_{amb} = 25 $^{\circ}C.$

- (1) VOUT
- (2) EN
- (3) $I_{I(VIN)}$

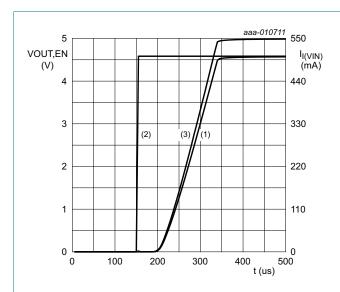
Fig 16. Typical enable time at $V_{I(VIN)}$ = 1 V; C_L = 0.1 μF



 $V_{I(VIN)}$ = 3.6 V; R_L = 10 $\Omega;$ C_L = 0.1 $\mu F;$ T_{amb} = 25 $^{\circ}C.$

- (1) VOUT
- (2) EN
- (3) I_{I(VIN)}

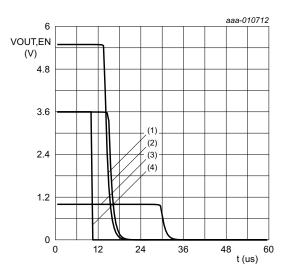
Fig 17. Typical enable time at $V_{I(VIN)}$ = 3.6 V; C_L = 0.1 μF



 $V_{I(VIN)}$ = 5.5 V; R_L = 10 $\Omega;$ C_L = 0.1 $\mu F;$ T_{amb} = 25 °C.

- (1) VOUT
- (2) EN
- (3) I_{I(VIN)}

Fig 18. Typical enable time at $V_{I(VIN)}$ = 5.5 V; C_L = 0.1 μF

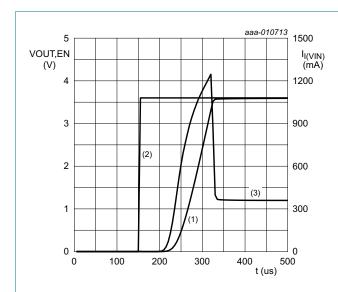


$$R_L = 10 \Omega$$
; $C_L = 0.1 \mu F$; $T_{amb} = 25 °C$

- (1) $V_{I(VIN)} = 5.5 \text{ V}$
- (2) $V_{I(VIN)} = 3.6 \text{ V}$
- (3) $V_{I(VIN)} = 1.0 \text{ V}$
- (4) EN

Fig 19. Typical disable time

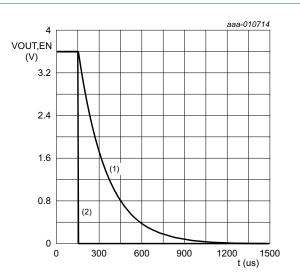
Logic controlled high-side power switch



 $V_{I(VIN)}$ = 3.6 V; R_L = 10 $\Omega;$ C_L = 20 $\mu F;$ T_{amb} = 25 $^{\circ}C.$

- (1) VOUT
- (2) EN
- (3) I_{I(VIN)}

Fig 20. Typical enable time at $V_{I(VIN)}$ = 3.6 V; C_L = 20 μF



 $V_{I(VIN)} = 3.6 \text{ V}; \text{ R}_L = 10 \Omega; \text{ C}_L = 20 \mu\text{F}; \text{ T}_{amb} = 25 \text{ }^{\circ}\text{C}$

- (1) VOUT
- (2) EN

Fig 21. Typical disable time at $V_{I(VIN)}$ = 3.6 V; C_L = 20 μF

Logic controlled high-side power switch

14. Package outline

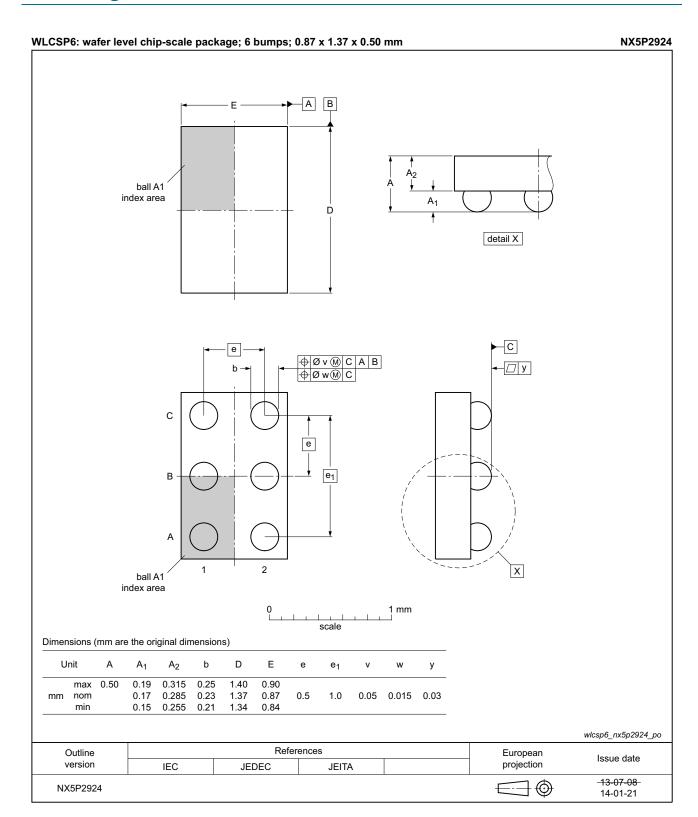


Fig 22. Package outline NX5P2924

Logic controlled high-side power switch

15. Abbreviations

Table 13. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
IEC	International Electrotechnical Commission
MOSFET	Metal-Oxide Semiconductor Field Effect Transistor

16. Revision history

Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
NX5P2924 v.1	20140224	Product data sheet	-	-

Logic controlled high-side power switch

17. Legal information

17.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

17.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

17.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

NX5P2924

Logic controlled high-side power switch

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

17.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

18. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

NX5P2924 **NXP Semiconductors**

Logic controlled high-side power switch

19. Contents

1	General description
2	Features and benefits
3	Applications
4	Ordering information
5	Marking 2
6	Functional diagram 2
7	Pinning information 3
7.1	Pinning
7.2	Pin description
8	Functional description 3
9	Limiting values 4
10	Recommended operating conditions 4
11	Thermal characteristics 5
12	Static characteristics 5
12.1	Graphs 6
12.2	ON resistance 8
12.3	ON resistance test circuit and graphs 8
13	Dynamic characteristics 9
13.1	Waveforms, graphs and test circuit 10
14	Package outline
15	Abbreviations
16	Revision history
17	Legal information
17.1	Data sheet status
17.2	Definitions
17.3	Disclaimers
17.4	Trademarks 16
18	Contact information 16
19	Contents

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.



OOO «ЛайфЭлектроникс" "LifeElectronics" LLC

ИНН 7805602321 КПП 780501001 P/C 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



Тел: +7 (812) 336 43 04 (многоканальный) Email: org@lifeelectronics.ru