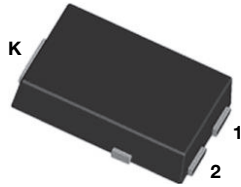
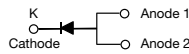


# High Current Density Surface Mount Trench MOS Barrier Schottky Rectifier

 Ultra Low  $V_F = 0.50$  V at  $I_F = 4$  A

**TMBS® eSMP® Series**

**TO-277A (SMPC)**

**FEATURES**

- Very low profile - typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available  
- Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

 AUTOMOTIVE  
GRADE  
Available

**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**
**TYPICAL APPLICATIONS**

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

**MECHANICAL DATA**
**Case:** TO-277A (SMPC)

 Molding compound meets UL 94 V-0 flammability rating  
Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	8 A
$V_{RRM}$	100 V
$I_{FSM}$	120 A
$V_F$ at $I_F = 8$ A (125 °C)	0.62 V
$T_J$ max.	175 °C
Package	TO-277A (SMPC)
Diode variations	Single die

MAXIMUM RATINGS ( $T_A = 25$ °C unless otherwise noted)			
PARAMETER	SYMBOL	V8PM10S	UNIT
Device marking code		8M10S	
Maximum repetitive peak reverse voltage	$V_{RRM}$	100	V
Maximum DC forward current	$I_{F(AV)}$ <sup>(1)</sup>	8	A
	$I_{F(AV)}$ <sup>(2)</sup>	3.5	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	$I_{FSM}$	120	A
Operating junction and storage temperature range	$T_J$ <sup>(3)</sup> , $T_{STG}$	-40 to +175	°C

**Notes**

- (1) Mounted on 30 mm x 30 mm pad areas aluminum PCB
- (2) Free air, mounted on recommended pad area
- (3) The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$



<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	$I_F = 4\text{ A}$	$T_A = 25\text{ }^\circ\text{C}$	$V_F^{(1)}$	0.58	-	V
	$I_F = 8\text{ A}$			0.70	0.78	
	$I_F = 4\text{ A}$	$T_A = 125\text{ }^\circ\text{C}$		0.50	-	
	$I_F = 8\text{ A}$			0.62	0.70	
Reverse current	$V_R = 70\text{ V}$	$T_A = 25\text{ }^\circ\text{C}$	$I_R^{(2)}$	0.01	-	mA
		$T_A = 125\text{ }^\circ\text{C}$		2	-	
	$V_R = 100\text{ V}$	$T_A = 25\text{ }^\circ\text{C}$		-	0.2	
		$T_A = 125\text{ }^\circ\text{C}$		4	10	
Typical junction capacitance	4.0 V, 1 MHz		$C_J$	860	-	pF

**Notes**

- (1) Pulse test: 300  $\mu\text{s}$  pulse width, 1 % duty cycle  
(2) Pulse test: Pulse width  $\leq 5\text{ ms}$

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25\text{ }^\circ\text{C}$ unless otherwise specified)			
PARAMETER	SYMBOL	V8PM10S	UNIT
Typical thermal resistance	$R_{\theta JA}^{(1)(2)}$	80	$^\circ\text{C/W}$
	$R_{\theta JM}^{(3)}$	4	

**Notes**

- (1) The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$   
(2) Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient  
(3) Units mounted on 30 mm x 30 mm aluminum PCB, thermal resistance  $R_{\theta JM}$  - junction to mount

<b>ORDERING INFORMATION</b> (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
V8PM10S-M3/H	0.10	H	1500	7" diameter plastic tape and reel
V8PM10S-M3/I	0.10	I	6500	13" diameter plastic tape and reel
V8PM10SHM3/H <sup>(1)</sup>	0.10	H	1500	7" diameter plastic tape and reel
V8PM10SHM3/I <sup>(1)</sup>	0.10	I	6500	13" diameter plastic tape and reel

**Note**

- (1) AEC-Q101 qualified

**RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25\text{ }^\circ\text{C}$  unless otherwise specified)

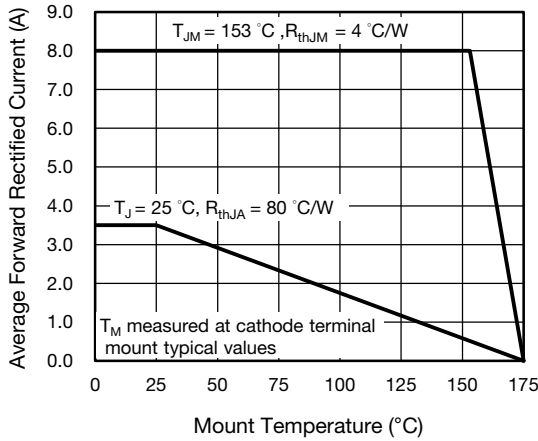


Fig. 1 - Maximum Forward Current Derating Curve

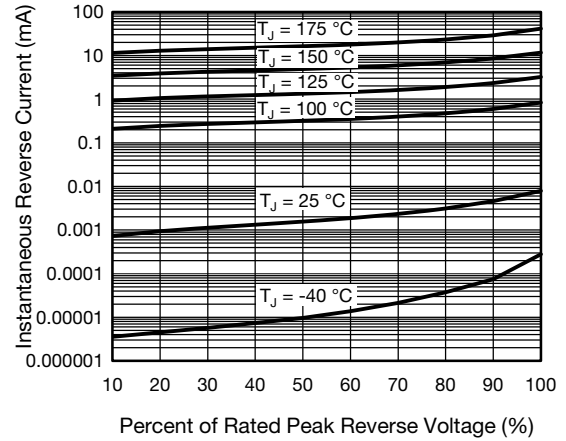


Fig. 4 - Typical Reverse Characteristics

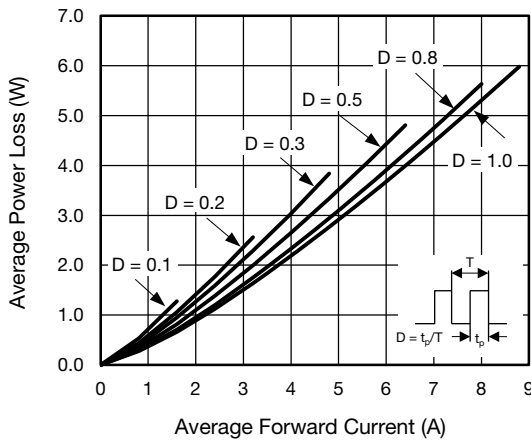


Fig. 2 - Forward Power Loss Characteristics

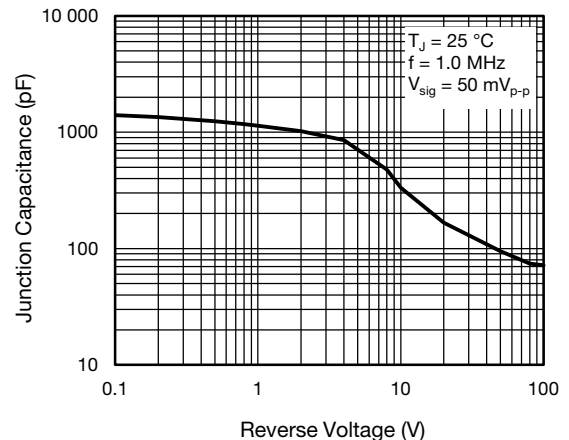


Fig. 5 - Typical Junction Capacitance

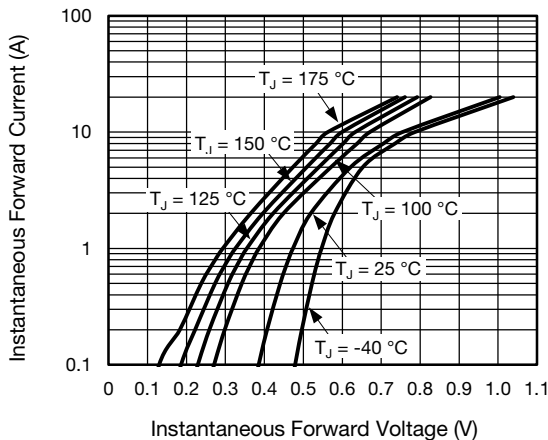


Fig. 3 - Typical Instantaneous Forward Characteristics

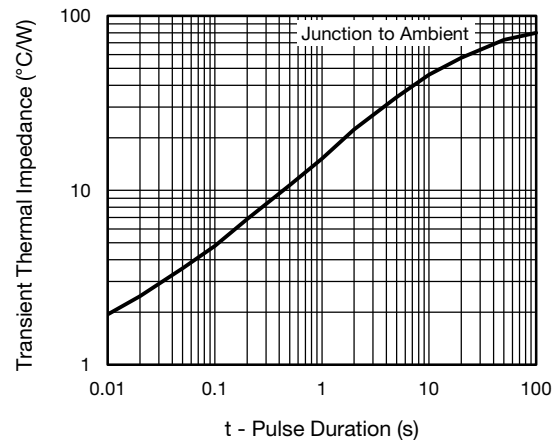
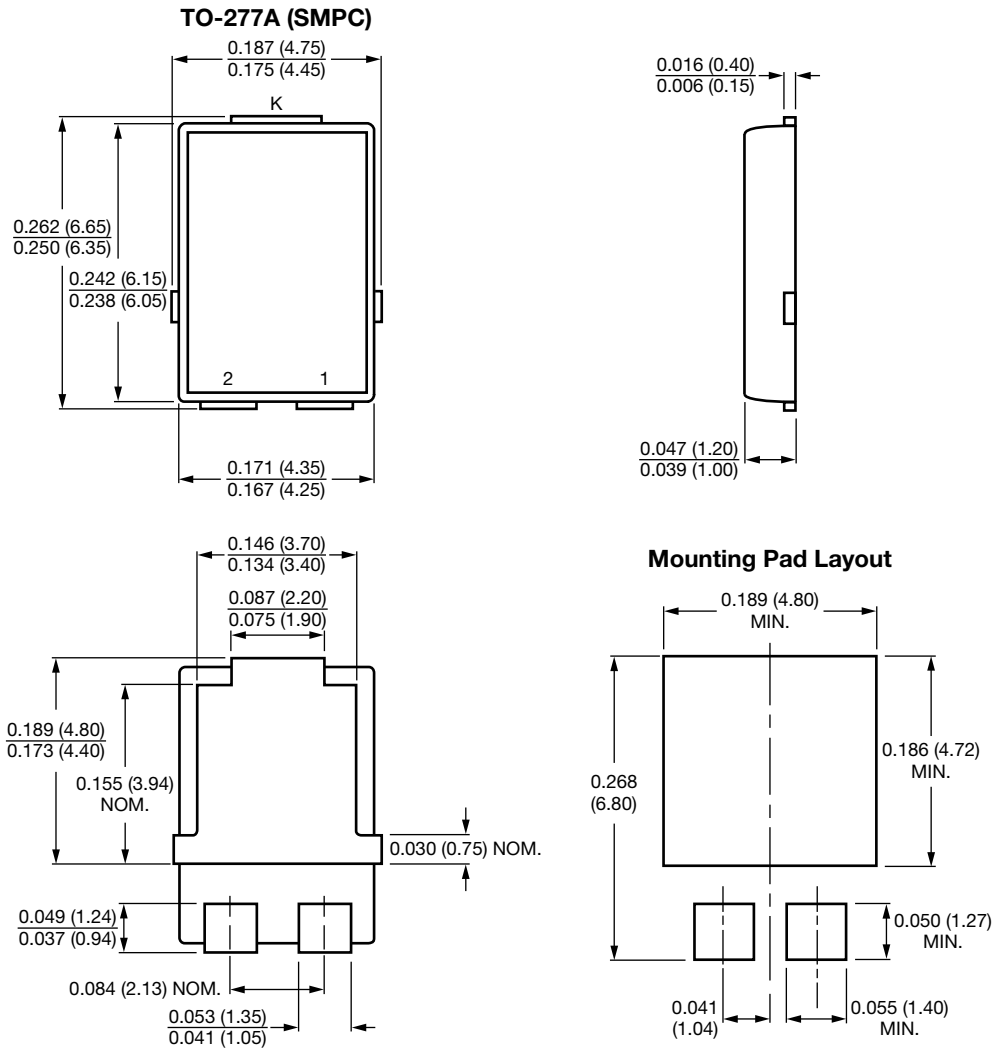


Fig. 6 - Typical Transient Thermal Impedance



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



Conform to JEDEC® TO-277A



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- Техническую поддержку проекта.
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- Изготовление тестовой платы монтаж и пусконаладочные работы.



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