

MBRAF360T3G

Surface Mount Schottky Power Rectifier

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system.

Features

- Low Profile Package for Space Constrained Applications
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- 150°C Operating Junction Temperature
- Guard-Ring for Stress Protection
- These are Pb-Free and Halide-Free Devices

Mechanical Characteristics

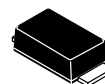
- Case: Epoxy, Molded, Epoxy Meets UL 94, V-0
- Weight: 95 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Cathode Polarity Band
- Device Meets MSL 1 Requirements
- ESD Ratings: Machine Model = C
Human Body Model = 3B



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SCHOTTKY BARRIER RECTIFIER 3.0 AMPERE 60 VOLTS



SMA-FL
CASE 403AA
STYLE 6

MARKING DIAGRAM



RAH = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MBRAF360T3G	SMA-FL (Pb-Free)	5000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MBRAF360T3G

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	60	V
Average Rectified Forward Current	$I_{F(AV)}$	3.0 @ $T_L = 100^\circ\text{C}$ 4.0 @ $T_L = 80^\circ\text{C}$	A
Peak Repetitive Forward Current (Rated V_R , Square Wave, 20 kHz) $T_C = 125^\circ\text{C}$	I_{FRM}	6	A
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I_{FSM}	125	A
Storage Temperature Range	T_{stg}	- 65 to +150	$^\circ\text{C}$
Operating Junction Temperature (Note 1)	T_J	- 65 to +150	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 2)	$R_{\theta JL}$	25	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	90	$^\circ\text{C/W}$

2. 1 inch square pad size (1 x 0.5 inch for each lead) on FR4 board.

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 3) ($I_F = 3.0\text{ A}$, $T_J = 25^\circ\text{C}$)	V_F	0.740	V
Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage, $T_J = 25^\circ\text{C}$) (Rated dc Voltage, $T_J = 100^\circ\text{C}$)	i_R	0.15 10	mA

3. Pulse Test: Pulse Width = 300 μs , Duty Cycle $\leq 2.0\%$.

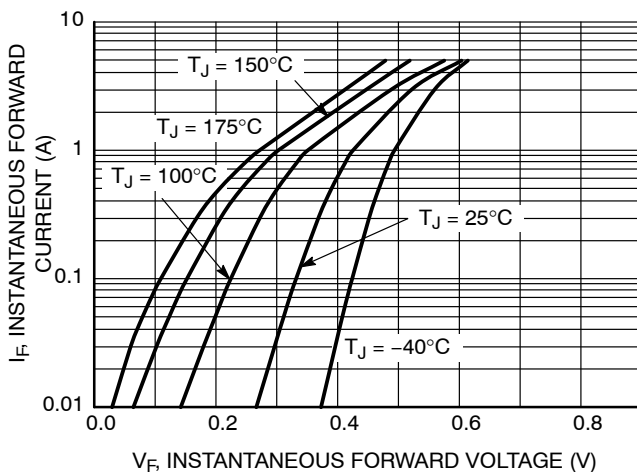


Figure 1. Typical Forward Voltage

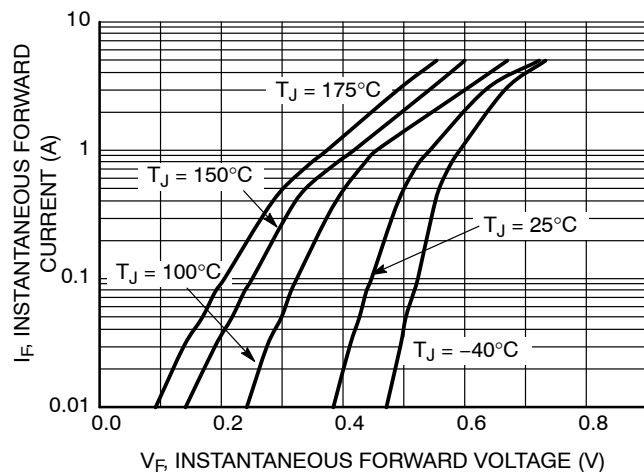


Figure 2. Maximum Forward Voltage

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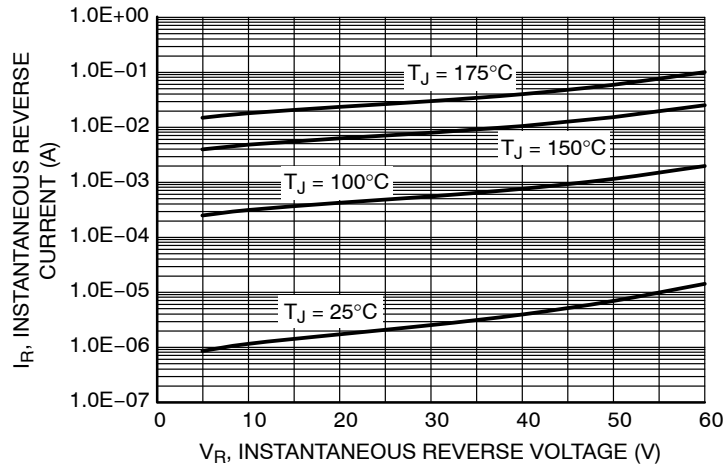


Figure 3. Typical Reverse Current

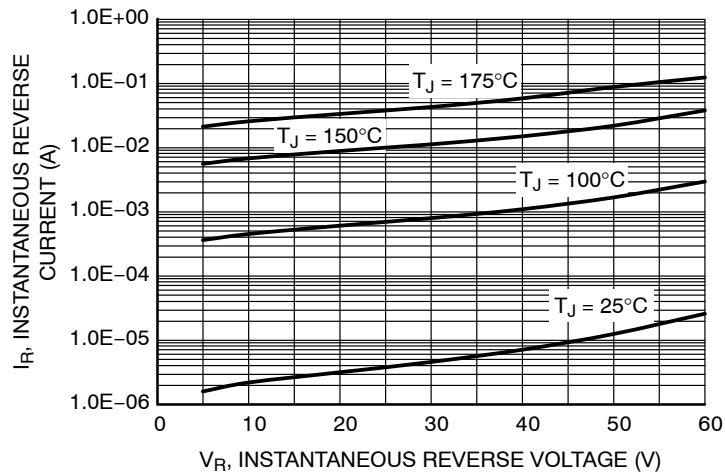


Figure 4. Maximum Reverse Current

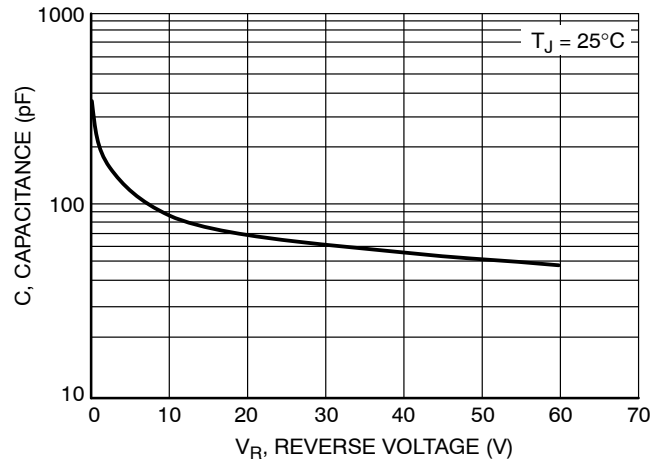


Figure 5. Typical Capacitance

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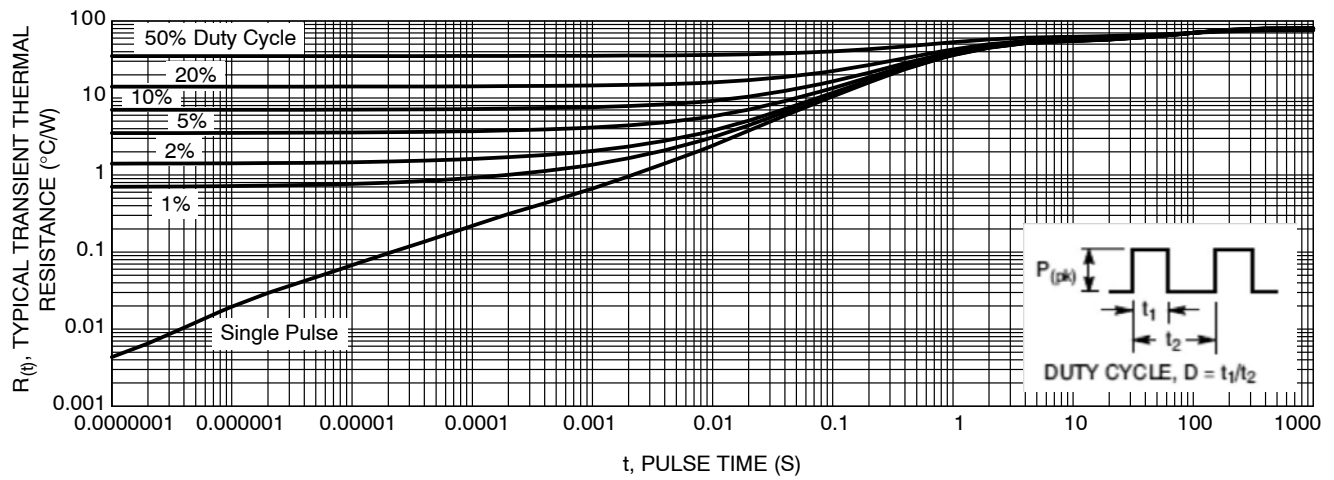
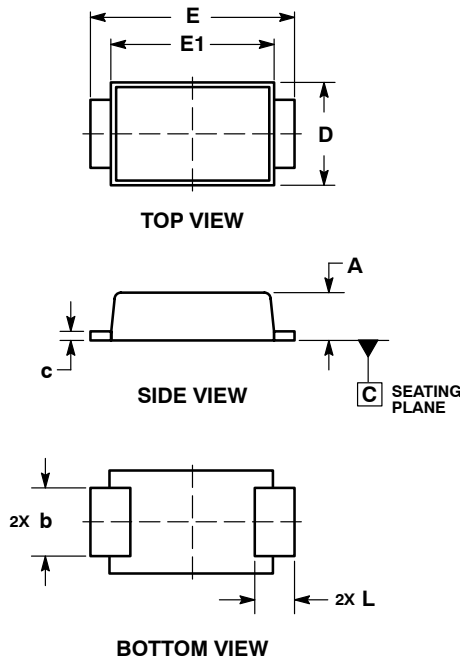


Figure 6. Typical Transient Thermal Response, Junction-to-Ambient

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PACKAGE DIMENSIONS

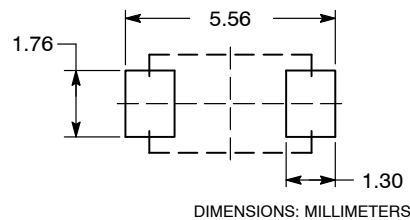
SMA-FL
CASE 403AA
ISSUE O



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.

MILLIMETERS		
DIM	MIN	MAX
A	0.90	1.10
b	1.25	1.65
c	0.15	0.30
D	2.40	2.80
E	4.80	5.40
E1	4.00	4.60
L	0.70	1.10

RECOMMENDED SOLDER FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

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Тел: +7 (812) 336 43 04 (многоканальный)

Email: org@lifeelectronics.ru

www.lifeelectronics.ru