

NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

Parameter	Value
V _{CC}	50V
I _{C(MAX.)}	100mA
R ₁	47kΩ
R ₂	47kΩ

Features

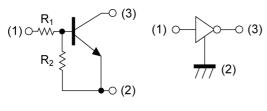
- 1) Built-In Biasing Resistors, $R_1 = R_2 = 47k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA144E series

Application

INVERTER, INTERFACE, DRIVER

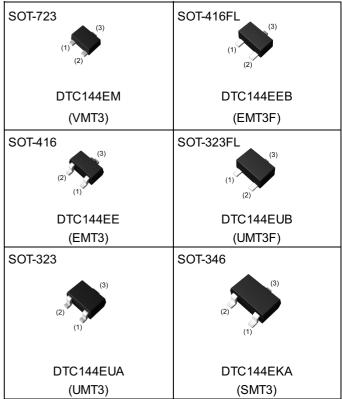
•Inner circuit

DTC144EM/ DTC144EEB/ DTC144EUB

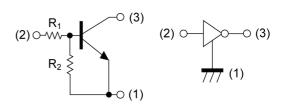


- (1) IN (BASE)
- (2) GND (EMITTER)
- (3) OUT (COLLECTOR)

Outline



DTC144EE/ DTC144EUA/ DTC144EKA



- (1) GND (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC144EM	SOT-723	1212	T2L	180	8	8000	26
DTC144EEB	SOT-416FL	1616	TL	180	8	3000	26
DTC144EE	SOT-416	1616	TL	180	8	3000	26
DTC144EUB	SOT-323FL	2021	TL	180	8	3000	26
DTC144EUA	SOT-323	2021	T106	180	8	3000	26
DTC144EKA	SOT-346	2928	T146	180	8	3000	26

● **Absolute maximum ratings** (T_a = 25°C)

Pa	rameter	Symbol	Values	Unit
Supply voltage	V_{CC}	50	V	
Input voltage	Input voltage			V
Output current	Output current			mA
Collector current			100	mA
	DTC144EM		150	mW
	DTC144EEB		150	
Davies dissination	DTC144EE	D *2	150	
Power dissipation	DTC144EUB	P _D *2	200	
	DTC144EUA		200	
	DTC144EKA		200	
Junction temperature		T _j	150	°C
Range of storage temperate	ure	T _{stg}	-55 to +150	°C

• Electrical characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol	Conditions		Values		Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offit
Innut voltore	$V_{I(off)}$	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.5	V
Input voltage	V _{I(on)}	$V_{O} = 0.3V, I_{O} = 2mA$	3.0	-	-	V
Output voltage	V _{O(on)}	I _O = 10mA, I _I = 0.5mA	1	100	300	mV
Input current	I _I	V _I = 5V	1	-	180	μA
Output current	I _{O(off)}	$V_{CC} = 50V, V_{I} = 0V$	1	-	500	nA
DC current gain	G _I	$V_{O} = 5V, I_{O} = 5mA$	68	-	-	-
Input resistance	R ₁	-	32.9	47	61.1	kΩ
Resistance ratio	R ₂ /R ₁	-	0.8	1.0	1.2	-
Transition frequency	f _T *1	$V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz	1	250	-	MHz

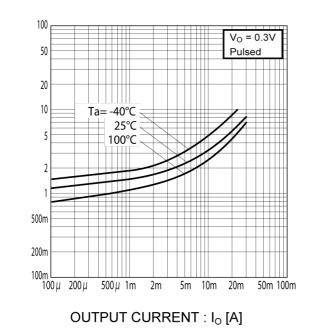
^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land.

INPUT VOLTAGE: V_{I(on)} [V]

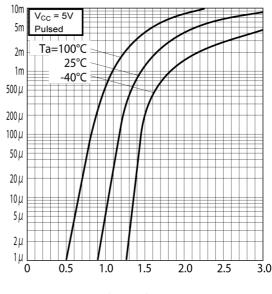
● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)



OUTPUT CURRENT : I_o [A]

Fig.2 Output current vs. input voltage (OFF characteristics)



INPUT VOLTAGE : V_{I(off)} [V]

Fig.3 Output current vs. output voltage

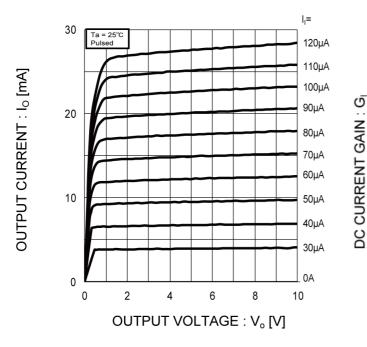
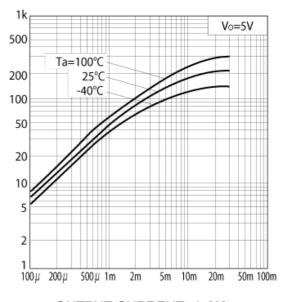


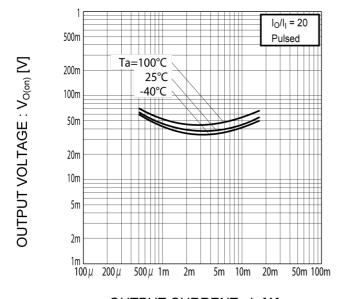
Fig.4 DC current gain vs. output current



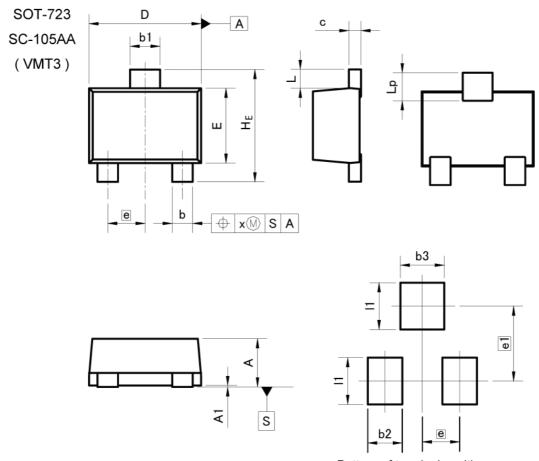
OUTPUT CURRENT: Io [A]

●Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]



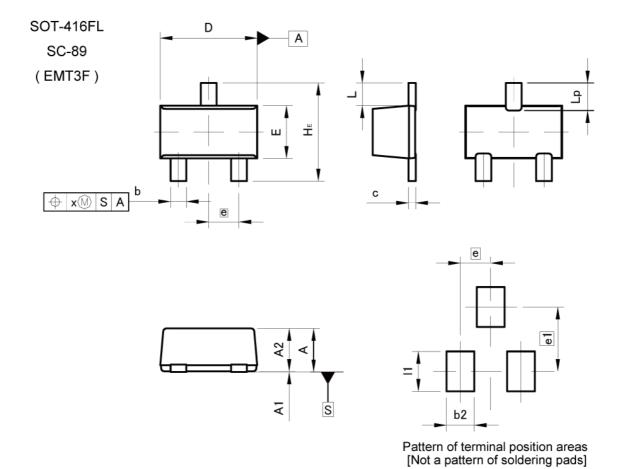
Pattern o	f termina	al position	areas
[Not a pa	attern of	soldering	pads]

DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	0.45	0.55	0.018	0.022	
A1	0.00	0.10	0.000	0.004	
b	0.17	0.27	0.007	0.011	
b1	0.27	0.37	0.011	0.015	
С	0.08	0.18	0.003	0.007	
D	1.10	1.30	0.043	0.051	
E	0.70	0.90	0.028	0.035	
е	0.40 0.		02		
HE	1.10	1.30	0.043	0.051	
L	0.10	0.30	0.004	0.012	
Lp	0.20	0.40	0.008	0.016	
х	-	0.10	_	0.004	

DIM	MILIM	ETERS	INCHES	
	MIN	MAX	MIN	MAX
b2	-	0.37	_	0.015
b3	_	0.47	7-	0.019
e1	0.80		0.0	31
11	=	0.50		0.020

Dimension in mm/inches



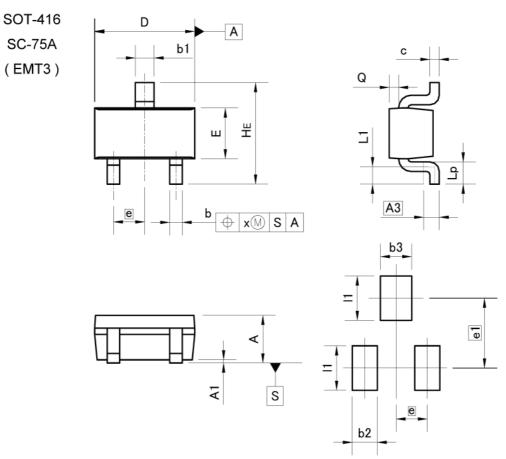


DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.65	0.85	0.026	0.033
A1	0.00	0.10	0.000	0.004
A2	0.60	0.80	0.024	0.031
b	0.21	0.36	0.008	0.014
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	0.76	0.96	0.030	0.038
е	0.	50	0.020	
HE	1.50	1.70	0.059	0.067
L	0.37		0.0	15
Lp	0.35	0.55	0.014	0.022
Х	<u> </u>	0.10	-	0.004

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	-	0.46	-	0.018
e1	-	1.05	-	0.041
11	-	0.65	-	0.026

Dimension in mm/inches





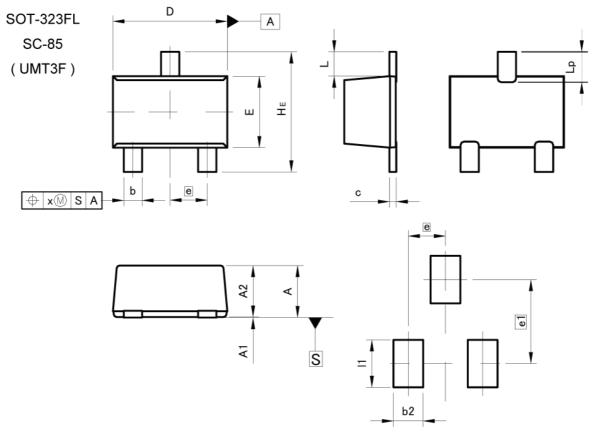
Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.60	0.80	0.024	0.031
A1	0.00	0.10	0.000	0.004
A3	0.	25	0.0	10
b	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.010	0.016
С	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
е	0.	50	0.020	
HE	1.40	1.80	0.055	0.071
L1	0.10	-	0.004	-
Lp	0.15		0.006	a -
Q	0.05	0.25	0.002	0.010
х	1-	0.10	, - ,	0.004

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	1	0.40	-	0.016
b3	I	0.50	-	0.020
e1	1.10		0.0	43
l1		0.70		0.028

Dimension in mm/inches





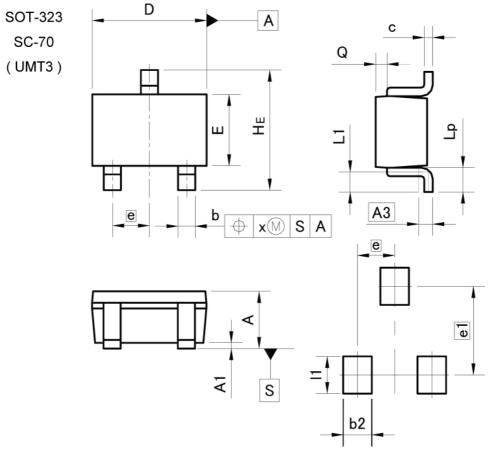
Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.85	1.05	0.033	0.041
A1	0.00	0.10	0.000	0.004
A2	0.80	1.00	0.031	0.039
b	0.27	0.42	0.011	0.017
С	0.08	0.18	0.003	0.007
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.0	26
HE	2.00	2.20	0.079	0.087
L	0.43		0.0	17
Lp	0.43	0.63	0.017	0.025
х	_	0.10	-	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	-	0.52	ı	0.020
e1	1.47		0.058	
l1	-	0.83	=	0.033

Dimension in mm/inches





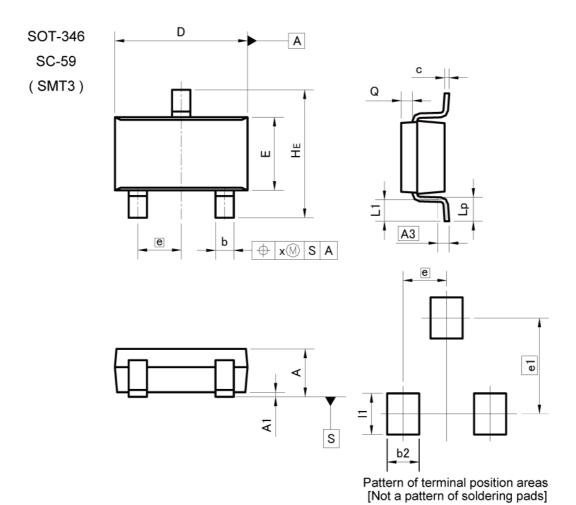
Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0	0.004
A3	0.25		0.01	
b	0.25	0.40	0.01	0.016
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.65		0.03	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.02
Lp	0.25	0.55	0.01	0.022
Q	0.10	0.30	0.004	0.012
х	_	0.10	_	0.004

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
e1	1.55		0.06	
b2	-	0.50	1	0.02
11	_	0.65	_	0.026

Dimension in mm/inches





DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.25		0.010	
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.95		0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
х	-	0.10	-	0.004
У	- 2	0.10	·-	0.004

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2		0.60	-	0.024	
e1	2.10		0.083		
11	-,:	0.90	-	0.035	

Dimension in mm/inches



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