

C3D20060D–Silicon Carbide Schottky Diode

Z-REC™ RECTIFIER

$$V_{RRM} = 600 \text{ V}$$

$$I_F, T_C < 135^\circ \text{C} = 28 \text{ A}$$

$$Q_c = 50 \text{ nC}$$

Features

- 600-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V_F

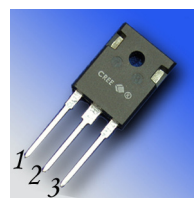
Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

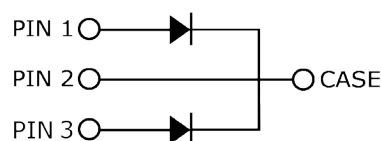
Applications

- Switch Mode Power Supplies
- Power Factor Correction
 - Typical PFC P_{out} : 2000W-4000W
- Motor Drives
 - Typical Power : 5HP-10HP

Package



TO-247-3



Part Number	Package	Marking
C3D20060D	TO-247-3	C3D20060

Maximum Ratings

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	600	V		
V_{RSM}	Surge Peak Reverse Voltage	600	V		
V_{DC}	DC Blocking Voltage	600	V		
I_F	Continuous Forward Current (Per Leg/Device)	14/28	A	$T_C < 135^\circ \text{C}$, No AC component	
I_{FRM}	Repetitive Peak Forward Surge Current (Per Leg/Device)	67/134 44/88	A	$T_C = 25^\circ \text{C}$, $t_p = 10 \text{ ms}$, Half Sine Wave, $D = 0.3$ $T_C = 110^\circ \text{C}$, $t_p = 10 \text{ ms}$, Half Sine Wave, $D = 0.3$	
I_{FSM}	Non-Repetitive Peak Forward Surge Current (Per Leg)	90/157 71/115	A	$T_C = 25^\circ \text{C}$, $t_p = 10 \text{ ms}$, Half Sine Wave, $D = 0.3$ $T_C = 110^\circ \text{C}$, $t_p = 10 \text{ ms}$, Half Sine Wave, $D = 0.3$	
I_{FSM}	Non-Repetitive Peak Forward Surge Current (Per Leg/Device)	250/500	A	$T_C = 25^\circ \text{C}$, $t_p = 10 \mu \text{s}$, Pulse	
P_{tot}	Power Dissipation (Per Leg)	136.3 59	W	$T_C = 25^\circ \text{C}$ $T_C = 125^\circ \text{C}$	
T_J, T_{stg}	Operating Junction and Storage Temperature	-55 to +175	$^\circ \text{C}$		
	TO-247 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	

Electrical Characteristics (Per Leg)

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V_F	Forward Voltage	1.5 2.0	1.8 2.4	V	$I_F = 10\text{ A}$ $T_J = 25^\circ\text{C}$ $I_F = 10\text{ A}$ $T_J = 175^\circ\text{C}$	
I_R	Reverse Current	10 20	50 200	μA	$V_R = 600\text{ V}$ $T_J = 25^\circ\text{C}$ $V_R = 600\text{ V}$ $T_J = 175^\circ\text{C}$	
Q_C	Total Capacitive Charge	25		nC	$V_R = 600\text{ V}$, $I_F = 10\text{ A}$ $di/dt = 500\text{ A}/\mu\text{s}$ $T_J = 25^\circ\text{C}$	
C	Total Capacitance	480 50 42		pF	$V_R = 0\text{ V}$, $T_J = 25^\circ\text{C}$, $f = 1\text{ MHz}$ $V_R = 200\text{ V}$, $T_J = 25^\circ\text{C}$, $f = 1\text{ MHz}$ $V_R = 400\text{ V}$, $T_J = 25^\circ\text{C}$, $f = 1\text{ MHz}$	

Note:

1. This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Symbol	Parameter	Typ.	Unit
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.1** 0.55*	$^\circ\text{C}/\text{W}$

** Per Leg, * Both Legs

Typical Performance (Per Leg)

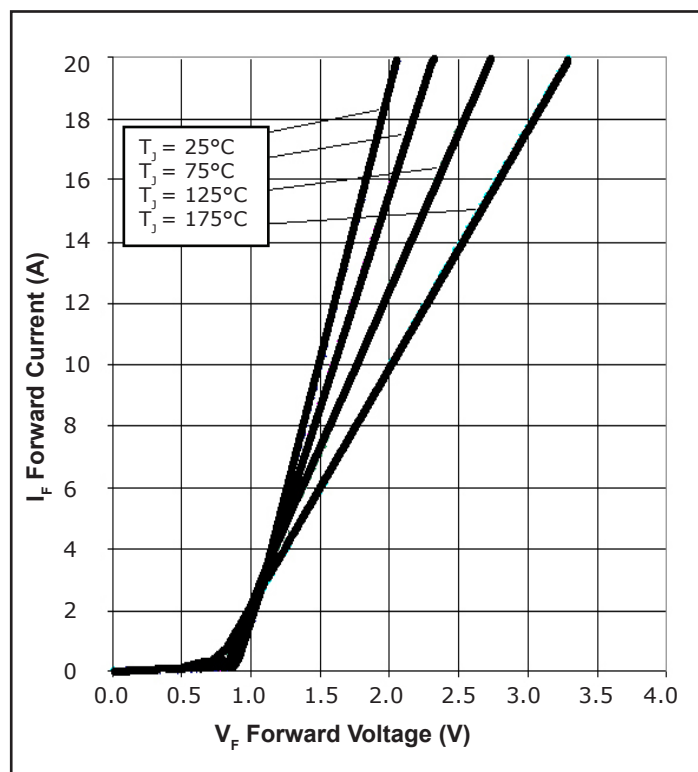


Figure 1. Forward Characteristics

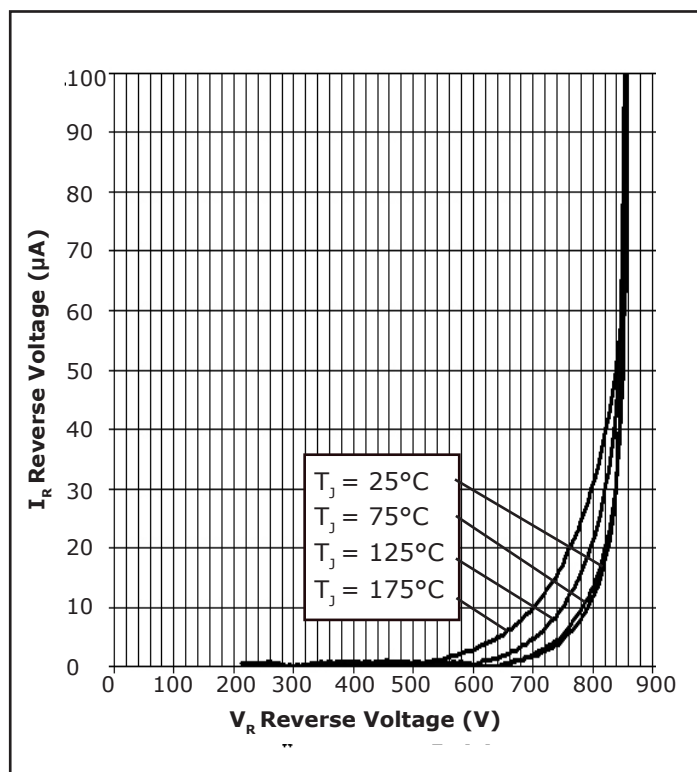


Figure 2. Reverse Characteristics

Typical Performance (Per Leg)

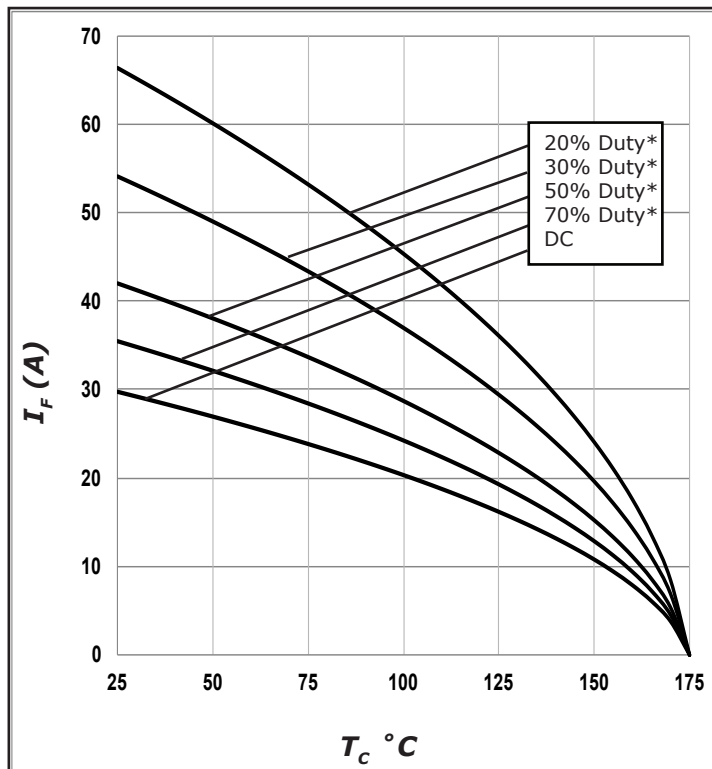


Figure 3. Current Derating

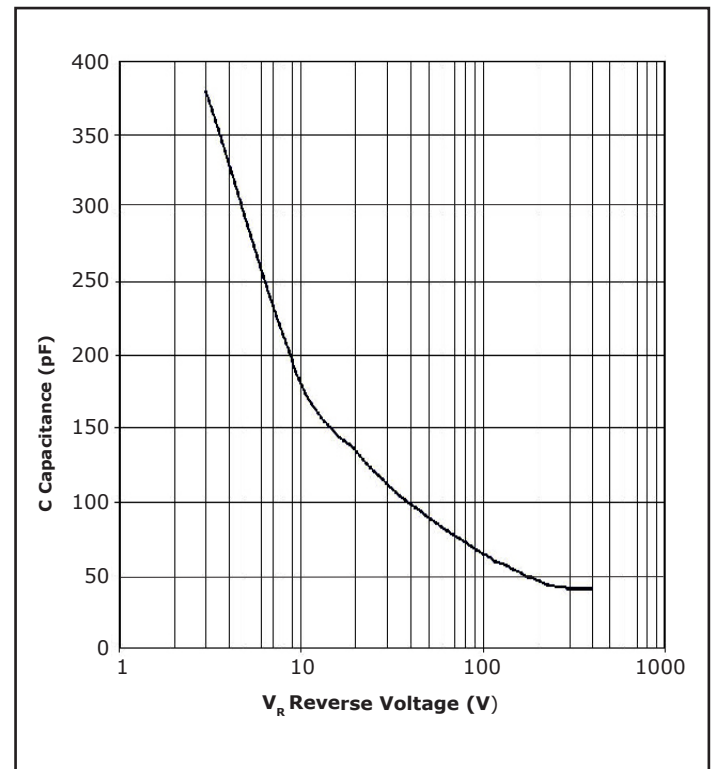


Figure 4. Capacitance vs. Reverse Voltage

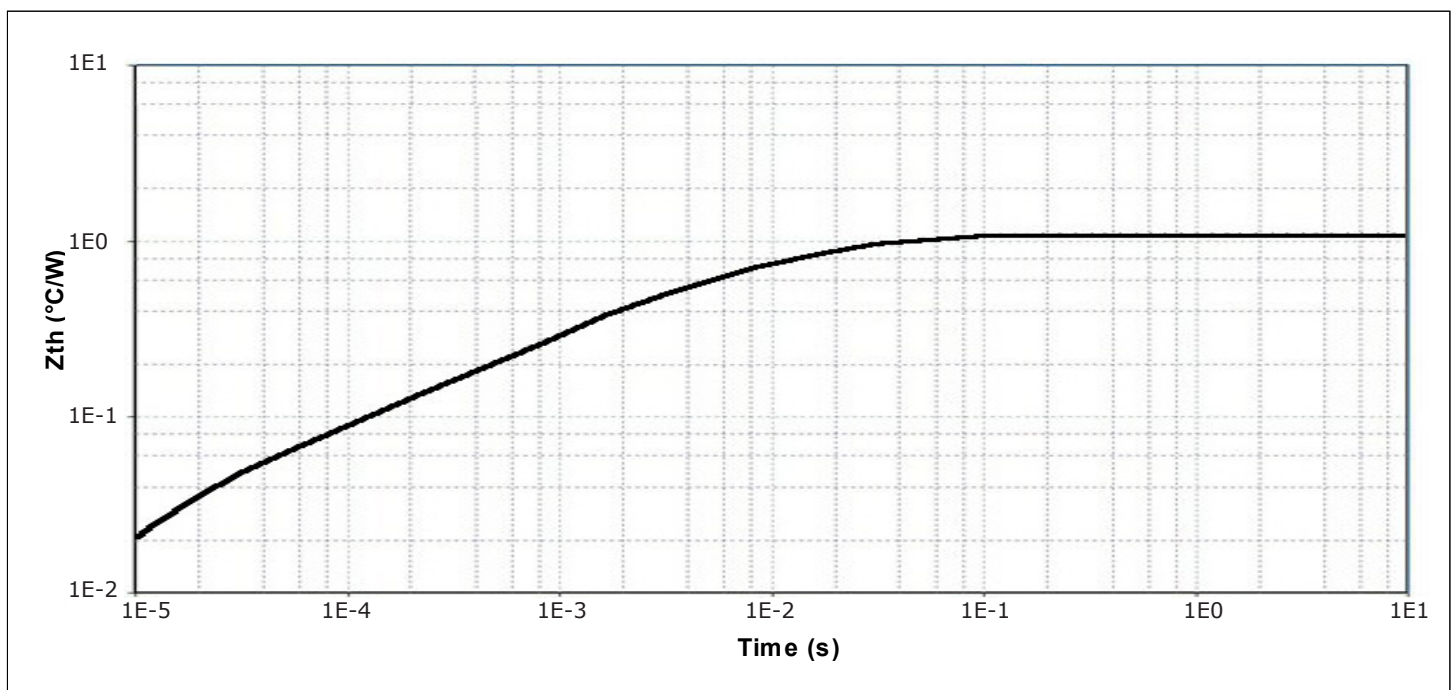


Figure 5. Transient Thermal Impedance

Typical Performance (Per Leg)

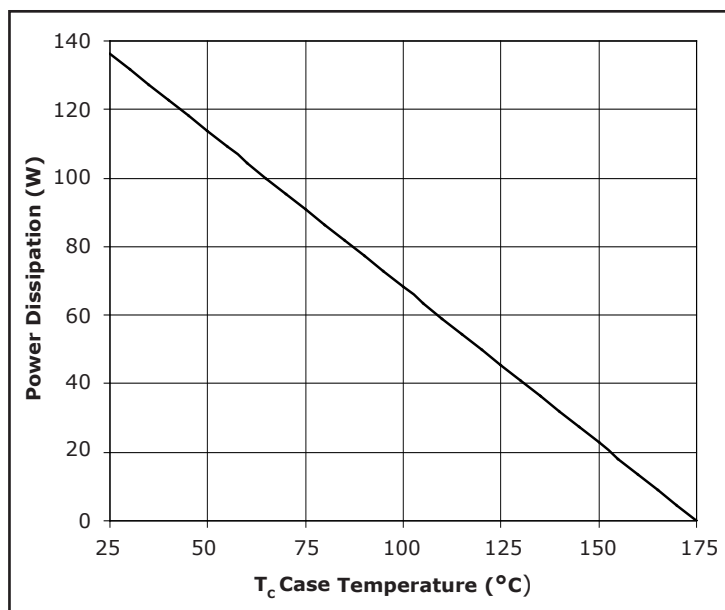
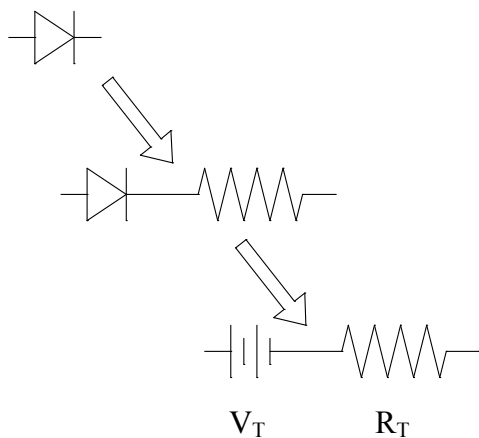


Figure 6. Power Derating

Diode Model (Per Leg)



$$V_{f_T} = V_T + I_f \cdot R_T$$

$$V_T = 0.98 + (T_J \cdot -1.6 \cdot 10^{-3})$$

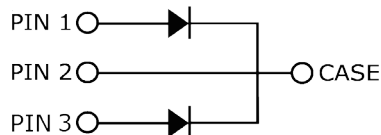
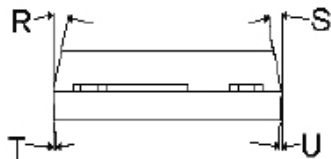
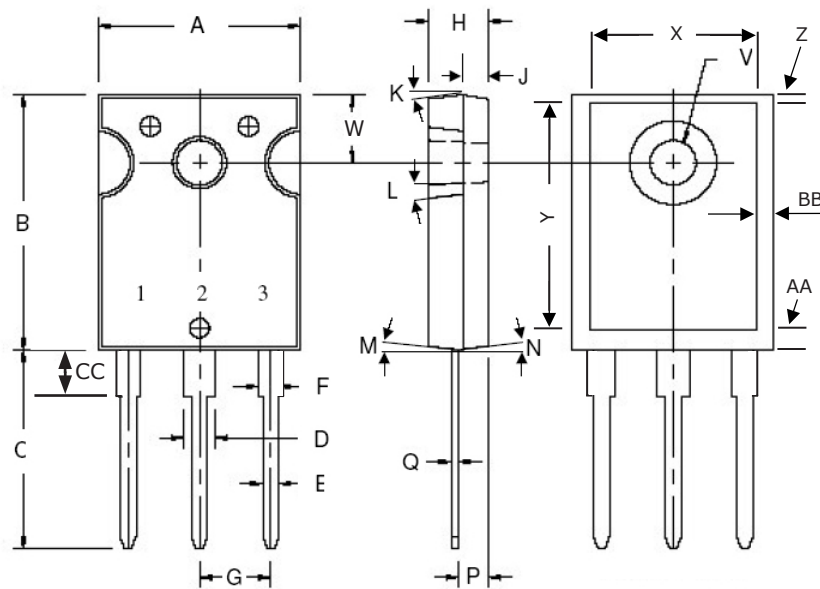
$$R_T = 0.04 + (T_J \cdot 0.522 \cdot 10^{-3})$$

Note: T_J = Diode Junction Temperature In Degrees Celcius



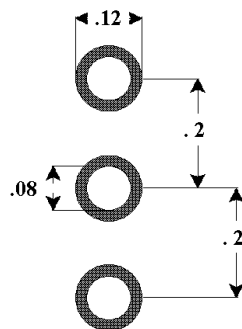
Package Dimensions

Package TO-247-3



POS	Inches		Millimeters	
	Min	Max	Min	Max
A	.605	.635	15.367	16.130
B	.800	.831	20.320	21.10
C	.780	.800	19.810	20.320
D	.095	.133	2.413	3.380
E	.046	.052	1.168	1.321
F	.060	.095	1.524	2.410
G	.215 TYP		5.460 TYP	
H	.175	.205	4.450	5.210
J	.075	.085	1.910	2.160
K	6°	21°	6°	21°
L	4°	6°	4°	6°
M	2°	4°	2°	4°
N	2°	4°	2°	4°
P	.090	.100	2.286	2.540
Q	.020	.030	.508	.762
R	9°	11°	9°	11°
S	9°	11°	9°	11°
T	2°	8°	2°	8°
U	2°	8°	2°	8°
V	.137	.144	3.487	3.658
W	.210	.248	5.334	6.300
X	.502	.557	12.751	14.150
Y	.637	.695	16.180	17.653
Z	.038	.052	0.964	1.321
AA	.110	.140	2.794	3.556
BB	.030	.046	0.766	1.168
CC	.161	.176	4.100	4.472

Recommended Solder Pad Layout



TO-247-3

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"The levels of environmentally sensitive, persistent biologically toxic (PBT), persistent organic pollutants (POP), or otherwise restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2002/95/EC on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS), as amended through April 21, 2006."

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