74AUP1G02-Q100

Low-power 2-input NOR gate Rev. 1 — 4 June 2014

Product data sheet

General description 1.

The 74AUP1G02-Q100 provides the single 2-input NOR function.

Schmitt-trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF}.

The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

Features and benefits 2.

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - ◆ Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - ◆ JESD8-12 (0.8 V to 1.3 V)
 - ◆ JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - ◆ JESD8-5 (1.8 V to 2.7 V)
 - ◆ JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - ◆ MIL-STD-883, method 3015 Class 3A. Exceeds 5000 V
 - HBM JESD22-A114F Class 3A. Exceeds 5000 V
 - ♦ MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation



3. Ordering information

Table 1. Ordering information

Type number	Package						
	Temperature range	Name	Description	Version			
74AUP1G02GW-Q100	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			

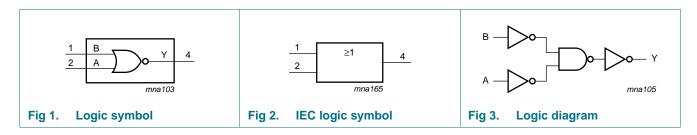
4. Marking

Table 2. Marking

Type number	Marking code ^[1]
74AUP1G02GW-Q100	pB

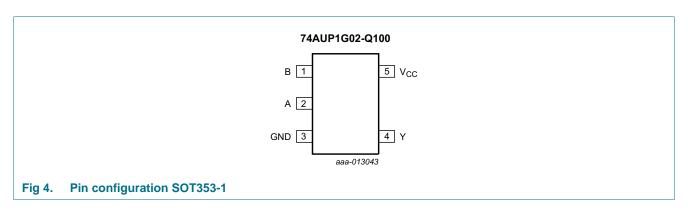
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1 Pinning



6.2 Pin description

Table 3. Pin description

Symbol	Pin	Description
В	1	data input
A	2	data input
GND	3	ground (0 V)
Υ	4	data output
V _{CC}	5	supply voltage

7. Functional description

Table 4. Function table[1]

Input		Output
Α	В	Υ
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

^[1] H = HIGH voltage level;L = LOW voltage level.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	$V_1 < 0 V$	-50	-	mA
V_{I}	input voltage		<u>[1]</u> –0.5	+4.6	V
I _{OK}	output clamping current	$V_O > V_{CC}$ or $V_O < 0$ V	-	±50	mA
Vo	output voltage	Active mode and Power-down mode	<u>[1]</u> –0.5	+4.6	V
I _O	output current	$V_O = 0 V \text{ to } V_{CC}$	-	±20	mA
I _{CC}	supply current		-	+50	mA
I_{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$	[2] _	250	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

^[2] For TSSOP5 packages: above 87.5 $^{\circ}$ C the value of P_{tot} derates linearly with 4.0 mW/K.

Recommended operating conditions

Table 6. **Recommended operating conditions**

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V_{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	0	200	ns/V

10. Static characteristics

Table 7. **Static characteristics**

74AUP1G02_Q100

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Uni
T _{amb} = 2	5 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	$0.70 \times V_{CC}$	-	-	V
		V _{CC} = 0.9 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	$0.30 \times V_{CC}$	V
		V _{CC} = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_{O} = -20 \mu A$; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	$V_{CC}-0.1$	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.75 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	2.05	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6	-	-	٧
/ _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A$; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		$I_O = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.3 \times V_{CC}$	V
		$I_O = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.31	V
		$I_O = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.31	V
		$I_O = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.31	V
		$I_O = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.44	V
		$I_O = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		$I_O = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.44	V

 Table 7.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
l _l	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μΑ
l _{OFF}	power-off leakage current	V_I or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μΑ
ΔI_{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μΑ
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μΑ
Δl _{CC}	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	1] -	-	40	μΑ
Cı	input capacitance	V_{CC} = 0 V to 3.6 V; V_I = GND or V_{CC}	-	0.8	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF
T _{amb} = -	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	$0.30 \times V_{CC}$	V
		V _{CC} = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_{O} = -20 \mu A$; $V_{CC} = 0.8 \text{ V}$ to 3.6 V	V _{CC} - 0.1	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.7 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.03	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.30	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.97	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A$; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		$I_O = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.3 \times V_{CC}$	V
		$I_O = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.37	V
		$I_O = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.35	V
		$I_O = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.33	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.45	V
		$I_O = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.33	V
		$I_O = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.45	V
l _l	input leakage current	$V_{I} = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.5	μΑ
I _{OFF}	power-off leakage current	V_I or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μΑ
ΔI_{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μΑ

74AUP1G02_Q100

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 Table 7.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Uni
I _{CC}	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.9	μΑ
∆l _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	<u>[1]</u> -	-	50	μΑ
Γ _{amb} = –	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	$V_{CC} = 0.8 \text{ V}$	$0.75 \times V_{CC}$	-	-	V
		V _{CC} = 0.9 V to 1.95 V	$0.70 \times V_{CC}$		-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	$0.25 \times V_{CC}$	V
		V _{CC} = 0.9 V to 1.95 V	-	-	$0.30 \times V_{CC}$	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_{O} = -20 \mu A$; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V _{CC} - 0.11	-	-	V
		$I_O = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.6 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		$I_O = -1.9 \text{ mA}$; $V_{CC} = 1.65 \text{ V}$	1.17	-	-	V
		$I_{O} = -2.3 \text{ mA}$; $V_{CC} = 2.3 \text{ V}$	1.77	-	-	V
		$I_O = -3.1 \text{ mA}$; $V_{CC} = 2.3 \text{ V}$	1.67	-	-	V
		$I_{O} = -2.7 \text{ mA}$; $V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_O = -4.0 \text{ mA}$; $V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
√ _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A$; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.11	V
		$I_O = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.33 \times V_{CC}$	V
		$I_O = 1.7 \text{ mA}$; $V_{CC} = 1.4 \text{ V}$	-	-	0.41	V
		$I_O = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.39	V
		$I_O = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.36	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.50	V
ı	input leakage current	$V_{I} = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μΑ
OFF	power-off leakage current	V_{I} or $V_{O} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.75	μA
∆l _{OFF}	additional power-off leakage current	V _I or V _O = 0 V to 3.6 V; V _{CC} = 0 V to 0.2 V	-	-	±0.75	μΑ
СС	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	1.4	μА
Δl _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	[1] -	-	75	μА

^[1] One input at V_{CC} – 0.6 V, other input at V_{CC} or GND.

11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 6

Symbol	Parameter	Conditions		Min	Typ 🗓	Max	Unit
T _{amb} = 25	°C; C _L = 5 pF						
t _{pd}	propagation delay	A, B to Y; see Figure 5	[2]				
		$V_{CC} = 0.8 \text{ V}$		-	17.0	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.5	5.1	10.8	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		1.6	3.7	6.7	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.3	3.0	5.3	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.0	2.4	3.9	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.0	2.2	3.4	ns
T _{amb} = 25	°C; C _L = 10 pF						
t _{pd}	propagation delay	A, B to Y; see Figure 5	[2]				
		$V_{CC} = 0.8 \text{ V}$		-	20.4	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.4	6.0	12.8	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		1.9	4.3	7.9	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.6	3.6	6.2	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.4	3.0	4.7	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.3	2.7	4.2	ns
T _{amb} = 25	°C; C _L = 15 pF						
t _{pd}	propagation delay	A, B to Y; see Figure 5	[2]				
		$V_{CC} = 0.8 \text{ V}$		-	23.9	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.4	6.8	14.6	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		2.3	4.8	8.9	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.9	4.0	7.0	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.7	3.4	5.4	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.6	3.2	4.8	ns
T _{amb} = 25	°C; C _L = 30 pF						
t _{pd}	propagation delay	A, B to Y; see Figure 5	[2]				
		$V_{CC} = 0.8 \text{ V}$		-	34.2	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		4.6	9.0	19.9	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		3.4	6.4	11.8	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		2.6	5.3	9.3	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		2.4	4.5	7.1	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		2.3	4.2	6.4	ns

 Table 8.
 Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 6

Symbol	Parameter	Conditions	Min	Typ 🗓	Max	Unit
T _{amb} = 25	°C					
C_{PD}	power dissipation capacitance	$f = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$	[3]			
		$V_{CC} = 0.8 \text{ V}$	-	2.6	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.7	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.9	-	pF
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	-	3.1	-	pF
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	3.5	-	pF
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	4.1	-	pF

^[1] All typical values are measured at nominal V_{CC} .

 $P_D = C_{PD} \times V_{CC}{}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}{}^2 \times f_o) \text{ where:}$

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 6

Symbol	Parameter	Conditions		–40 °C t	o +85 °C	-40 °C to	+125 °C	Unit
				Min	Max	Min	Max	
C _L = 5 pF			'			'		'
t _{pd}	propagation delay	A, B to Y; see Figure 5	[1]					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.1	12.1	2.1	13.4	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		1.4	7.8	1.4	8.6	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.1	6.2	1.1	6.9	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		0.9	4.6	0.9	5.1	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		0.8	4.0	8.0	4.4	ns
C _L = 10 pF								
t _{pd}	propagation delay	A, B to Y; see Figure 5	[1]					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.2	14.3	2.2	15.8	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		1.7	9.2	1.7	10.2	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.5	7.3	1.5	8.1	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.2	5.6	1.2	6.2	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.2	5.0	1.2	5.5	ns

^[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

^[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 Table 9.
 Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 6

Symbol	Parameter	Conditions		-40 °C to +85 °C		-40 °C to +125 °C		Unit
				Min Max		Min	Max	
C _L = 15 pF			'		1		'	
t _{pd}	propagation delay	A, B to Y; see Figure 5	<u>[1]</u>					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.1	16.4	3.1	18.1	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		2.0	10.4	2.0	11.5	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		1.7	8.3	1.7	9.2	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.5	6.3	1.5	7.0	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.4	5.7	1.4	6.3	ns
C _L = 30 pF	-							
t _{pd}	propagation delay	A, B to Y; see Figure 5	<u>[1]</u>					
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		4.1	22.4	4.1	24.7	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		2.9	13.9	2.9	15.3	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		2.3	11.1	2.3	12.3	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		2.1	8.5	2.1	9.4	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		2.1	7.7	2.1	8.5	ns

^[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

12. Waveforms

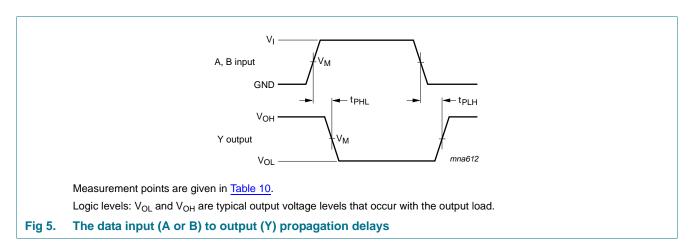
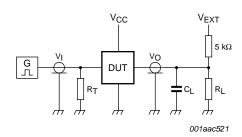


Table 10. Measurement points

Supply voltage	Output	Input		
V _{CC}	V _M	V _M	V _I	$t_r = t_f$
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V _{CC}	≤ 3.0 ns



Test data is given in Table 11.

Definitions for test circuit:

 R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_0 of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

Fig 6. Test circuit for measuring switching times

Table 11. Test data

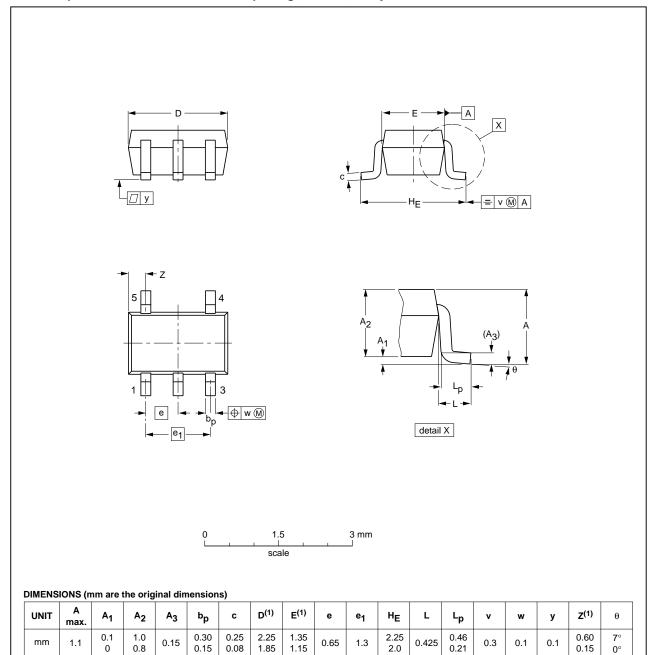
Supply voltage	Load	V _{EXT}			
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k Ω or 1 M Ω	open	GND	$2\times V_{CC}$

[1] For measuring enable and disable times R_L = 5 k Ω , for measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

13. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT353-1		MO-203	SC-88A			-00-09-01- 03-02-19

Fig 7. Package outline SOT353-1 (TSSOP5)

74AUP1G02_Q100

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14. Abbreviations

Table 12. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MIL	Military
MM	Machine Model

15. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G02_Q100 v.1	20140604	Product data sheet	-	-

16. Legal information

16.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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74AUP1G02_Q100

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18. Contents

1	General description
2	Features and benefits
3	Ordering information
4	Marking 2
5	Functional diagram 2
6	Pinning information 2
6.1	Pinning
6.2	Pin description
7	Functional description 3
8	Limiting values 3
9	Recommended operating conditions 4
10	Static characteristics 4
11	Dynamic characteristics
12	Waveforms
13	Package outline
14	Abbreviations
15	Revision history
16	Legal information
16.1	Data sheet status
16.2	Definitions
16.3	Disclaimers
16.4	Trademarks14
17	Contact information 14
18	Contents 15



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