

### Thyristor/Diode and Thyristor/Thyristor, 135 A to 160 A (New INT-A-PAK Power Modules)



New INT-A-PAK

#### FEATURES

- High voltage
- Electrically isolated by DBC ceramic ( $Al_2O_3$ )
- 3500 V<sub>RMS</sub> isolating voltage
- Industrial standard package
- High surge capability
- Glass passivated chips
- Modules uses high voltage power thyristor/diodes in three basic configurations
- Simple mounting
- UL approved file E78996 
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for multiple level



**RoHS**  
COMPLIANT

#### PRODUCT SUMMARY

|             |                |
|-------------|----------------|
| $I_{T(AV)}$ | 135 A to 160 A |
|-------------|----------------|

#### APPLICATIONS

- DC motor control and drives
- Battery charges
- Welders
- Power converters
- Lighting control
- Heat and temperature control

#### MAJOR RATINGS AND CHARACTERISTICS

| SYMBOL        | CHARACTERISTICS | VSK.136..   | VSK.142.. | VSK.162.. | UNITS              |
|---------------|-----------------|-------------|-----------|-----------|--------------------|
| $I_{T(AV)}$   | 85 °C           | 135         | 140       | 160       | A                  |
| $I_{T(RMS)}$  |                 | 300         | 310       | 355       | A                  |
| $I_{TSM}$     | 50 Hz           | 3200        | 4500      | 4870      |                    |
|               | 60 Hz           | 3360        | 4712      | 5100      |                    |
| $I^2t$        | 50 Hz           | 51.5        | 102       | 119       | kA <sup>2</sup> s  |
|               | 60 Hz           | 47          | 92.5      | 108       |                    |
| $I^2\sqrt{t}$ |                 | 515.5       | 1013      | 1190      | kA <sup>2</sup> √s |
| $V_{RRM}$     | Range           | 400 to 1600 |           |           | V                  |
| $T_J$         | Range           | - 40 to 125 |           |           | °C                 |

#### ELECTRICAL SPECIFICATIONS

##### VOLTAGE RATINGS

| TYPE NUMBER                   | VOLTAGE CODE | $V_{RRM}/V_{DRM}$ , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE<br>V | $V_{RSM}/V_{DSM}$ , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE<br>V | $I_{RRM}/I_{DRM}$ AT 125 °C<br>mA |
|-------------------------------|--------------|------------------------------------------------------------------|----------------------------------------------------------------------|-----------------------------------|
| VSK.136<br>VSK.142<br>VSK.162 | 04           | 400                                                              | 500                                                                  | 50                                |
|                               | 08           | 800                                                              | 900                                                                  |                                   |
|                               | 12           | 1200                                                             | 1300                                                                 |                                   |
|                               | 14           | 1400                                                             | 1500                                                                 |                                   |
|                               | 16           | 1600                                                             | 1700                                                                 |                                   |

# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series



Vishay High Power Products

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

| FORWARD CONDUCTION                                             |                     |                                                                                             |                                  |                                                                 |         |         |         |                    |
|----------------------------------------------------------------|---------------------|---------------------------------------------------------------------------------------------|----------------------------------|-----------------------------------------------------------------|---------|---------|---------|--------------------|
| PARAMETER                                                      | SYMBOL              | TEST CONDITIONS                                                                             |                                  |                                                                 | VSK.136 | VSK.142 | VSK.162 | UNITS              |
| Maximum average on-state current at case temperature           | I <sub>T(AV)</sub>  | 180° conduction, half sine wave                                                             |                                  |                                                                 | 135     | 140     | 160     | A                  |
|                                                                |                     |                                                                                             |                                  |                                                                 | 85      | 85      | 85      | °C                 |
| Maximum RMS on-state current                                   | I <sub>T(RMS)</sub> | As AC switch                                                                                |                                  |                                                                 | 300     | 310     | 355     | A                  |
| Maximum peak, one-cycle on-state, non-repetitive surge current | I <sub>TSM</sub>    | t = 10 ms                                                                                   | No voltage reapplied             | Sine half wave, initial T <sub>J</sub> = T <sub>J</sub> maximum | 3200    | 4500    | 4870    |                    |
|                                                                |                     | t = 8.3 ms                                                                                  |                                  |                                                                 | 3360    | 4712    | 5100    |                    |
|                                                                |                     | t = 10 ms                                                                                   | 100 % V <sub>RRM</sub> reapplied |                                                                 | 2700    | 3785    | 4100    |                    |
|                                                                |                     | t = 8.3 ms                                                                                  |                                  |                                                                 | 2800    | 3963    | 4300    |                    |
| Maximum I <sup>2</sup> t for fusing                            | I <sup>2</sup> t    | t = 10 ms                                                                                   | No voltage reapplied             |                                                                 | 51.5    | 102     | 119     | kA <sup>2</sup> s  |
|                                                                |                     | t = 8.3 ms                                                                                  |                                  |                                                                 | 47      | 92.5    | 108     |                    |
|                                                                |                     | t = 10 ms                                                                                   | 100 % V <sub>RRM</sub> reapplied |                                                                 | 36.5    | 71.6    | 84      |                    |
|                                                                |                     | t = 8.3 ms                                                                                  |                                  |                                                                 | 33.3    | 65.4    | 76.7    |                    |
| Maximum I <sup>2</sup> √t for fusing                           | I <sup>2</sup> √t   | t = 0.1 ms to 10 ms, no voltage reapplied                                                   |                                  |                                                                 | 515.5   | 1013    | 1190    | kA <sup>2</sup> √s |
| Low level value of threshold voltage                           | V <sub>T(TO)1</sub> | (16.7 % × π × I <sub>T(AV)</sub> ) < I < π × I <sub>T(AV)</sub> , T <sub>J</sub> maximum    |                                  |                                                                 | 0.86    | 0.83    | 0.8     | V                  |
| High level value of threshold voltage                          | V <sub>T(TO)2</sub> | (I > π × I <sub>T(AV)</sub> ), T <sub>J</sub> maximum                                       |                                  |                                                                 | 1.05    | 1       | 0.98    |                    |
| Low level value on-state slope resistance                      | r <sub>t1</sub>     | (16.7 % × π × I <sub>T(AV)</sub> ) < I < π × I <sub>T(AV)</sub> , T <sub>J</sub> maximum    |                                  |                                                                 | 2.02    | 1.78    | 1.67    | mΩ                 |
| High level value on-state slope resistance                     | r <sub>t2</sub>     | (I > π × I <sub>T(AV)</sub> ), T <sub>J</sub> maximum                                       |                                  |                                                                 | 1.65    | 1.43    | 1.38    |                    |
| Maximum on-state voltage drop                                  | V <sub>TM</sub>     | I <sub>TM</sub> = π × I <sub>T(AV)</sub> , T <sub>J</sub> = 25 °C, 180° conduction          |                                  |                                                                 | 1.57    | 1.55    | 1.54    | V                  |
| Maximum forward voltage drop                                   | V <sub>FM</sub>     | I <sub>TM</sub> = π × I <sub>T(AV)</sub> , T <sub>J</sub> = 25 °C, 180° conduction          |                                  |                                                                 | 1.57    | 1.55    | 1.54    | V                  |
| Maximum holding current                                        | I <sub>H</sub>      | Anode supply = 6 V initial I <sub>T</sub> = 30 A, T <sub>J</sub> = 25 °C                    |                                  |                                                                 | 200     |         |         | mA                 |
| Maximum latching current                                       | I <sub>L</sub>      | Anode supply = 6 V resistive load = 1 Ω<br>Gate pulse: 10 V, 100 μs, T <sub>J</sub> = 25 °C |                                  |                                                                 | 400     |         |         |                    |

| SWITCHING             |                 |                                                                                                                                                |                                                                                              |           |       |
|-----------------------|-----------------|------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------|-----------|-------|
| PARAMETER             | SYMBOL          | TEST CONDITIONS                                                                                                                                |                                                                                              | VALUES    | UNITS |
| Typical delay time    | t <sub>gd</sub> | T <sub>J</sub> = 25 °C                                                                                                                         | Gate current = 1 A, dI <sub>g</sub> /dt = 1 A/μs<br>V <sub>d</sub> = 0.67 % V <sub>DRM</sub> | 1         | μs    |
| Typical rise time     | t <sub>gr</sub> |                                                                                                                                                |                                                                                              | 2         |       |
| Typical turn-off time | t <sub>q</sub>  | I <sub>TM</sub> = 300 A, - dI/dt = 15 A/μs; T <sub>J</sub> = T <sub>J</sub> maximum<br>V <sub>R</sub> = 50 V; dV/dt = 20 V/μs; gate 0 V, 100 Ω |                                                                                              | 50 to 200 |       |

| BLOCKING                                           |                          |                                                             |        |       |
|----------------------------------------------------|--------------------------|-------------------------------------------------------------|--------|-------|
| PARAMETER                                          | SYMBOL                   | TEST CONDITIONS                                             | VALUES | UNITS |
| Maximum peak reverse and off-state leakage current | $I_{RRM}$ ,<br>$I_{DRM}$ | $T_J = 125^\circ\text{C}$                                   | 50     | mA    |
| RMS insulation voltage                             | $V_{INS}$                | 50 Hz, circuit to base,<br>all terminals shorted, t = 1 s   | 3500   | V     |
| Critical rate of rise of off-state voltage         | $dV/dt$                  | $T_J = T_J$ maximum,<br>exponential to 67 % rated $V_{DRM}$ | 1000   | V/μs  |



# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

Vishay High Power Products

| TRIGGERING                                  |                    |                                                                                                 |                                                             |        |       |  |
|---------------------------------------------|--------------------|-------------------------------------------------------------------------------------------------|-------------------------------------------------------------|--------|-------|--|
| PARAMETER                                   | SYMBOL             | TEST CONDITIONS                                                                                 |                                                             | VALUES | UNITS |  |
| Maximum peak gate power                     | P <sub>GM</sub>    | t <sub>p</sub> ≤ 5 ms, T <sub>J</sub> = T <sub>J</sub> maximum                                  |                                                             | 12     | W     |  |
| Maximum average gate power                  | P <sub>G(AV)</sub> | f = 50 Hz, T <sub>J</sub> = T <sub>J</sub> maximum                                              |                                                             | 3      |       |  |
| Maximum peak gate current                   | I <sub>GM</sub>    | t <sub>p</sub> ≤ 5 ms, T <sub>J</sub> = T <sub>J</sub> maximum                                  |                                                             | 3      | A     |  |
| Maximum peak negative gate voltage          | - V <sub>GT</sub>  |                                                                                                 |                                                             | 10     | V     |  |
| Maximum required DC gate voltage to trigger | V <sub>GT</sub>    | T <sub>J</sub> = - 40 °C                                                                        | Anode supply = 6 V,<br>resistive load; R <sub>a</sub> = 1 Ω | 4      |       |  |
|                                             |                    | T <sub>J</sub> = 25 °C                                                                          |                                                             | 2.5    |       |  |
|                                             |                    | T <sub>J</sub> = T <sub>J</sub> maximum                                                         |                                                             | 1.7    |       |  |
| Maximum required DC gate current to trigger | I <sub>GT</sub>    | T <sub>J</sub> = - 40 °C                                                                        |                                                             | 270    | mA    |  |
|                                             |                    | T <sub>J</sub> = 25 °C                                                                          |                                                             | 150    |       |  |
|                                             |                    | T <sub>J</sub> = T <sub>J</sub> maximum                                                         |                                                             | 80     |       |  |
| Maximum gate voltage that will not trigger  | V <sub>GD</sub>    | T <sub>J</sub> = T <sub>J</sub> maximum, rated V <sub>DRM</sub> applied                         |                                                             | 0.3    | V     |  |
| Maximum gate current that will not trigger  | I <sub>GD</sub>    |                                                                                                 |                                                             | 10     | mA    |  |
| Maximum rate of rise of turned-on current   | dI/dt              | T <sub>J</sub> = T <sub>J</sub> maximum, I <sub>TM</sub> = 400 A rated V <sub>DRM</sub> applied |                                                             | 300    | A/μs  |  |

| THERMAL AND MECHANICAL SPECIFICATIONS                     |                                  |                                                                                                                                                              |               |      |       |
|-----------------------------------------------------------|----------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------|------|-------|
| PARAMETER                                                 | SYMBOL                           | TEST CONDITIONS                                                                                                                                              | VALUES        |      | UNITS |
| Maximum junction operating temperature range              | T <sub>J</sub>                   |                                                                                                                                                              | - 40 to 125   |      | °C    |
| Maximum storage temperature range                         | T <sub>Stg</sub>                 |                                                                                                                                                              | - 40 to 150   |      |       |
| Maximum thermal resistance, junction to case per junction | R <sub>thJC</sub>                | DC operation                                                                                                                                                 | 0.18          | 0.16 | K/W   |
| Maximum thermal resistance, case to heatsink per module   | R <sub>thCS</sub>                | Mounting surface, smooth, flat and greased                                                                                                                   | 0.05          |      |       |
| Mounting torque ± 10 %                                    | IAP to heatsink<br>busbar to IAP | A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound. Lubricated threads. | 4 to 6        |      | Nm    |
| Approximate weight                                        |                                  |                                                                                                                                                              | 200           |      | g     |
|                                                           |                                  |                                                                                                                                                              | 7.1           |      | oz.   |
| Case style                                                |                                  |                                                                                                                                                              | New INT-A-PAK |      |       |

| ΔR CONDUCTION PER JUNCTION |                                                    |        |        |        |        |                                                     |        |        |        |        |       |
|----------------------------|----------------------------------------------------|--------|--------|--------|--------|-----------------------------------------------------|--------|--------|--------|--------|-------|
| DEVICES                    | SINUSOIDAL CONDUCTION<br>AT T <sub>J</sub> MAXIMUM |        |        |        |        | RECTANGULAR CONDUCTION<br>AT T <sub>J</sub> MAXIMUM |        |        |        |        | UNITS |
|                            | 180°                                               | 120°   | 90°    | 60°    | 30°    | 180°                                                | 120°   | 90°    | 60°    | 30°    |       |
| VSK.136                    | 0.007                                              | 0.01   | 0.013  | 0.0155 | 0.017  | 0.009                                               | 0.012  | 0.014  | 0.015  | 0.017  | K/W   |
| VSK.142                    | 0.0019                                             | 0.0019 | 0.0020 | 0.0020 | 0.0021 | 0.0018                                              | 0.0022 | 0.0023 | 0.0023 | 0.0020 |       |
| VSK.162                    | 0.0030                                             | 0.0031 | 0.0032 | 0.0033 | 0.0034 | 0.0029                                              | 0.0036 | 0.0039 | 0.0041 | 0.0040 |       |

## Note

- Table shows the increment of thermal resistance  $R_{thJC}$  when devices operate at different conduction angles than DC

# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series



Vishay High Power Products

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

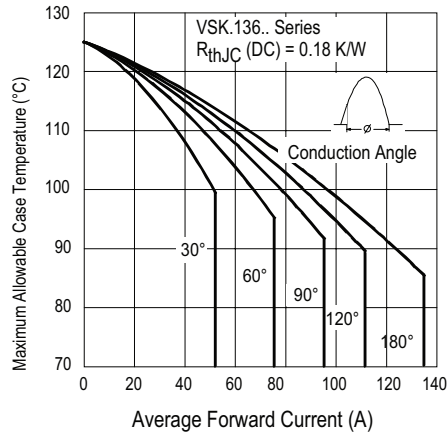


Fig. 1 - Current Ratings Characteristics

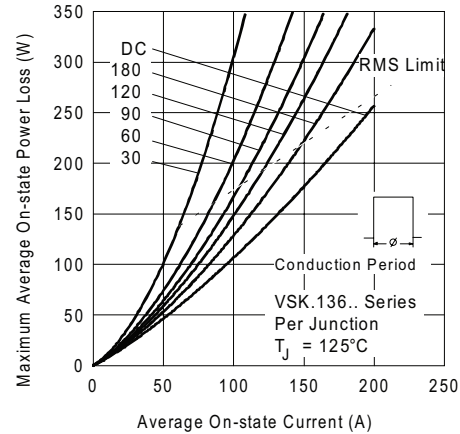


Fig. 4 - On-State Power Loss Characteristics

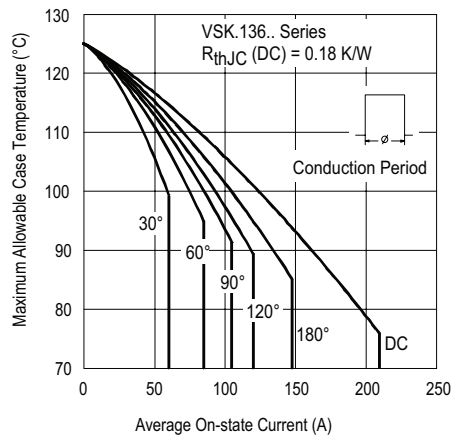


Fig. 2 - Current Ratings Characteristics

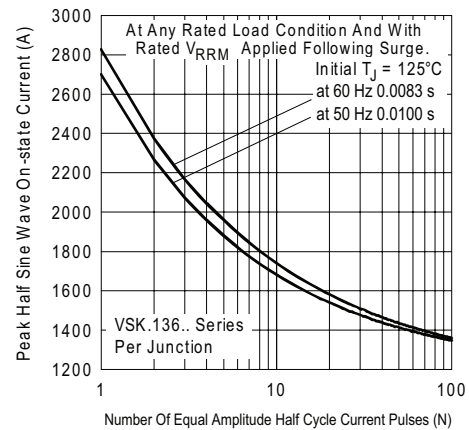


Fig. 5 - Maximum Non-Repetitive Surge Current

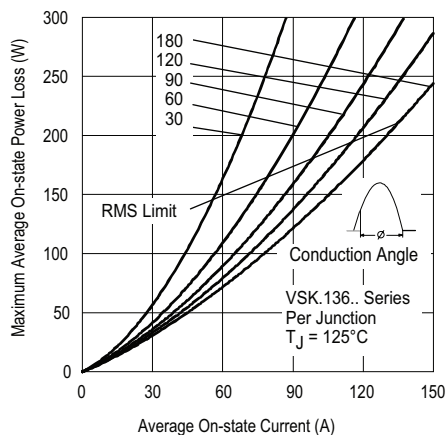


Fig. 3 - On-State Power Loss Characteristics

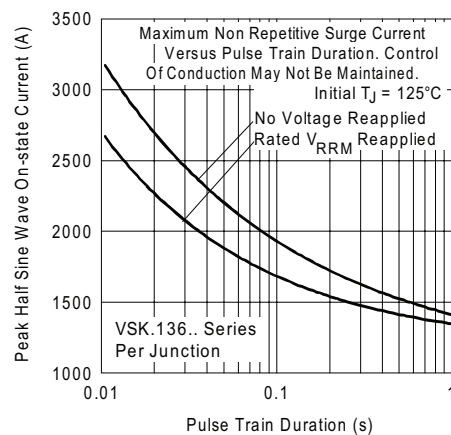


Fig. 6 - Maximum Non-Repetitive Surge Current



# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

Vishay High Power Products

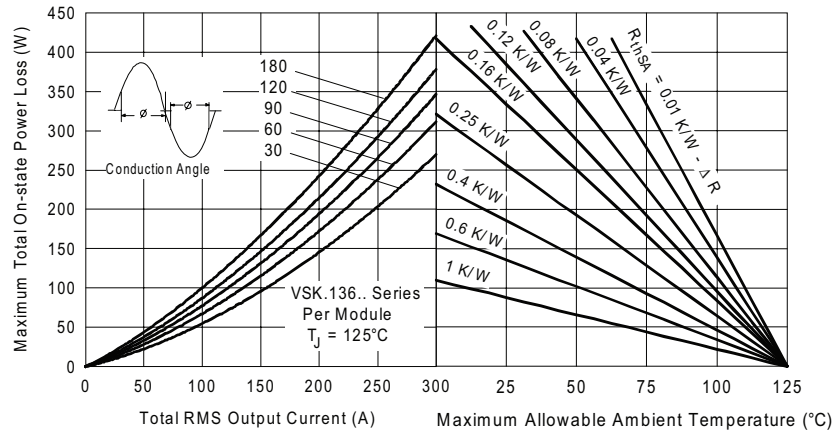


Fig. 7 - On-State Power Loss Characteristics

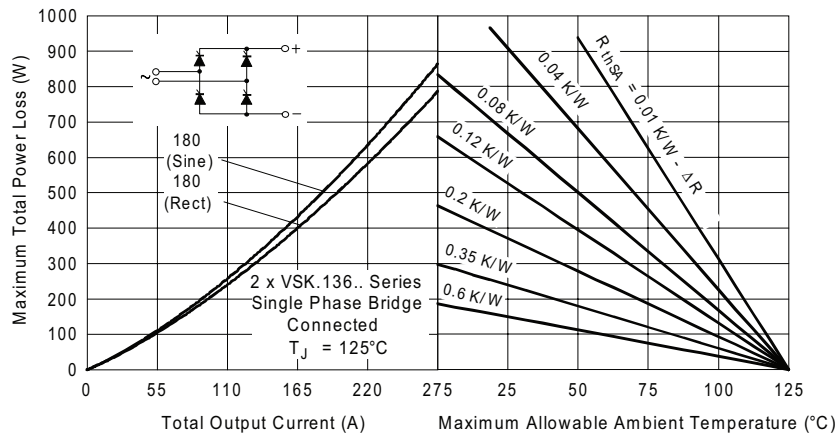


Fig. 8 - On-State Power Loss Characteristics

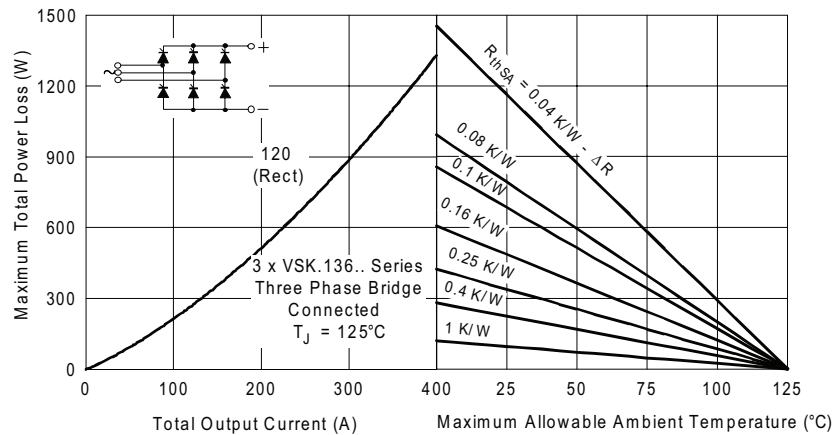


Fig. 9 - On-State Power Loss Characteristics

# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series



Vishay High Power Products

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

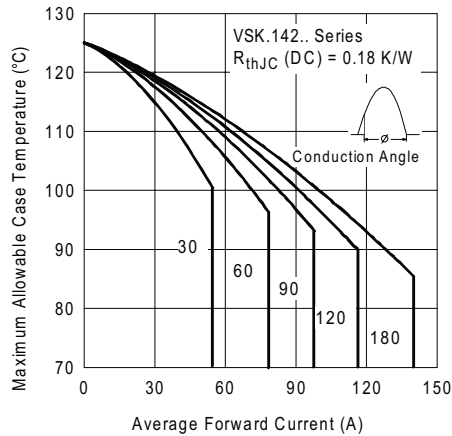


Fig. 10 - Current Ratings Characteristics

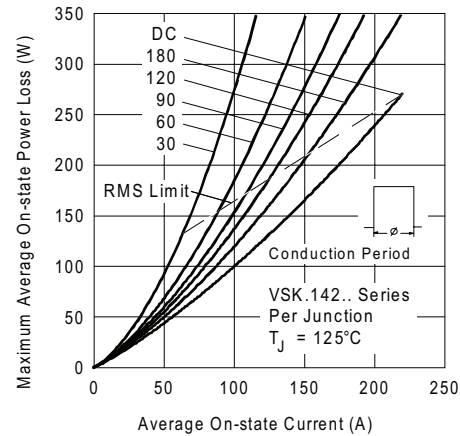


Fig. 13 - On-State Power Loss Characteristics

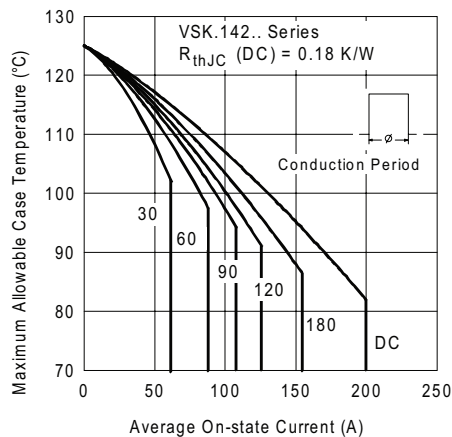


Fig. 11 - Current Ratings Characteristics

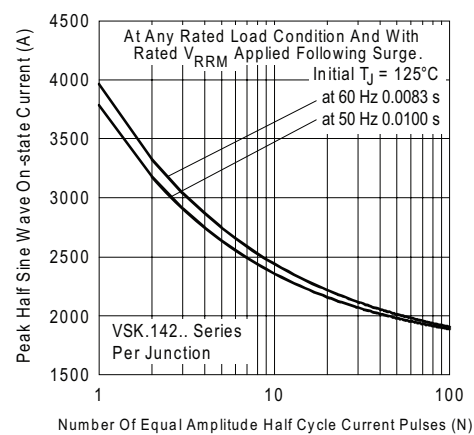


Fig. 14 - Maximum Non-Repetitive Surge Current

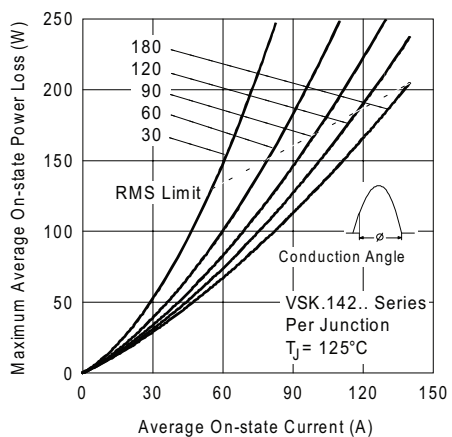


Fig. 12 - On-State Power Loss Characteristics

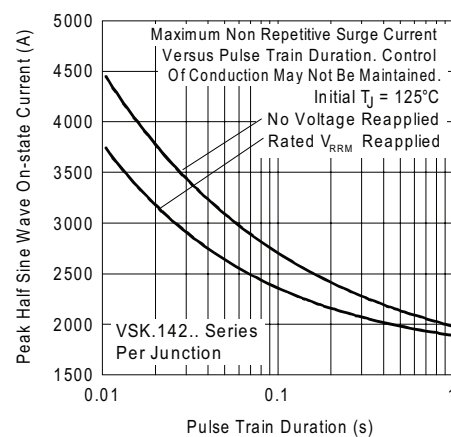


Fig. 15 - Maximum Non-Repetitive Surge Current



# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

Vishay High Power Products

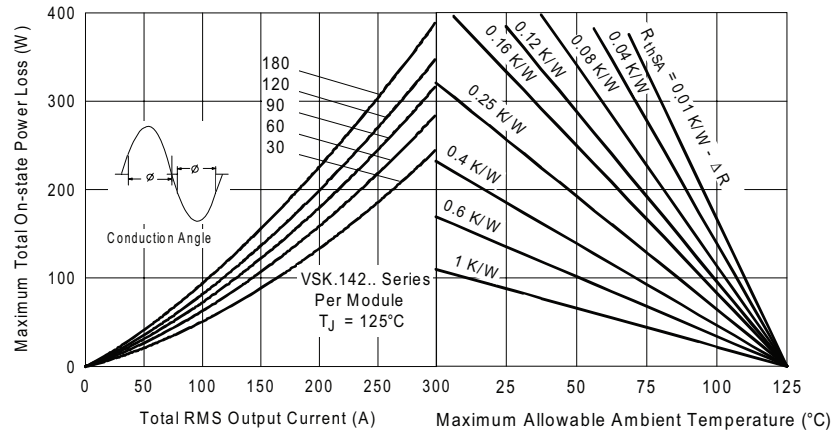


Fig. 16 - On-State Power Loss Characteristics

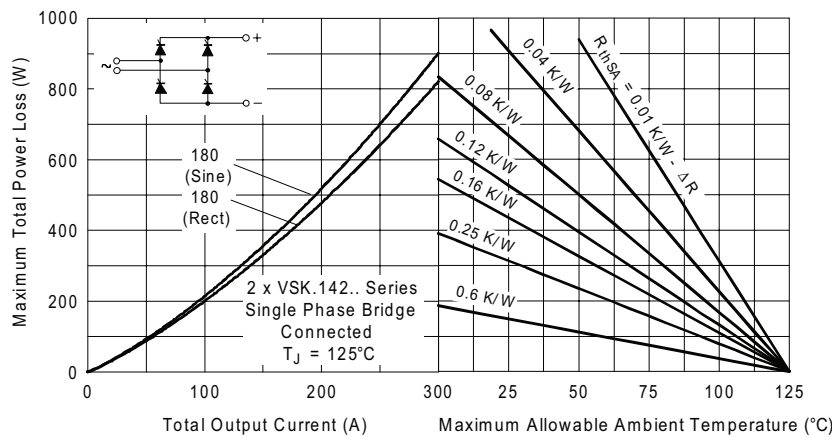


Fig. 17 - On-State Power Loss Characteristics

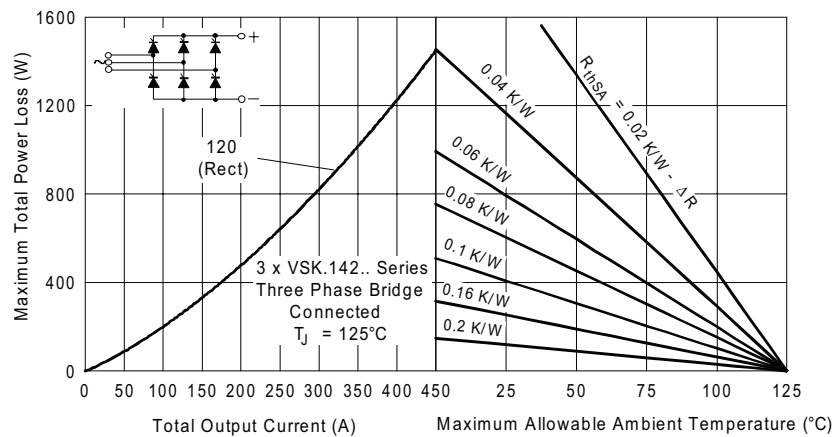


Fig. 18 - On-State Power Loss Characteristics

# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series



Vishay High Power Products

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

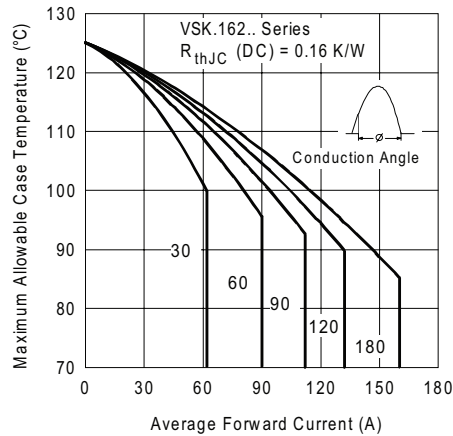


Fig. 19 - Current Ratings Characteristics

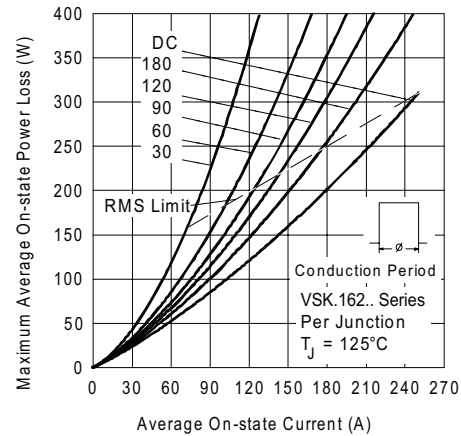


Fig. 22 - On-State Power Loss Characteristics

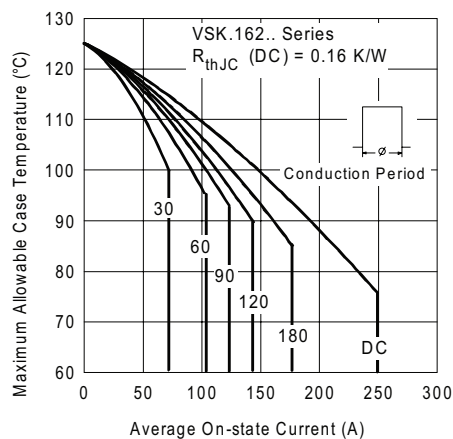


Fig. 20 - Current Ratings Characteristics

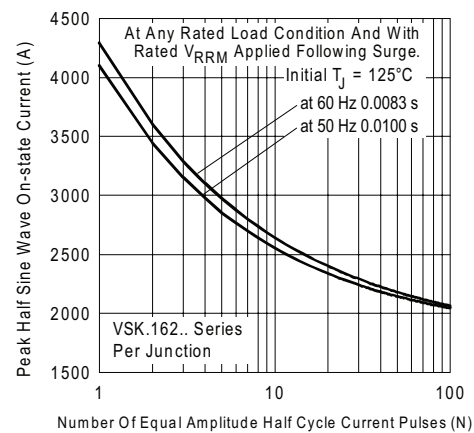


Fig. 23 - Maximum Non-Repetitive Surge Current

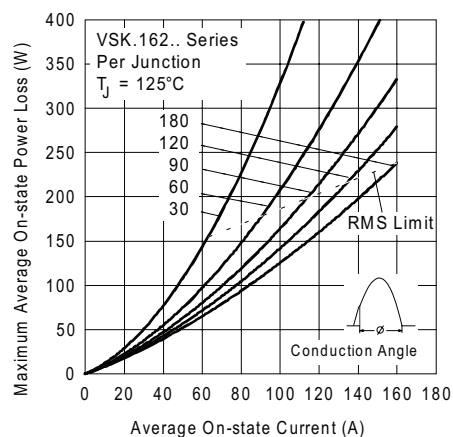


Fig. 21 - On-State Power Loss Characteristics

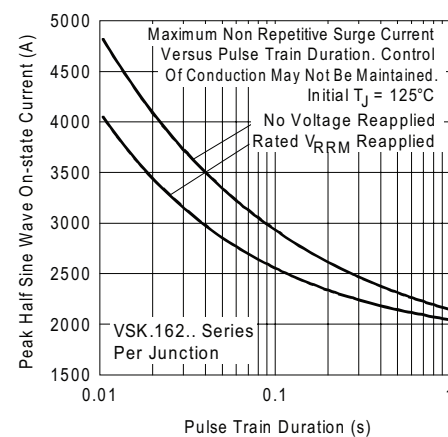


Fig. 24 - Maximum Non-Repetitive Surge Current



# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

Vishay High Power Products

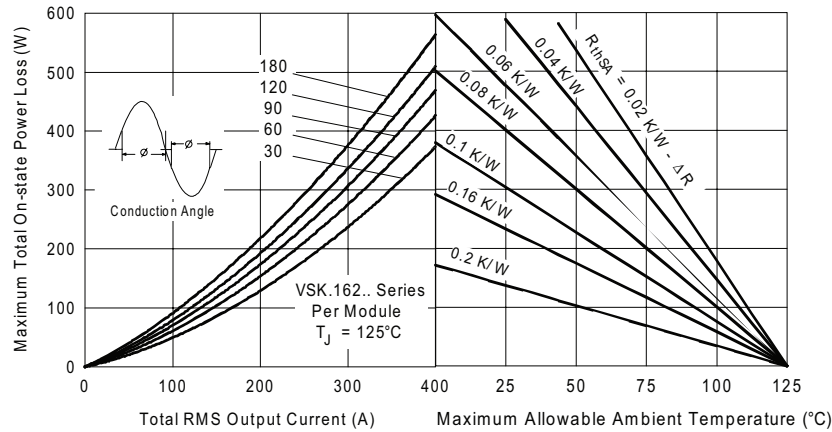


Fig. 25 - On-State Power Loss Characteristics

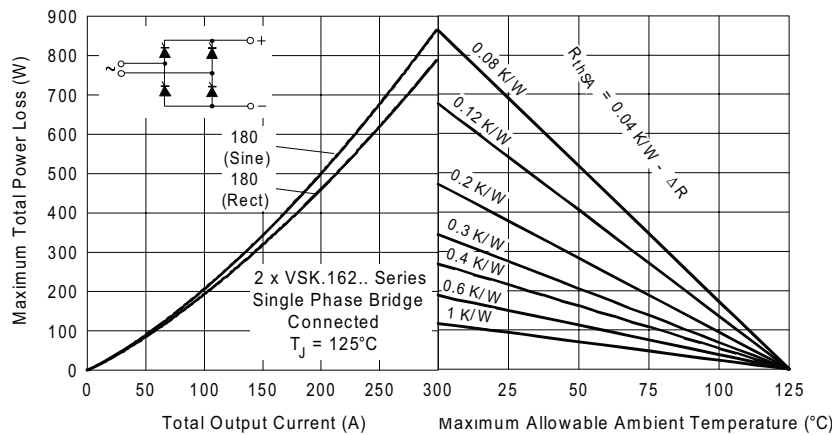


Fig. 26 - On-State Power Loss Characteristics

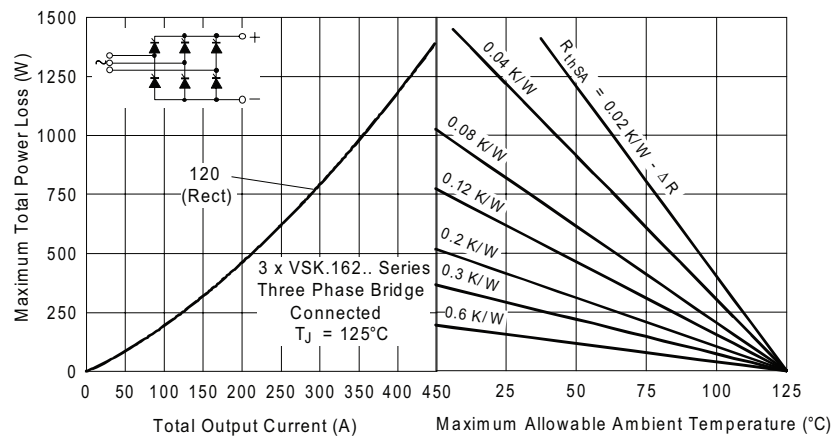


Fig. 27 - On-State Power Loss Characteristics

# VSK.136..PbF, VSK.142..PbF, VSK.162..PbF Series



Vishay High Power Products

Thyristor/Diode and  
Thyristor/Thyristor, 135 A to 160 A  
(New INT-A-PAK Power Modules)

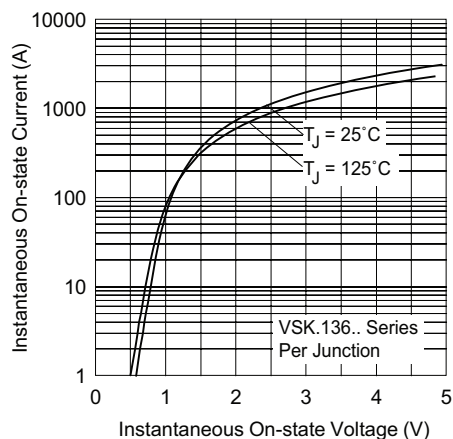


Fig. 28 - On-State Voltage Drop Characteristics

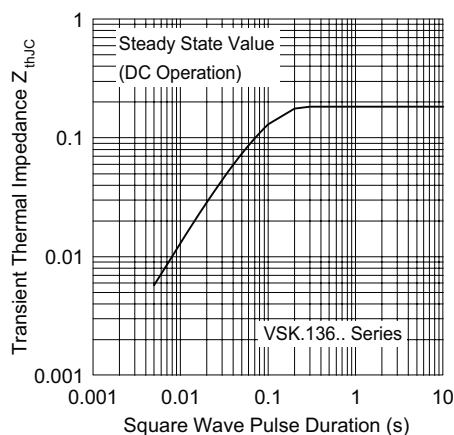


Fig. 31 - Thermal Impedance  $Z_{thJC}$  Characteristics

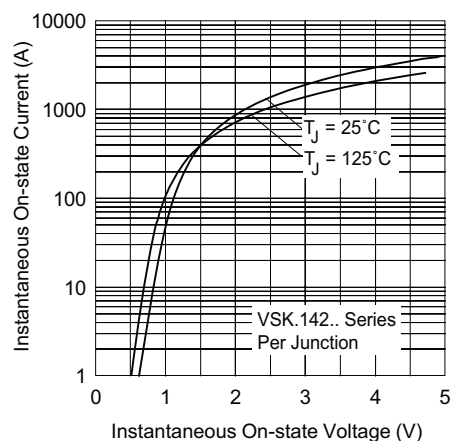


Fig. 29 - On-State Voltage Drop Characteristics

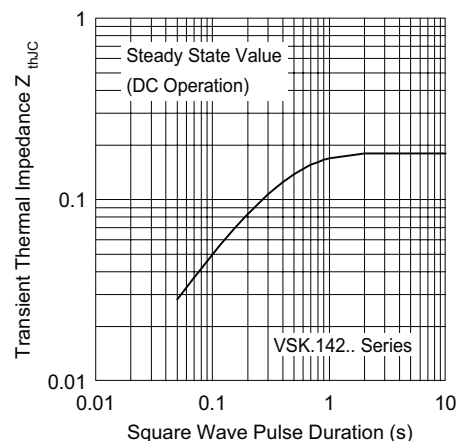


Fig. 32 - Thermal Impedance  $Z_{thJC}$  Characteristics

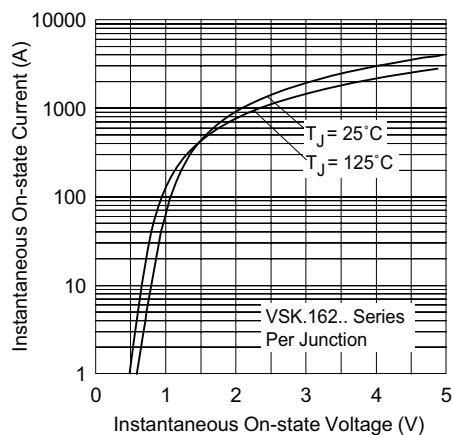


Fig. 30 - On-State Voltage Drop Characteristics

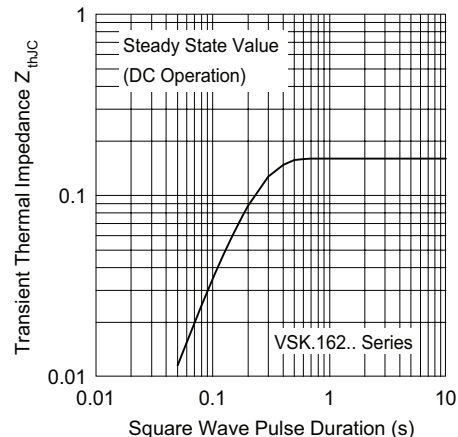


Fig. 33 - Thermal Impedance  $Z_{thJC}$  Characteristics

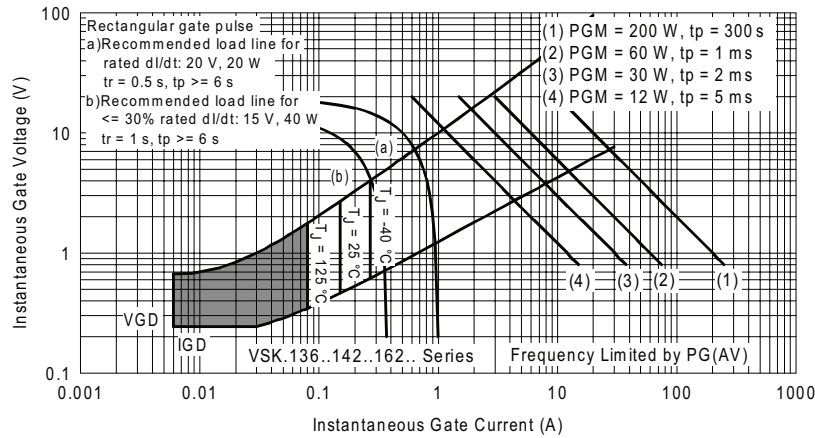


Fig. 34 - Gate Characteristics

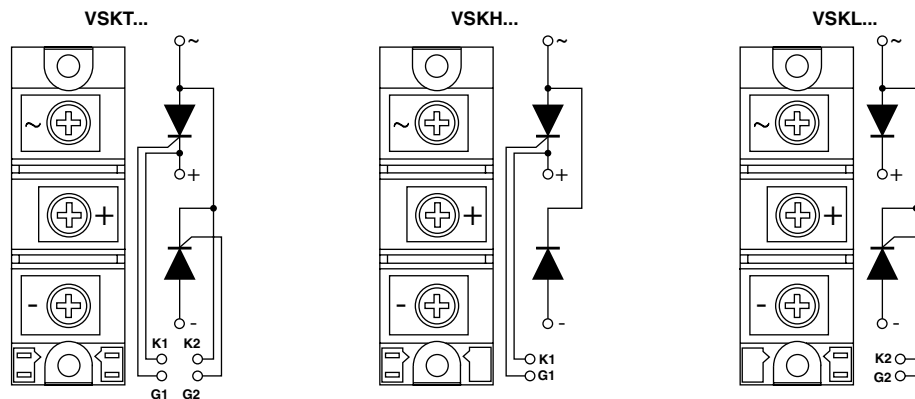
### ORDERING INFORMATION TABLE

| Device code | VSK                                 | T | 162 | / | 16 | PbF |
|-------------|-------------------------------------|---|-----|---|----|-----|
|             | 1                                   | 2 | 3   |   | 4  | 5   |
| 1           | Module type                         |   |     |   |    |     |
| 2           | Circuit configuration               |   |     |   |    |     |
| 3           | Current rating: $I_{T(AV)}$         |   |     |   |    |     |
| 4           | Voltage code $\times 100 = V_{RRM}$ |   |     |   |    |     |
| 5           | PbF = Lead (Pb)-free                |   |     |   |    |     |

#### Note

- To order the optional hardware go to [www.vishay.com/doc?95172](http://www.vishay.com/doc?95172)

### CIRCUIT CONFIGURATION



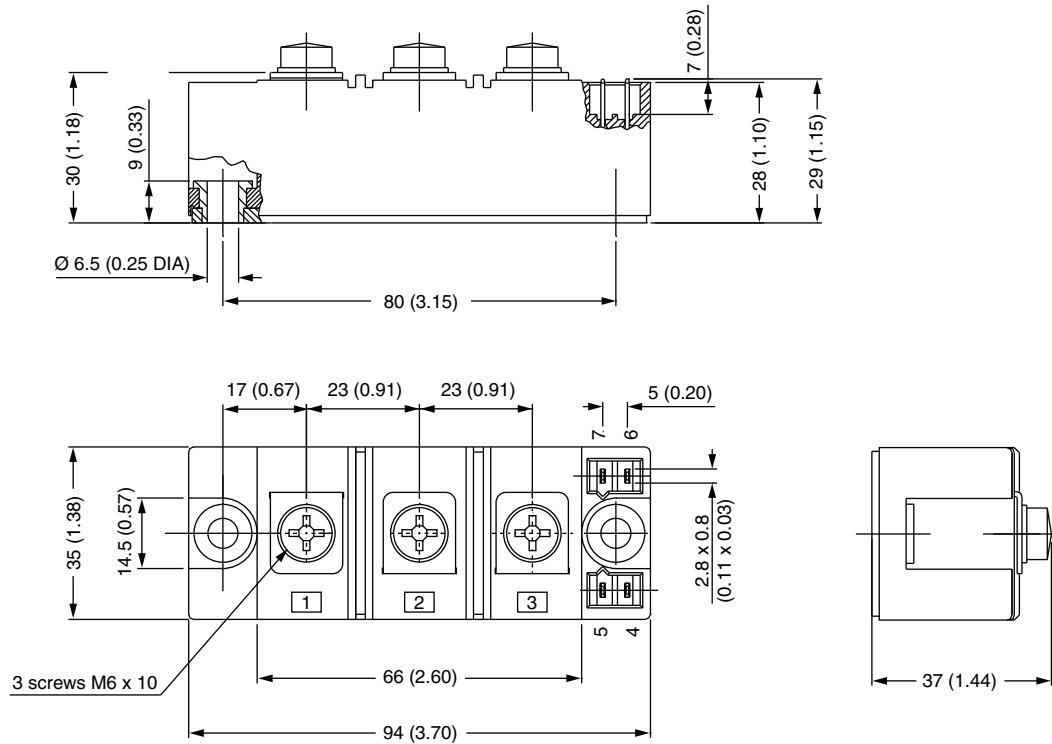
### LINKS TO RELATED DOCUMENTS

Dimensions

[www.vishay.com/doc?95067](http://www.vishay.com/doc?95067)

## INT-A-PAK IGBT/Thyristor

**DIMENSIONS** in millimeters (inches)





## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

## Material Category Policy

**Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.**

**Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.**

**Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.**

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



Тел: +7 (812) 336 43 04 (многоканальный)

Email: [org@lifeelectronics.ru](mailto:org@lifeelectronics.ru)

[www.lifeelectronics.ru](http://www.lifeelectronics.ru)