NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Datasheet

Parameter	Value
V <sub>CC</sub>	50V
I <sub>C(MAX.)</sub>	100mA
R <sub>1</sub>	47kΩ
R <sub>2</sub>	22kΩ

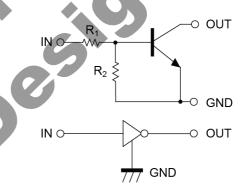
### Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy:
- 5) Complementary PNP Types: DTA144W series
- 6) Lead Free/RoHS Compliant.

# DTC144WE SOT-416(SC-75A) OUT UMT3 OUT GND DTC144WUA SOT-323(SC-70)



### Inner circuit



# Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

# Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC144WE	EMT3	1616	TL	180	8	3000	86
DTC144WUA	UMT3	2021	T106	180	8	3000	86
DTC144WKA	SMT3	2928	T146	180	8	3000	86

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter			Values	Unit	
Supply voltage		V <sub>cc</sub>	50	V	
Input voltage			-10 to 40	V	
Output current		Io	30	mA	
Collector current	Collector current		100	mA	
	DTC144WE		150	mW	
Power dissipation	DTC144WUA	P <sub>D</sub> *2	200		
	DTC144WKA		200		
Junction temperature		Tj	150	°C	
Range of storage temperature		T <sub>stg</sub>	-55 to +150	°C	

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

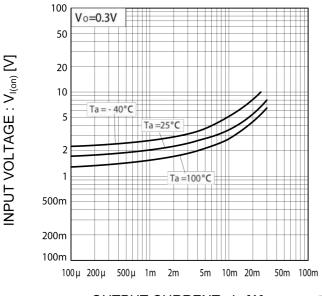
●Electrical characteristics (T <sub>a</sub>	= 25°C)					
Davanatas	Currente e l	Conditions		Values		1.1:4
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input valtage	V <sub>I(off)</sub>	$V_{CC} = 5V, I_{O} = 100\mu A$	1	-	0.8	V
Input voltage	V <sub>I(on)</sub>	$V_0 = 0.3V, I_0 = 2mA$	4	-	-	V
Output voltage	V <sub>O(on)</sub>	$I_{Q}/I_{I} = 10mA/0.5mA$	1	0.1	0.3	V
Input current	I	V <sub>1</sub> = 5V	ı	-	0.16	mA
Output current	I <sub>O(off)</sub>	$V_{CC} = 50V, V_{I} = 0V$	ı	-	0.5	μA
DC current gain	G	$V_{O} = 5V, I_{O} = 5mA$	56	-	ı	-
Input resistance	$R_1$	-	32.9	47	61.1	kΩ
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.37	0.47	0.57	-
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz	-	250	-	MHz

<sup>\*1</sup> Characteristics of built-in transistor

<sup>\*2</sup> Each terminal mounted on a reference footprint

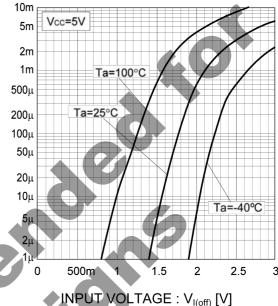
# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)



OUTPUT CURRENT : Io [A]

Fig.2 Output current vs. input voltage (OFF characteristics)



OUTPUT CURRENT : Io [A]

Fig.3 Output current vs. output voltage

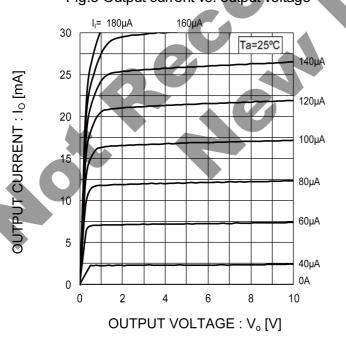
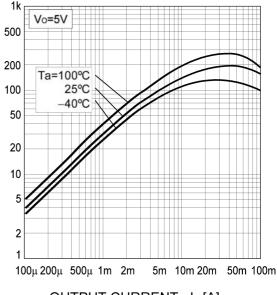


Fig.4 DC current gain vs. output current



OUTPUT CURRENT: Io [A]

OC CURRENT GAIN: G

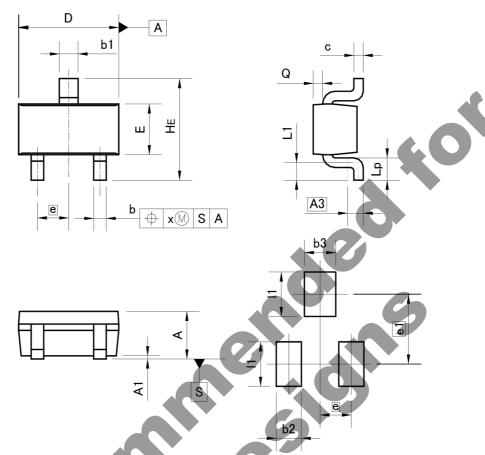
# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output voltage vs. output current



### Dimensions

EMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	TERS	INCHES	
DIM	MIN	MAX	MIN	MAX
A	0.60	0.80	0.024	0.031
A1	0.00	0.10	0.000	0.004
A3	0.2	5	0.0	110
Ь	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.010	0.016
C	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
е	0.5	0	0.020	
HE	1.40	1.80	0.055	0.071
L1	0.10	<del>=</del> :	0.004	. <del>m</del> 2
Lp	0.15	57.0	0.006	<del>17</del> 8.
Q	0.05	0.25	0.002	0.010
X	=	0.10	_	0.004

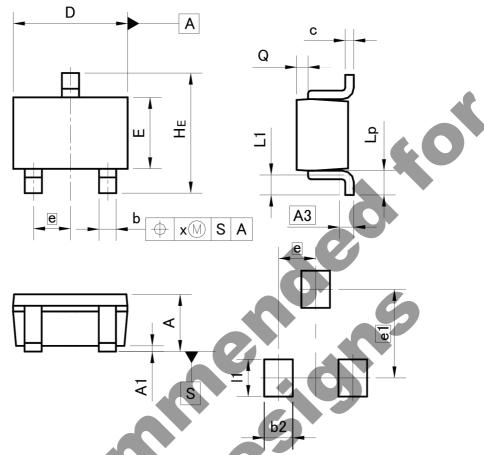
DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	₹.	0.40	-32	0.016
b3	<del></del>	0.50	÷.	0.020
e1	1.10		0.0	043
11	<del>42</del> 5	0.70	-	0.028

Dimension in mm/inches



### Dimensions

UMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
A	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.3	25	0.0	10
b	0.15	0.30	0.006	0.012
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.	65	0.026	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.020
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
×	=	0.10	9	0.004

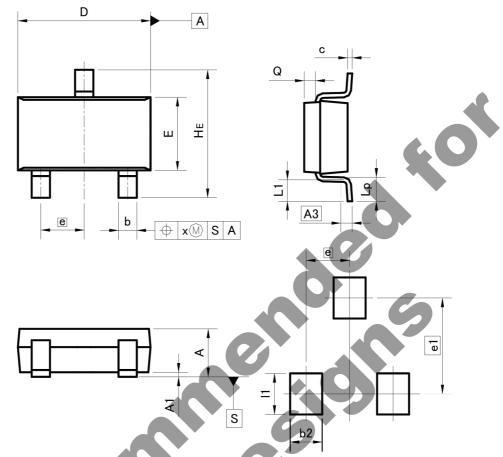
DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	-	0.50	_	0.020
e1	1.55		0.0	061
11	_	0.65	_	0.026

Dimension in mm/inches



## Dimensions

SMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.2	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.9	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
×	2	0.10	3. <u>70.</u>	0.004
У	ш)	0.10	-	0.004
DIM	MILIME	ETERS	INC	HES
	MIN	MAX	MIN	MAX
b2	_	0.60	-	0.024

Dimension in mm/inches

e1

0.035

0.083

0.90

2.10

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