

# C0G (NP0) Dielectric



## General Specifications



C0G (NP0) is the most popular formulation of the “temperature-compensating,” EIA Class I ceramic materials. Modern C0G (NP0) formulations contain neodymium, samarium and other rare earth oxides.

C0G (NP0) ceramics offer one of the most stable capacitor dielectrics available. Capacitance change with temperature is  $0 \pm 30 \text{ ppm}/^\circ\text{C}$  which is less than  $\pm 0.3\% \Delta C$  from  $-55^\circ\text{C}$  to  $+125^\circ\text{C}$ . Capacitance drift or hysteresis for C0G (NP0) ceramics is negligible at less than  $\pm 0.05\%$  versus up to  $\pm 2\%$  for films. Typical capacitance change with life is less than  $\pm 0.1\%$  for C0G (NP0), one-fifth that shown by most other dielectrics. C0G (NP0) formulations show no aging characteristics.

## PART NUMBER (see page 2 for complete part number explanation)

**0805**

**Size**  
(L" x W")

**5**

**Voltage**  
6.3V = 6  
10V = Z  
16V = Y  
25V = 3  
50V = 5  
100V = 1  
200V = 2  
500V = 7

**A**

**Dielectric**  
C0G (NP0) = A

**101**

**Capacitance Code (In pF)**  
2 Sig. Digits +  
Number of  
Zeros

**J**

**Capacitance Tolerance**  
B =  $\pm 10 \text{ pF}$  ( $< 10 \text{ pF}$ )  
C =  $\pm 25 \text{ pF}$  ( $< 10 \text{ pF}$ )  
D =  $\pm 50 \text{ pF}$  ( $< 10 \text{ pF}$ )  
F =  $\pm 1\%$  ( $\geq 10 \text{ pF}$ )  
G =  $\pm 2\%$  ( $\geq 10 \text{ pF}$ )  
J =  $\pm 5\%$   
K =  $\pm 10\%$

**A**

**Failure Rate**  
A = Not  
Applicable

**T**

**Terminations**  
T = Plated Ni  
and Sn  
7 = Gold Plated

**2**

**Packaging**  
2 = 7" Reel  
4 = 13" Reel  
7 = Bulk Cass.  
9 = Bulk

**A**

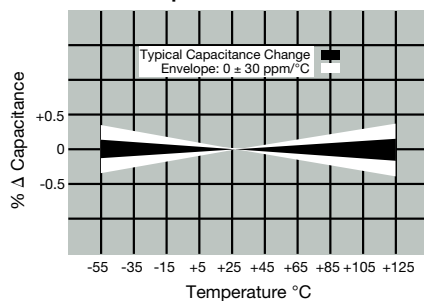
**Special Code**  
A = Std.  
Product

**Contact Factory For**  
1 = Pd/Ag Term

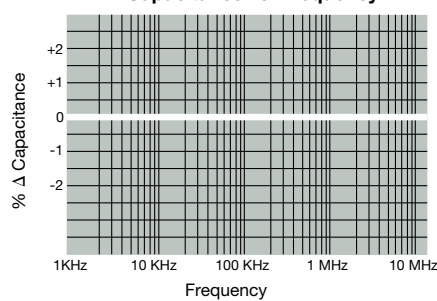
**Contact Factory For**  
Multiples

NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.  
Contact factory for non-specified capacitance values.

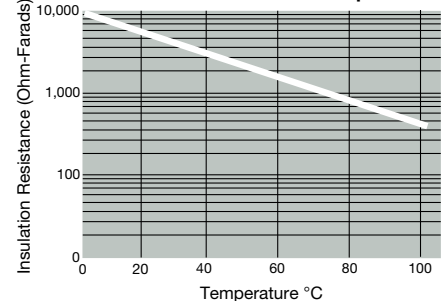
**Temperature Coefficient**



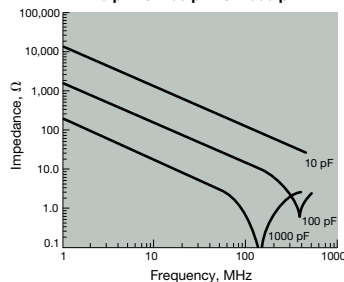
**Δ Capacitance vs. Frequency**



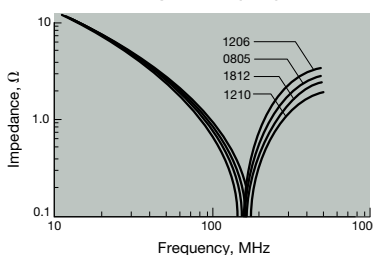
**Insulation Resistance vs Temperature**



**Variation of Impedance with Cap Value**  
Impedance vs. Frequency  
0805 - C0G (NP0)  
10 pF vs. 100 pF vs. 1000 pF



**Variation of Impedance with Chip Size**  
Impedance vs. Frequency  
1000 pF - C0G (NP0)



**Variation of Impedance with Ceramic Formulation**  
Impedance vs. Frequency  
1000 pF - C0G (NP0) vs X7R  
0805



# COG (NP0) Dielectric



## Specifications and Test Methods

Parameter/Test		NP0 Specification Limits	Measuring Conditions	
<b>Operating Temperature Range</b>		-55°C to +125°C	Temperature Cycle Chamber	
<b>Capacitance</b>		Within specified tolerance	Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF 1.0 kHz ± 10% for cap > 1000 pF Voltage: 1.0Vrms ± .2V	
<b>Q</b>		<30 pF: Q ≥ 400+20 x Cap Value ≥30 pF: Q ≥ 1000	Charge device with rated voltage for 60 ± 5 secs @ room temp/humidity	
<b>Insulation Resistance</b>		100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with 300% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.	
<b>Dielectric Strength</b>		No breakdown or visual defects	Deflection: 2mm Test Time: 30 seconds 1mm/sec 	
<b>Resistance to Flexure Stresses</b>	Appearance	No defects		
	Capacitance Variation	±5% or ±.5 pF, whichever is greater		
	Q	Meets Initial Values (As Above)		
	Insulation Resistance	≥ Initial Value x 0.3		
<b>Solderability</b>		≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds	
<b>Resistance to Solder Heat</b>	Appearance	No defects, <25% leaching of either end terminal	Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.	
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater		
	Q	Meets Initial Values (As Above)		
	Insulation Resistance	Meets Initial Values (As Above)		
<b>Thermal Shock</b>	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp	≤ 3 minutes
	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and measure after 24 hours at room temperature	
<b>Load Life</b>	Appearance	No visual defects	Charge device with twice rated voltage in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0).  Remove from test chamber and stabilize at room temperature for 24 hours before measuring.	
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater		
	Q (C=Nominal Cap)	≥ 30 pF: Q ≥ 350 ≥10 pF, <30 pF: Q ≥ 275 +5C/2 <10 pF: Q ≥ 200 +10C		
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)		
	Dielectric Strength	Meets Initial Values (As Above)		
<b>Load Humidity</b>	Appearance	No visual defects	Store in a test chamber set at 85°C ± 2°C/ 85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.  Remove from chamber and stabilize at room temperature for 24 ± 2 hours before measuring.	
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater		
	Q	≥ 30 pF: Q ≥ 350 ≥10 pF, <30 pF: Q ≥ 275 +5C/2 <10 pF: Q ≥ 200 +10C		
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)		
		Dielectric Strength	Meets Initial Values (As Above)	





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