



**Product data sheet** 

### **1. General description**

Planar passivated sensitive gate four quadrant triac in a SOT78 (TO-220AB) plastic package intended for use in general purpose bidirectional switching and phase control applications. This sensitive gate "series E" triac is intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

### 2. Features and benefits

- High blocking voltage capability
- Direct triggering from low power drivers and logic ICs
- Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants
- · Low holding current for low current loads and lowest EMI at commutation
- Sensitive gate

## **3. Applications**

- General purpose motor control
- · General purpose switching

### 4. Quick reference data

Symbol	Parameter	Conditions	Values		Unit	
Absolute	maximum rating					
$V_{\text{DRM}}$	repetitive peak off-state voltage			600		V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 102 °C; <u>Fig. 1; Fig. 2; Fig. 3</u>	8		A	
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 20 ms; <u>Fig. 4</u> ; <u>Fig. 5</u>	65		A	
Symbol	Parameter	Conditions	Min Typ Max		Unit	
Static ch	aracteristics		· ·	·		
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	2.5	10	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	4	10	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	5	10	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G+; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	11	25	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>i</sub> = 25 °C; <u>Fig. 9</u>	_	2.5	20	mA

## **5. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	
2	T2	main terminal 2	ך ⊖ ך	
3	G	gate		sym051
mb	T2	mounting base; main terminal 2		symus i

# 6. Ordering information

Table 3. Ordering inform	nation		
Type number	Package		
	Name	Description	Version
BT137-600E	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

# 7. Marking

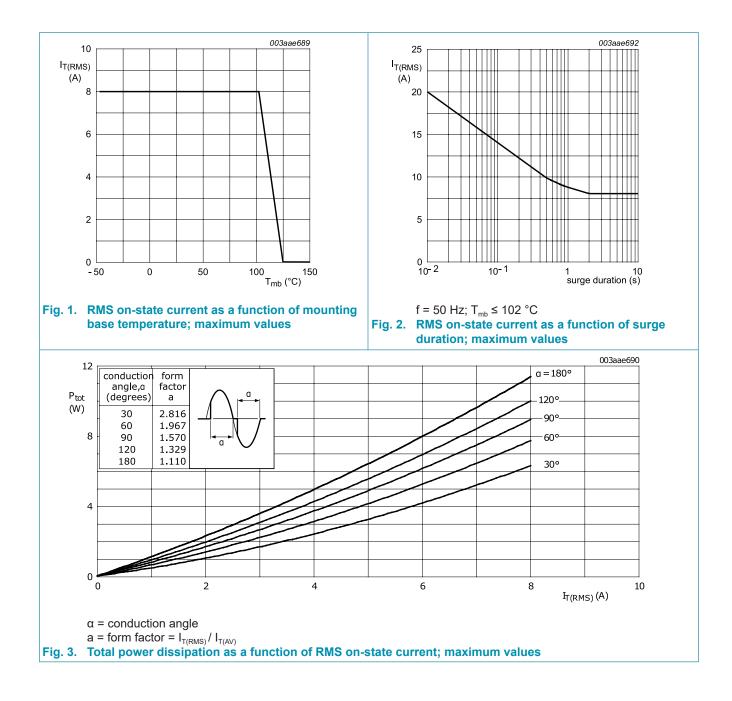
Table 4. Marking codes	
Type number	Marking codes
BT137-600E	BT137-600E

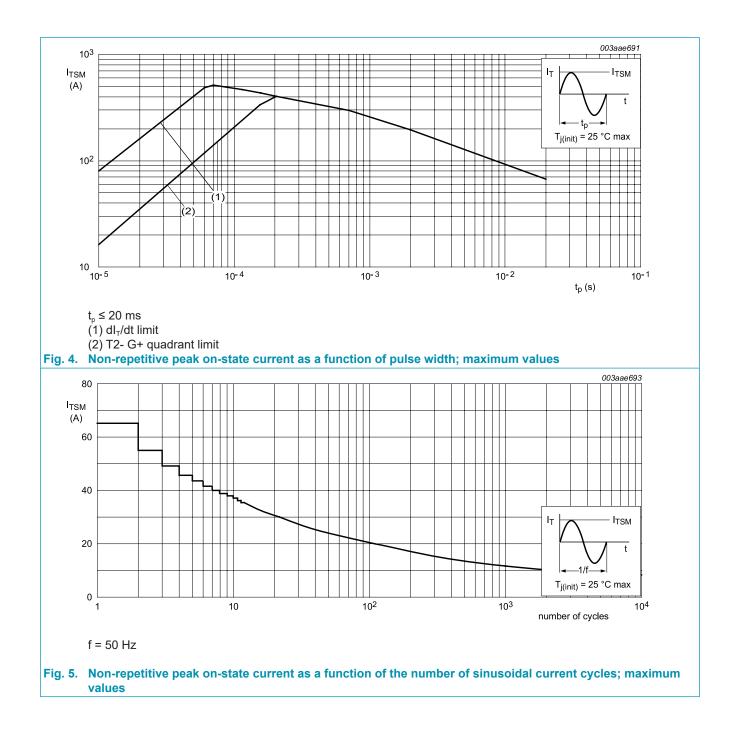
# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

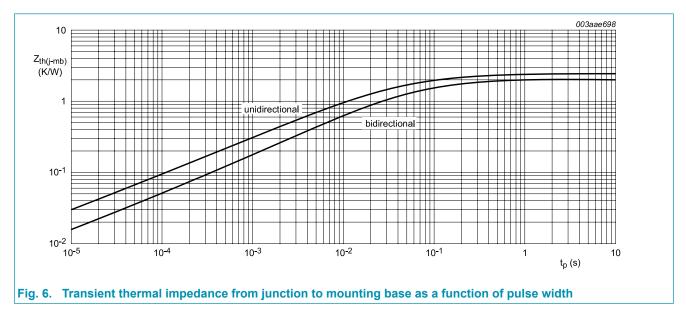
Symbol	Parameter	Conditions	Values	Unit
$V_{\text{DRM}}$	repetitive peak off-state voltage		600	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 102 °C; <u>Fig 1; Fig 2; Fig 3</u>	8	A
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 20 \text{ ms}; Fig 4; Fig 5$	65	A
		full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 16.7 \text{ ms}$	71	A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	21	A <sup>2</sup> s
dl⊤/dt	rate of rise of on-state current	I <sub>G</sub> = 20 mA; T2+ G+	50	A/µs
		I <sub>G</sub> = 20 mA; T2+ G-	50	A/µs
		I <sub>G</sub> = 20 mA; T2- G-	50	A/µs
		I <sub>G</sub> = 50 mA; T2- G+	10	A/µs
I <sub>GM</sub>	peak gate current		2	А
P <sub>GM</sub>	peak gate power		5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	0.5	W
T <sub>stg</sub>	storage temperature		-40 to 150	°C
Tj	junction temperature		125	°C





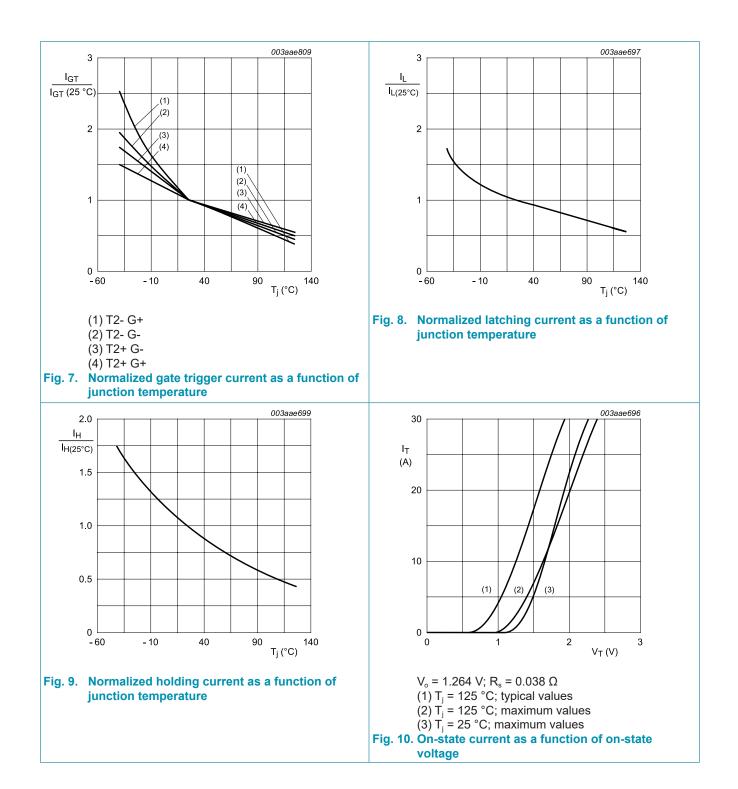
## 9. Thermal characteristics

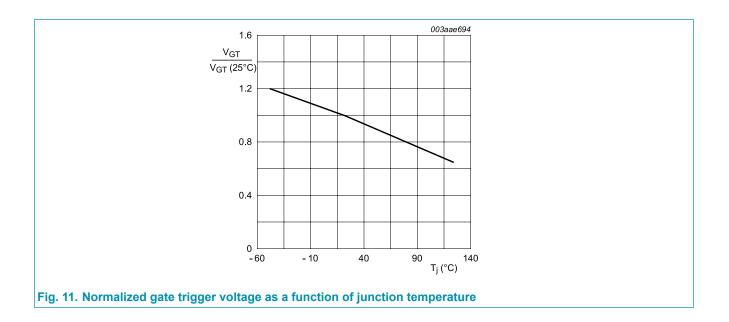
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance	full cycle; <u>Fig 6</u>	-	-	2	K/W
	from junction to mounting base	half cycle; <u>Fig 6</u>	-	-	2.4	K/W
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



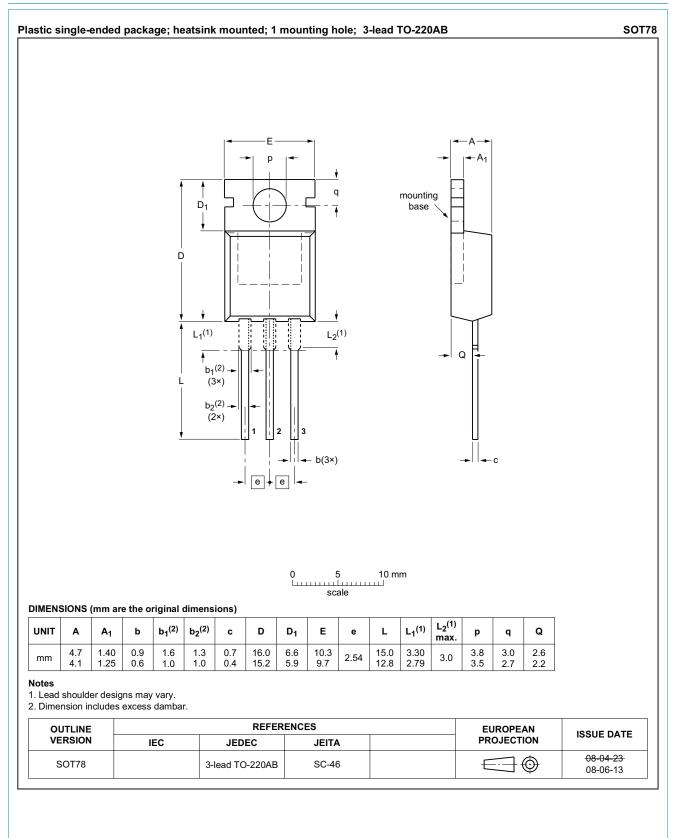
## **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics	· · · · · ·				
Ι <sub>GT</sub>	gate trigger current	$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2+ G+};$ $T_{j} = 25 \text{ °C}; \text{ Fig. 7}$	-	2.5	10	mA
		$V_{D}$ = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	4	10	mA
		$V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}; \text{ T2- G-};$ $T_{j} = 25 \text{ °C}; \text{ Fig. 7}$	-	5	10	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \text{ Fig. 7}$	-	11	25	mA
IL	latching current	$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2+ G+};$ T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	3	25	mA
		$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2+ G-};$ T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	14	35	mA
		$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2- G-};$ T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	3	25	mA
		$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2- G+};$ T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	4	35	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	2.5	20	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 10 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.3	1.65	V
V <sub>GT</sub>	gate trigger voltage	$V_{D} = 12 \text{ V}; \text{ I}_{T} = 0.1 \text{ A}; \text{ T}_{j} = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		V <sub>D</sub> = 400 V; I <sub>T</sub> = 0.1 A; T <sub>j</sub> = 125 °C; Fig. 11	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 600 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mA
Dynamic	characteristics	· · · · ·	I			
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 402 V; T <sub>j</sub> = 125 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	-	50	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM} = 12 \text{ A}; V_D = 600 \text{ V}; I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu\text{s}$	-	2	-	μs





## **11. Package outline**



## 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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