

## 1. General description

Planar passivated Silicon Controlled Rectifier with sensitive gate in a SOT54 (TO-92) plastic package. This SCR is designed to be interfaced directly to microcontrollers, logic ICs and other low power gate trigger circuits.

### 2. Features and benefits

- · Planar passivated for voltage ruggedness and reliability
- Sensitive gate
- Direct triggering from low power gate circuits and logic ICs

### 3. Applications

- Ignition circuits
- Lighting ballasts
- Protection circuits
- Switched Mode Power Supplies

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RRM</sub>	repetitive peak reverse voltage			-	-	400	V
I <sub>T(AV)</sub>	average on-state current	half sine wave; T <sub>lead</sub> ≤ 83 °C; <u>Fig. 1</u>		-	-	0.5	A
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; T <sub>lead</sub> ≤  83 °C; <u>Fig. 2;</u> <u>Fig. 3</u>		-	-	0.8	A
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 10 ms; <u>Fig. 4; Fig. 5</u>		-	-	8	A
		half sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 8.3 ms		-	-	9	A
Tj	junction temperature			-	-	125	°C
Static chara	acteristics						
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 10 mA; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>		-	50	200	μA
Dynamic ch	naracteristics		· · · · ·				
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 268 V; T <sub>j</sub> = 125 °C; R <sub>GK</sub> = 1 kΩ; exponential waveform; Fig. 12		500	800	-	V/µs

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_{DM}$ = 268 V; T <sub>j</sub> = 125 °C; exponential waveform; gate open circuit; Fig. 12	-	25	-	V/µs

## 5. Pinning information

Table 2. F	Pinning inf	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A	anode		А <del>- Д -</del> К
2	G	gate		G sym037
3	К	cathode	TO-92 (SOT54)	

# 6. Ordering information

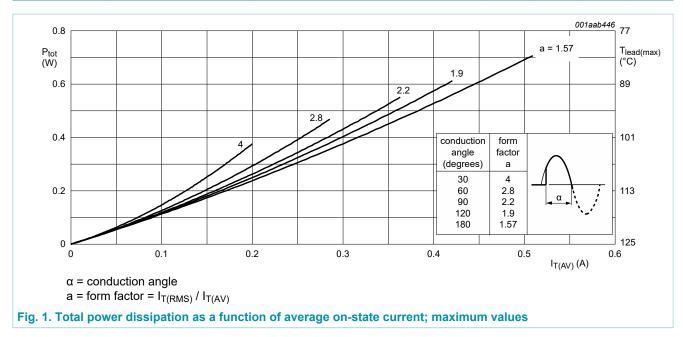
Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
BT169D	TO-92	plastic single-ended leaded (through hole) package; 3 leads	SOT54			

## 7. Limiting values

#### Table 4. Limiting values

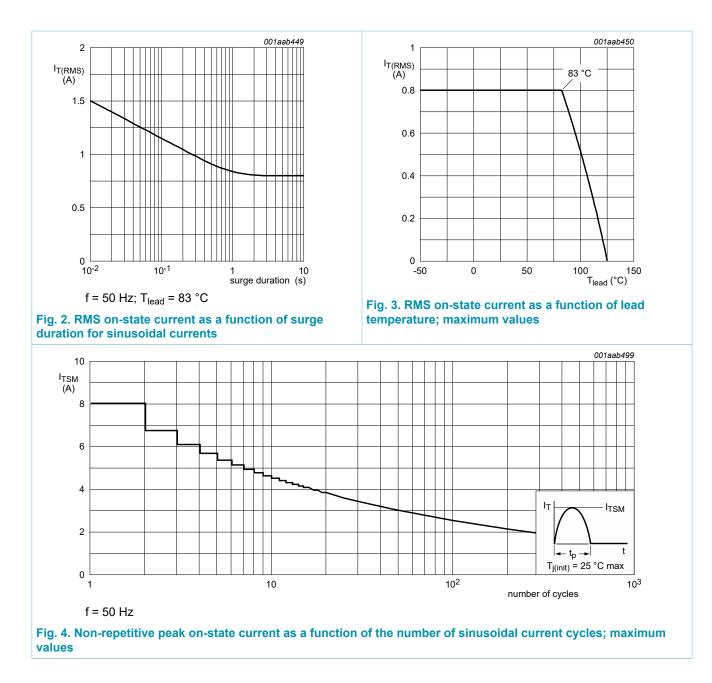
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	400	V
V <sub>RRM</sub>	repetitive peak reverse voltage		-	400	V
I <sub>T(AV)</sub>	average on-state current	half sine wave; T <sub>lead</sub> ≤ 83 °C; <u>Fig. 1</u>	-	0.5	А
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; T <sub>lead</sub> ≤ 83 °C; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	0.8	А
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)} = 25 \text{ °C}$ ; $t_p = 10 \text{ ms}$ ; Fig. 4; Fig. 5	-	8	A
		half sine wave; T <sub>j(init)</sub> = 25 °C; t <sub>p</sub> = 8.3 ms	-	9	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	-	0.32	A²s
dl <sub>T</sub> /dt	rate of rise of on-state current	I <sub>T</sub> = 2 A; I <sub>G</sub> = 10 mA; dI <sub>G</sub> /dt = 100 mA/μs	-	50	A/µs
I <sub>GM</sub>	peak gate current		-	1	Α
V <sub>RGM</sub>	peak reverse gate voltage		-	5	V
P <sub>GM</sub>	peak gate power		-	2	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.1	W
T <sub>stg</sub>	storage temperature		-40	150	°C
T <sub>j</sub>	junction temperature		-	125	°C



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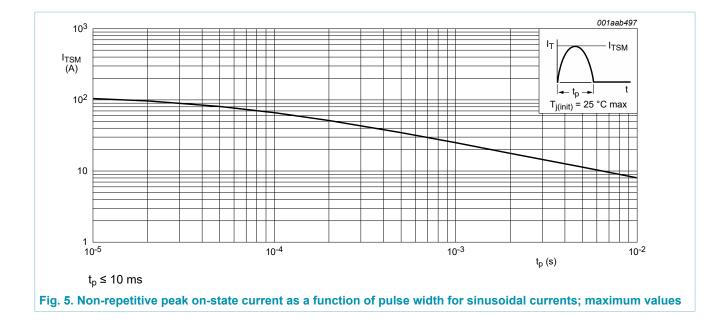
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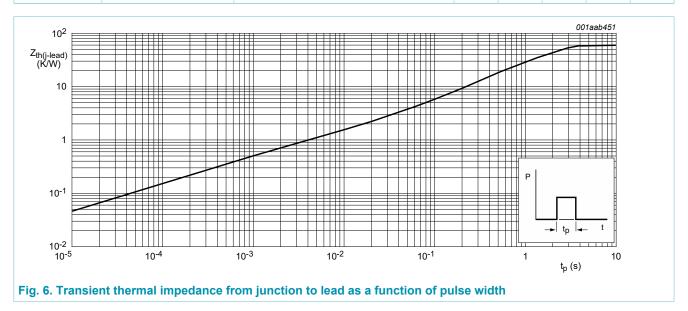
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### 8. Thermal characteristics

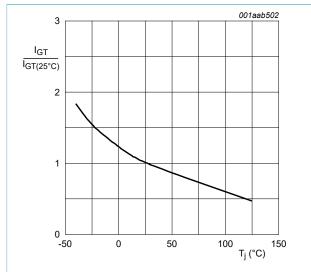
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-lead)</sub>	thermal resistance from junction to lead	<u>Fig. 6</u>	-	-	60	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	printed circuit board mounted: lead length = 4 mm	-	150	-	K/W



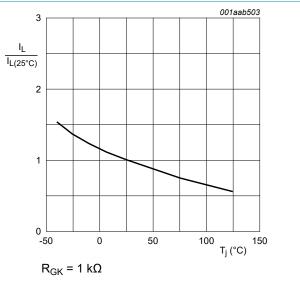
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#### 9. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 10 mA; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	50	200	μA
l	latching current	V <sub>D</sub> = 12 V; I <sub>G</sub> = 0.5 mA; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	2	6	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	2	5	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 1.2 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.25	1.7	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 10 mA; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	0.5	0.8	V
		V <sub>D</sub> = 400 V; I <sub>T</sub> = 10 mA; T <sub>j</sub> = 125 °C; <u>Fig. 11</u>	0.2	0.3	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 400 V; T <sub>j</sub> = 125 °C	-	0.05	0.1	mA
I <sub>R</sub>	reverse current	V <sub>R</sub> = 400 V; T <sub>j</sub> = 125 °C	-	0.05	0.1	mA
Dynamic ch	naracteristics	· · · · · · · · · · · · · · · · · · ·				
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 268 V; T <sub>j</sub> = 125 °C; R <sub>GK</sub> = 1 kΩ; exponential waveform; <u>Fig. 12</u>	500	800	-	V/µs
		$V_{DM}$ = 268 V; T <sub>j</sub> = 125 °C; exponential waveform; gate open circuit; <u>Fig. 12</u>	-	25	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$\begin{split} I_{TM} &= 2 \text{ A};  V_D = 400 \text{ V};  I_G = 10  \text{mA};  \text{d} I_G / \\ \text{d} t &= 0.1  \text{A} / \mu \text{s};  \text{T}_j = 25 ^\circ \text{C} \end{split}$	-	2	-	μs
t <sub>q</sub>	commutated turn-off time	$V_{DM}$ = 268 V; T <sub>j</sub> = 125 °C; I <sub>TM</sub> = 1.6 A; $V_R$ = 35 V; (dI <sub>T</sub> /dt) <sub>M</sub> = 30 A/µs; dV <sub>D</sub> / dt = 2 V/µs; R <sub>GK(ext)</sub> = 1 kΩ	-	100	-	μs



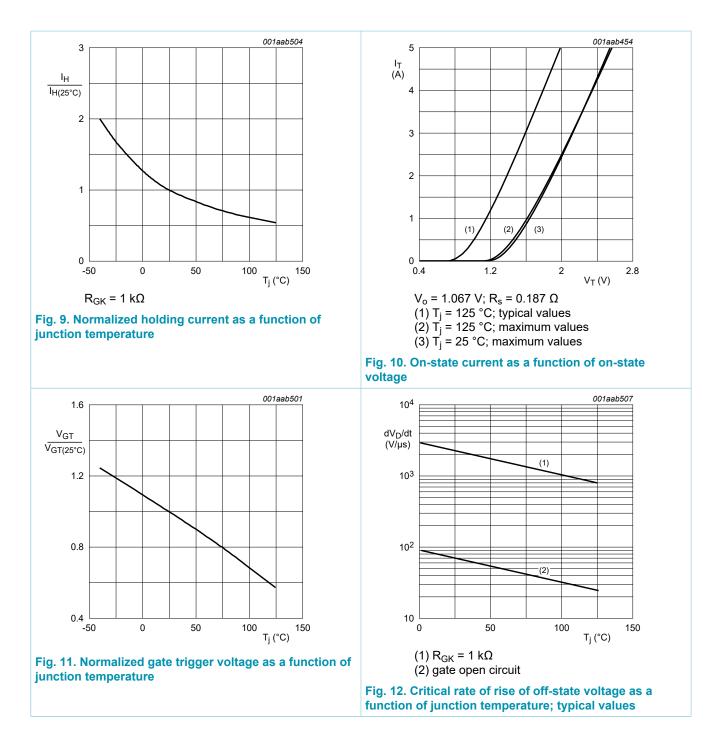






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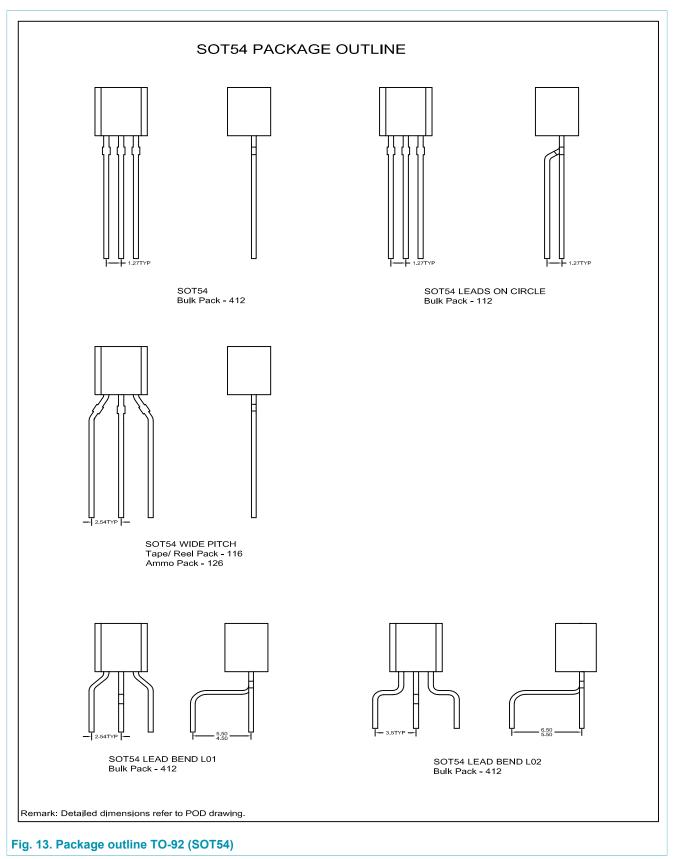


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BT169D SCR

## 10. Package outline



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## 11. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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### **12. Contents**

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Limiting values	3
8.	Thermal characteristics	6
9.	Characteristics	7
10	Package outline	9
11	. Legal information	10

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