

Low Profile, High Current Dual Inductors



Manufactured under one or more of the following:
US Patents; 6,198,375/6,204,744/6,449,829/6,460,244.
 Several foreign patents, and other patents pending.

STANDARD ELECTRICAL SPECIFICATIONS					
L_0 INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A (μH)	DCR TYP. 25 °C (m Ω)	DCR MAX. 25 °C (m Ω)	HEAT RATING CURRENT DC TYP. (A) ⁽³⁾	SATURATION CURRENT DC TYP. (A) ⁽⁴⁾	SRF TYP. (MHz)
10	50.0	53.50	3.65	5.2	13.0
15	62.0	66.34	4.8	5.5	10.0
22	103.0	110.21	3.7	3.8	9.0
33	149.0	159.43	3.1	3.2	6.1

Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range -55 °C to +155 °C
- (3) DC current (A) that will cause an approximate ΔT of 40 °C
- (4) DC current (A) that will cause L_0 to drop approximately 20 %
- (5) The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

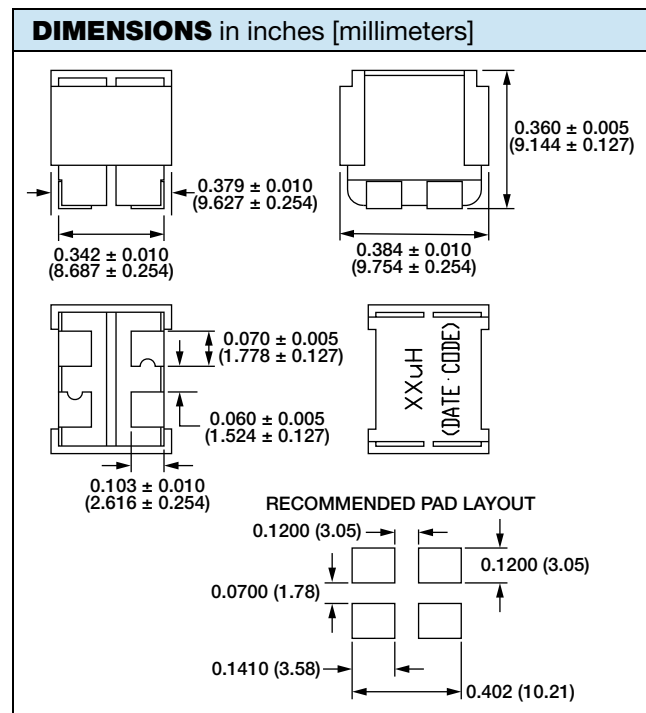
FEATURES

- Two inductors in one package
- High temperature, up to 155 °C
- Shielded construction
- Optimal design realizes high quality sound and low distortion
- Low coupling for minimal cross-talk between inductors
- Frequency range up to 1 MHz
- Lowest DCR/ μH , in this package size
- Handles high transient current spikes without saturation
- Ultra-low buzz noise, due to composite construction
- AEC-Q200 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

- Class D audio amplifiers



DESCRIPTION					
IHLD-3232HB-5A	100 μH	$\pm 20\%$	ER	e3	
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD	

GLOBAL PART NUMBER																	
I	H	L	D	3	2	3	2	H	B	E	R	1	0	1	M	5	A
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES			



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