74AHCT17A Hex buffer Schmitt trigger Rev. 1 — 18 May 2017

Product data sheet

General description

The 74AHCT17A is a hex buffer with Schmitt-trigger inputs, capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

Designed to operate over a V_{CC} range from 4.5 V to 5.5 V, the inputs are TTL compatible. This range allows the device to be used to translate from 3.3 V to 5 V.

This device is fully specified for partial Power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

Features and benefits

- · Direct interface with TTL levels
- Supply voltage range from 4.5 V to 5.5 V
- Typical t_{pd} of 3.2 ns at 5 V
- Typical $V_{OL(p)}$ < 0.8 V at V_{CC} = 5 V, T_{amb} = 25 °C
- Typical $V_{OH(v)} > 2.3 \text{ V}$ at $V_{CC} = 5 \text{ V}$, $T_{amb} = 25 ^{\circ}\text{C}$
- Supports mixed-mode voltage operation on all ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
 - MM JESD22-A115-A exceeds 150 V
 - CDM JESD22-C101E exceeds 2 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

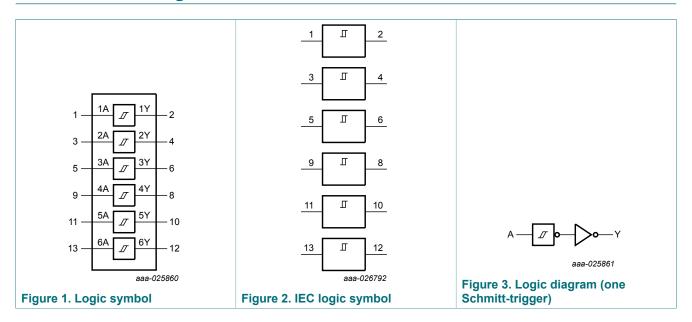
Ordering information

Table 1. Ordering information

Type number	Package	Package									
	Temperature range	Name	Description	Version							
74AHCT17APW	-40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1							

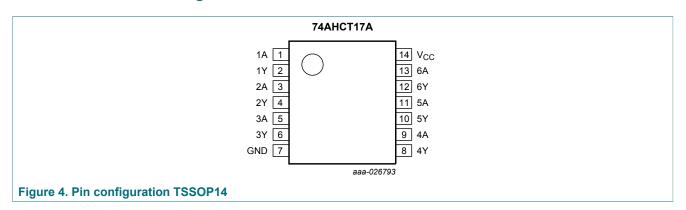


4 Functional diagram



5 Pinning information

5.1 Pinning



5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description							
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	data input							
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	data output							
GND	7	ground (0 V)							
V _{CC}	14	supply voltage							

74AHCT17A

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6 Functional description

Table 3. Function table ^[1]

Input	Output
nA	nY
L	L
Н	Н

^[1] H = HIGH voltage level;

7 Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
VI	input voltage		[1]	-0.5	+7.0	V
Vo	output voltage	output HIGH or LOW state	[2] [3]	-0.5	V _{CC} + 0.5	V
		output power-down	[2]	-0.5	+7.0	V
I _{IK}	input clamping current	V _I < 0 V		-20	-	mA
I _{OK}	output clamping current	V _O < 0 V		-20	-	mA
Io	output current	$V_O = 0 V \text{ to } V_{CC}$		-	±25	mA
I _{CC}	supply current			-	75	mA
I _{GND}	ground current			-75	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C	[4]	-	500	mW

^[1] If the input current ratings are observed, the minimum input voltage ratings may be exceeded.

8 Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	output HIGH or LOW state	0	-	V _{CC}	V
		output power-down	0	-	5.5	V
T _{amb}	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 5.0 V ± 0.5 V	-	-	1	ms/V

74AHCT17A

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L = LOW voltage level.

^[2] If the output current ratings are observed, the output voltage ratings may be exceeded.

^[3] This value is limited to 7 V maximum.

^[4] For TSSOP14 package: above 75 °C, the value of Ptot derates linearly at 7 mW/K.

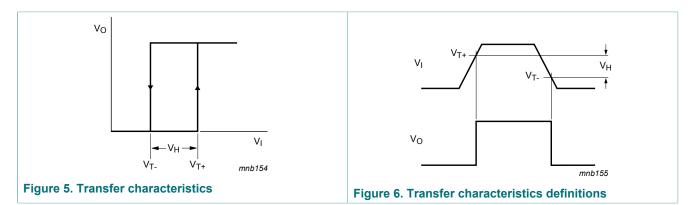
9 Static characteristics

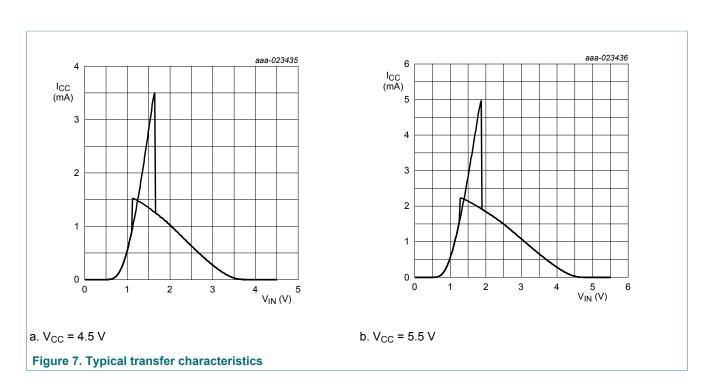
Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C	;	-40 °C to +85 °C		-40 °C to	Unit	
			Min	Тур	Max	Min	Max	Min	Max	
V_{T+}	positive-going	V _{CC} = 4.5 V	-	-	1.9	-	1.9	-	1.9	٧
	threshold voltage	V _{CC} = 5.5 V	-	-	2.1	-	2.1	-	2.1	V
V _{T-}	negative-going	V _{CC} = 4.5 V	0.5	-	-	0.5	-	0.5	-	V
threshold voltage		V _{CC} = 5.5 V	0.6	-	-	0.6	-	0.6	-	V
V _H	hysteresis	V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.4	1.4	V
	voltage	V _{CC} = 5.5 V	0.4	-	1.5	0.4	1.5	0.4	1.5	V
V _{OH}	HIGH-level	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = -50 μA	4.4	-	_	4.4	-	4.4	-	V
		I _O = -8 mA	3.94	-	_	3.8	-	3.7	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$								
		Ι _Ο = 50 μΑ	-	-	0.1	-	0.1	-	0.1	V
		I _O = 8 mA	-	-	0.36	-	0.44	-	0.55	V
I _{OFF}	power-off leakage current	V_1 or V_0 = GND to 5.5 V; V_{CC} = 0 V	-	-	0.5	-	5	-	5	μΑ
lı	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0 \text{ V to } 5.5 \text{ V}$	-	-	±0.1	-	±1	-	±1	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	2	-	20	-	20	μΑ
ΔI _{CC}	additional supply current	per input pin; V_{CC} = 5.5 V; V_{I} = 3.4 V; I_{O} = 0 A; other pins at V_{CC} or GND	-	-	1.35	-	1.5	-	1.5	mA

9.1 Transfer characteristics waveforms





10 Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. For test circuit, see Figure 9.

Symbol	Parameter	Conditions			25 °C		-40 °C to	+85 °C	-40 °C to	Unit	
				Min	Typ ^[1]	Max	Min	Max	Min	Max	
t _{pd}	propagation	nA to nY; see Figure 8	[2]								
delay	V _{CC} = 4.5 V to 5.5 V										
		C _L = 15 pF		-	3.2	8.3	1	9.9	1	10.6	ns
		C _L = 50 pF		-	5.0	9.8	1	11.3	1	12.1	ns
Cı	input capacitance	$V_I = V_{CC}$ or GND; $V_{CC} = 5 V$		-	2	6	-	6	-	6	pF
Co	output capacitance	$V_O = V_{CC}$ or GND; $V_{CC} = 5 \text{ V}$		-	5	-	-	-	-	-	pF
C _{PD}	power dissipation capacitance	per buffer; $C_L = 0$ pF; f = 10 MHz; $V_I = GND$ to V_{CC}	[3]	-	9.8	-	-	-	-	-	pF

Table 8. Noise characteristics

GND = 0 V. For test circuit, see Figure 9.

Symbol	Parameter	Conditions	Ta	_{imb} = 25 °	Unit	
			Min	Тур	Max	
V _{CC} = 5 \	; C _L = 50 pF					
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.4	0.8	V
$V_{OL(v)}$	LOW-level output voltage (valley)		-0.8	-0.2	-	V
V _{OH(v)}	HIGH-level output voltage (valley)		-	4.5	-	V
V _{IH(AC)}	AC HIGH-level input voltage		2	-	-	V
V _{IL(AC)}	AC LOW-level input voltage		-	-	0.8	V

Typical values are measured at T_{amb} = 25 °C and V_{CC} = 5 V. t_{pd} is the same as t_{PLH} and t_{PHL} . C_{PD} is used to determine the dynamic power dissipation P_D (µW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in Volts.

10.1 Waveforms and test circuit

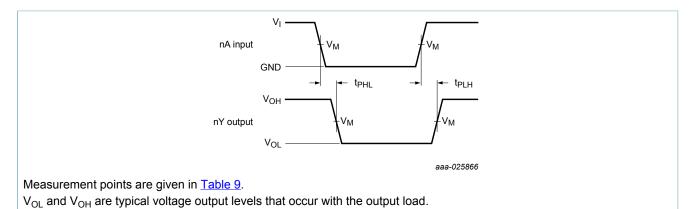
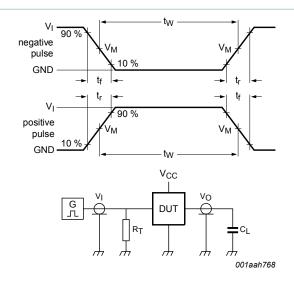


Table 9. Measurement points

Figure 8. Propagation delay input (nA) to output (nY)

Input	Output
V_{M}	V_{M}
1.5 V	0.5V _{CC}



Test data is given in Table 10.

Definitions test circuit:

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator

 \mathbf{C}_{L} = Load capacitance including jig and probe capacitance

Figure 9. Test circuit for measuring switching times

Table 10. Test data

Input		Load	Test
VI	t _r , t _f	CL	
GND to 3 V	3.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}

11 Package outline

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm SOT402-1 pin 1 index detail X - (w (M) DIMENSIONS (mm are the original dimensions)

						-,												
UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E (2)	е	HE	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.19	0.2 0.1	5.1 4.9	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.72 0.38	8° 0°

Notes

- Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOT402-1		MO-153			99-12-27 03-02-18	

Figure 10. Package outline SOT402-1 (TSSOP14)

74AHCT17A

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12 Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

13 Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHCT17A v.1	20170518	Product data sheet	-	-

14 Legal information

14.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Contents

1	General description	1
2	Features and benefits	
3	Ordering information	1
4	Functional diagram	2
5	Pinning information	2
5.1	Pinning	
5.2	Pin description	
6	Functional description	
7	Limiting values	
8	Recommended operating conditions	3
9	Static characteristics	4
9.1	Transfer characteristics waveforms	5
10	Dynamic characteristics	6
10.1	Waveforms and test circuit	
11	Package outline	9
12	Abbreviations	10
13	Revision history	10
14	Legal information	11

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