

Insulated Ultrafast Rectifier Module, 130 A



SOT-227

PRIMARY CHARACTERISTICS

V_R	600 V
$I_{F(AV)}$ per module at $T_C = 92\text{ }^\circ\text{C}$	130 A
t_{rr}	37 ns
Type	Modules - diode FRED Pt®
Package	SOT-227

FEATURES

- Two fully independent diodes
- Fully insulated package
- Ultrafast, soft reverse recovery, with high operation junction temperature ($T_J \text{ max.} = 175\text{ }^\circ\text{C}$)
- Low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- Industry standard outline
- UL approved file E78996 
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

DESCRIPTION / APPLICATIONS

The VS-UFB130FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V_R		600	V
Continuous forward current per diode	I_F	$T_C = 85\text{ }^\circ\text{C}$	82	A
Single pulse forward current per diode	I_{FSM}	$T_C = 25\text{ }^\circ\text{C}$	750	
Maximum power dissipation per module	P_D	$T_C = 85\text{ }^\circ\text{C}$	246	W
RMS isolation voltage	V_{ISOL}	Any terminal to case, $t = 1\text{ min}$	2500	V
Operating junction and storage temperatures	T_J, T_{Stg}		-55 to +175	$^\circ\text{C}$

**ELECTRICAL SPECIFICATIONS PER DIODE** ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	$I_R = 100\text{ }\mu\text{A}$	600	-	-	
Forward voltage	V_{FM}	$I_F = 60\text{ A}$	-	1.43	1.80	V
		$I_F = 60\text{ A}, T_J = 125\text{ }^{\circ}\text{C}$	-	1.23	1.48	
		$I_F = 120\text{ A}$	-	1.66	2.10	
		$I_F = 120\text{ A}, T_J = 125\text{ }^{\circ}\text{C}$	-	1.50	1.82	
Reverse leakage current	I_{RM}	$V_R = V_R\text{ rated}$	-	0.1	50	μA
		$T_J = 175\text{ }^{\circ}\text{C}, V_R = V_R\text{ rated}$	-	0.20	1	mA
Junction capacitance	C_T	$V_R = 600\text{ V}$	-	43	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1.0\text{ A}, dI_F/dt = 200\text{ A}/\mu\text{s}, V_R = 30\text{ V}$	-	37	-	ns
		$T_J = 25\text{ }^{\circ}\text{C}$	-	79	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	164	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	6	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	15	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	230	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	1220	-	

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction-to-case, single leg conducting	R_{thJC}		-	-	0.73	$^{\circ}\text{C}/\text{W}$
Junction-to-case, both leg conducting			-	-	0.365	
Case to heatsink	R_{thCS}	Flat, greased surface	-	0.10	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style			SOT-227			

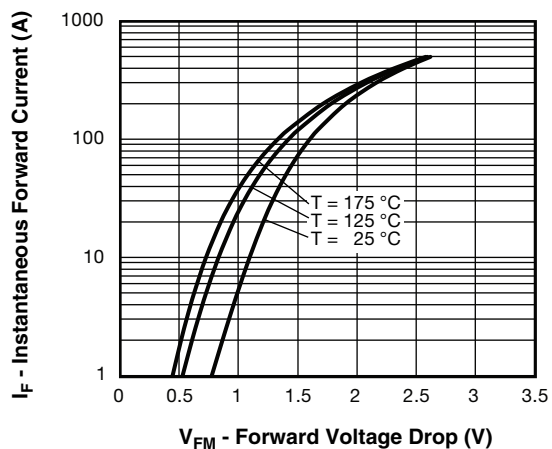


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

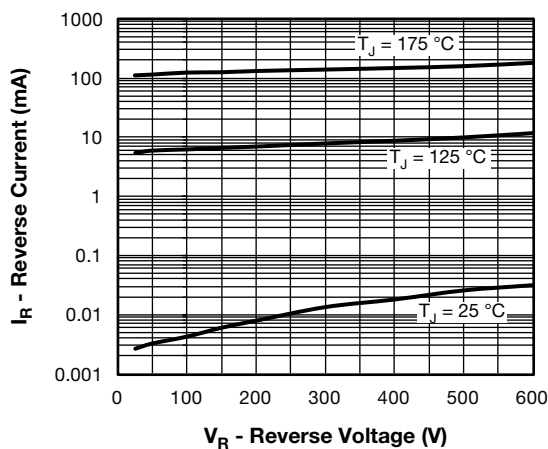


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

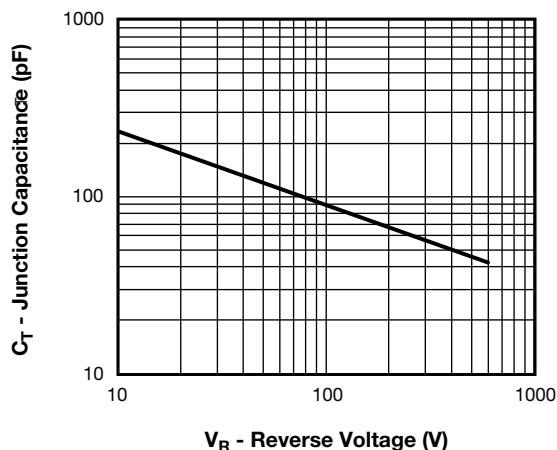


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

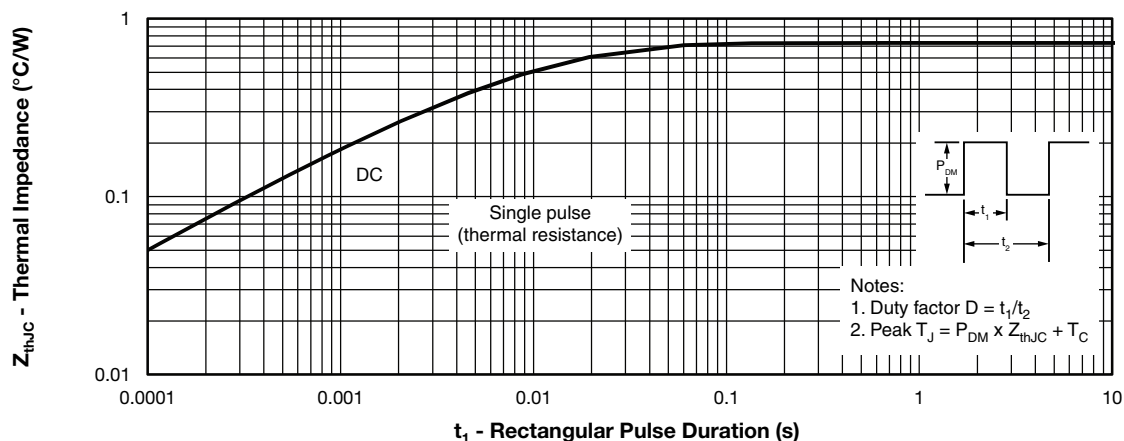


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

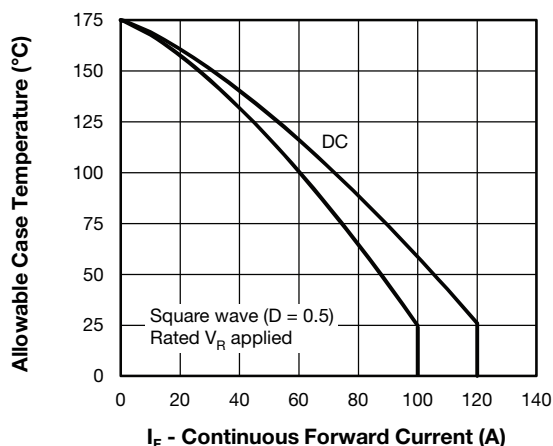


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

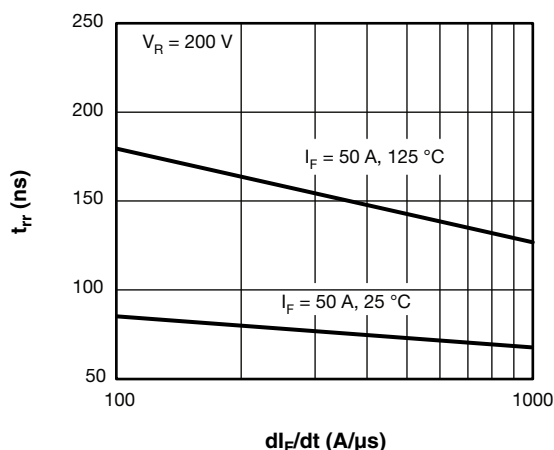


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

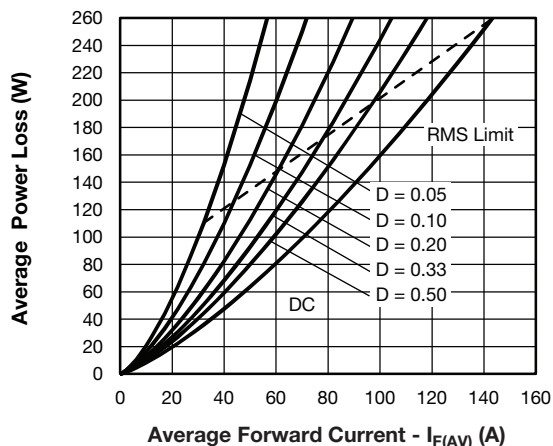


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

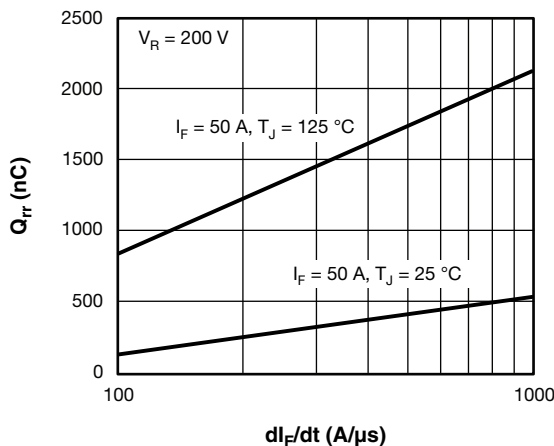


Fig. 8 - Typical Stored Charge vs. dI_F/dt

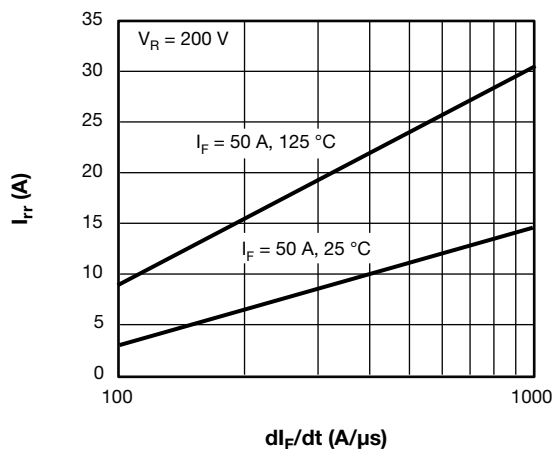


Fig. 9 - Typical Stored Current vs. dI_F/dt

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

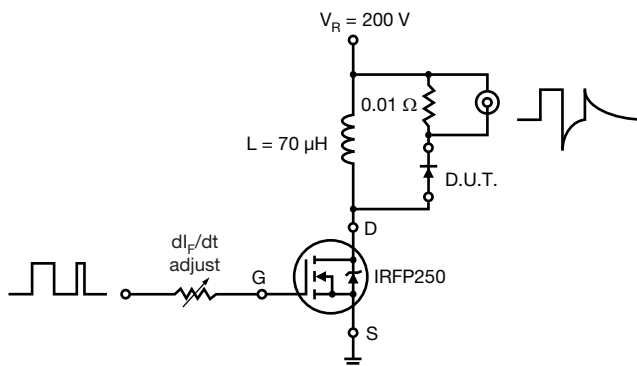


Fig. 10 - Reverse Recovery Parameter Test Circuit

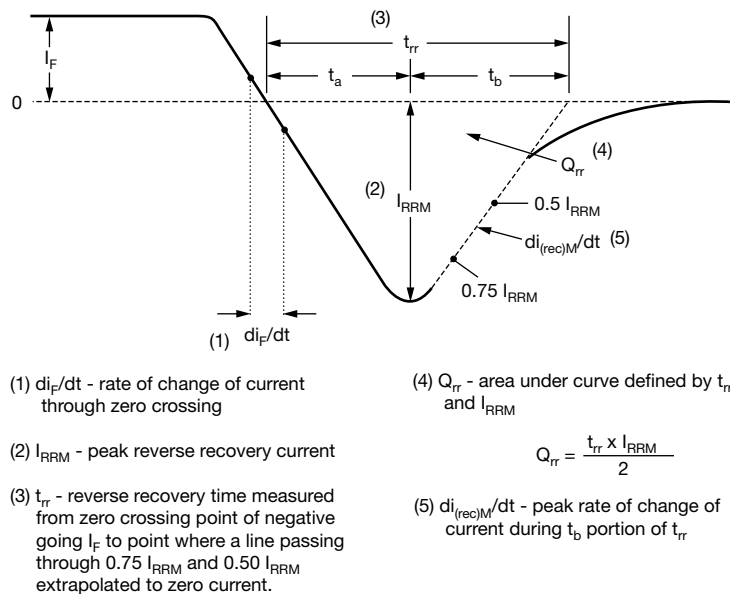


Fig. 11 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code	VS-	UF	B	130	F	A	60
	1	2	3	4	5	6	7

- 1 - Vishay Semiconductors product
- 2 - Ultrafast rectifier
- 3 - Ultrafast Pt diffused
- 4 - Current rating (130 = 130 A)
- 5 - Circuit configuration (two separate diodes, parallel pin-out)
- 6 - Package indicator (SOT-227 standard insulated base)
- 7 - Voltage rating (60 = 600 V)

CIRCUIT CONFIGURATION

CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two separate diodes, parallel pin-out	F	Lead Assignment The circuit drawing for configuration F consists of two parts. On the left is a schematic diagram showing two diodes connected in parallel. The top diode has its cathode (indicated by a vertical line) connected to pin 4 and its anode (indicated by a triangle) connected to pin 3. The bottom diode has its cathode connected to pin 1 and its anode connected to pin 2. On the right is a physical component layout showing a rectangular package with four pins. The pins are labeled 1, 2, 3, and 4. Pin 1 is at the bottom left, pin 2 is at the bottom right, pin 3 is at the top right, and pin 4 is at the top left. The layout shows the internal structure of the package, including the diodes and their connections to the pins.

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?95423
Packaging information	www.vishay.com/doc?95425



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