

# Temperature Sensor IC

## Analog Output

### BD1020HFV

#### General Description

BD1020HFV is a low quiescent current (4μA) and high accuracy temperature sensor.

This IC has a linear response with respect to temperature.

#### Features

- Low Thermal Resistance ( typically 187°C/W )
- ESD Rating 8kV ( HBM )
- Excellent Ripple Rejection Characteristics

#### Applications

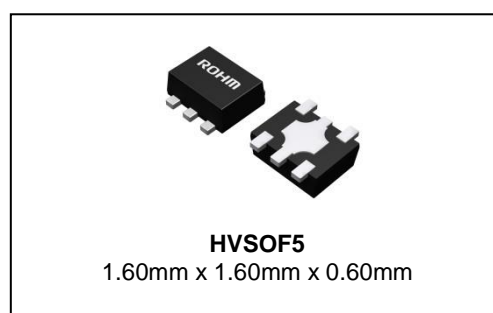
Cell Phone ( RF Module, Battery Thermal Management ), Audio Systems, Digital Still Camera, LCD, PDP, Optical pick up module for DVD and BlueRay

#### Key Specifications

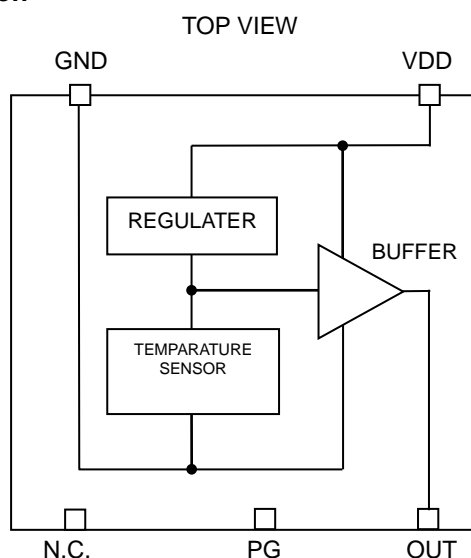
- Operating Voltage Range: 2.4V to 5.5V
- High Accuracy:  $\pm 1.5^{\circ}\text{C}(\text{Max})$  @  $T_a=30^{\circ}\text{C}$   
 $\pm 2.5^{\circ}\text{C}(\text{Max})$  @  $T_a=-30^{\circ}\text{C}, +100^{\circ}\text{C}$
- Temperature Sensitivity:  $-8.2\text{ mV}/^{\circ}\text{C}$  (Typ)
- Quiescent Current: 4.0μA(Typ)
- Detection Temperature Range:  $-30^{\circ}\text{C}$  to  $+100^{\circ}\text{C}$

#### Package

W (Typ) x D (Typ) x H (Max)



#### Block Diagram and Pin Configuration



#### Pin Descriptions

Pin No.	Pin Name	Function	Comment
1	N.C.	-	Please set to OPEN .
2	PG	Heat Condition	Please connect to Temperature Measurement part.
3	OUT	Output Voltage for is inversely proportional with temperature	-
4	VDD	Power Supply	-
5	GND	Ground	-

**Absolute Maximum Ratings (Ta=25°C)**

Parameter	Symbol	Limit	Unit
Power Supply Voltage	V <sub>DD</sub>	-0.3 to +7.0 (Note 1)	V
Output Voltage	V <sub>OUT</sub>	-0.3 to +V <sub>DD</sub> +0.3	V
Output Current	I <sub>OUT</sub>	±1	mA
Power Dissipation	P <sub>d</sub>	0.53 (Note 2)	W
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

(Note 1) However, not exceeding P<sub>d</sub>.

(Note 2) When mounted on ROHM standard board, derate by 5.36mW/°C for Ta higher than 25 °C.

**Caution:** Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

**Recommended Operating Conditions**

Parameter	Symbol	min	Typ	Max	Unit
Power Supply Voltage	V <sub>DD</sub>	2.40	3.00	5.50	V
Operation Temperature	T <sub>opr</sub>	-30	-	+100	°C

**Electrical Characteristics**

(Unless otherwise specified, V<sub>DD</sub>=3.0V, Ta=25°C)

Parameter	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Accuracy	T <sub>acc</sub>	-	-	±1.5	°C	Ta = 30°C
		-	-	±2.5		Ta = 100°C
		-	-	±2.5		Ta = -30°C
Temperature Sensitivity	V <sub>SE</sub>	-8.4	-8.2	-8.0	mV/°C	
Supply Current	I <sub>S</sub>	-	4.0	7.0	μA	
Output Voltage	V <sub>OUT</sub>	1.288	1.300	1.312	V	Ta = 30°C
Output Voltage Line Regulation	ΔV <sub>OUTVDD</sub>	-	-	4	mV	V <sub>DD</sub> = 2.4V to 5.5V
Output Voltage Load Regulation	ΔV <sub>OUTRL</sub>	-	-	1	mV	Difference of I <sub>OUT</sub> : 0μA/0.7μA

## Typical Performance Curves

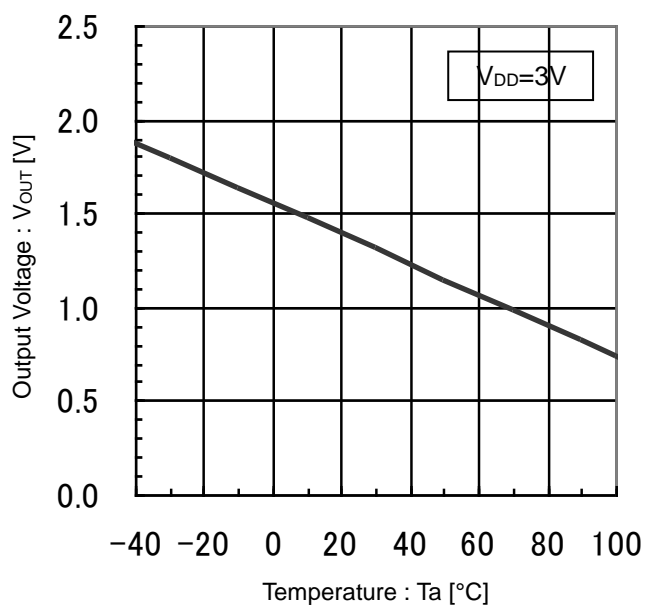


Figure 1. Output Voltage vs Temperature

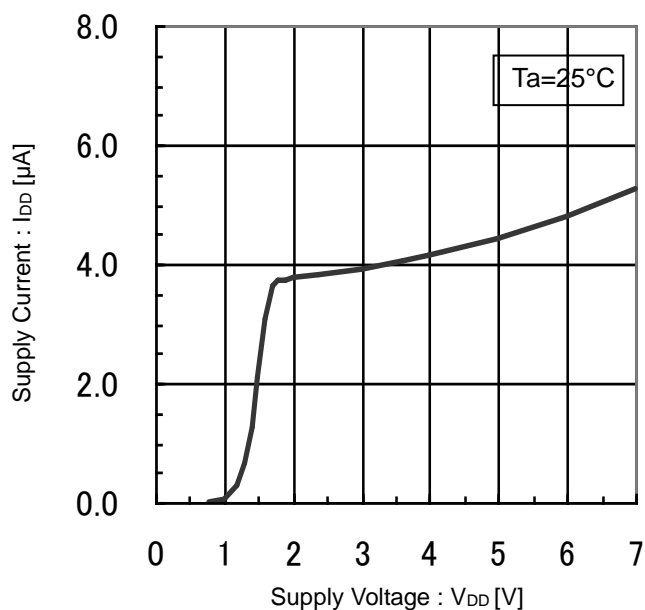


Figure 2. Supply Current vs Supply Voltage

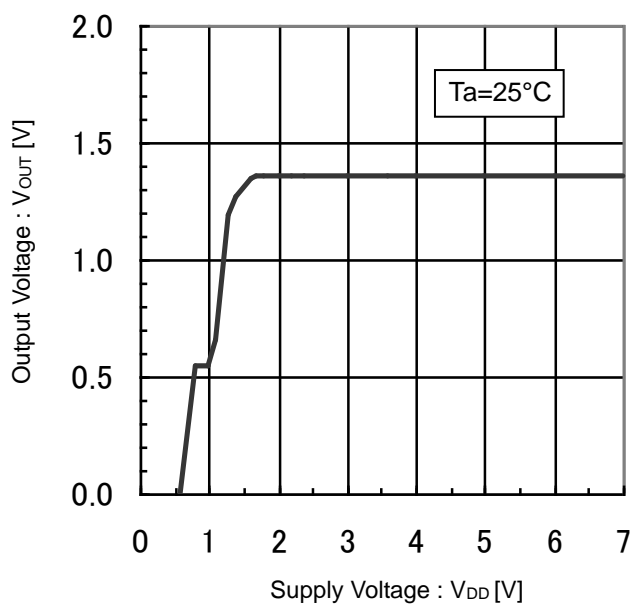


Figure 3. Output Voltage vs Supply Voltage

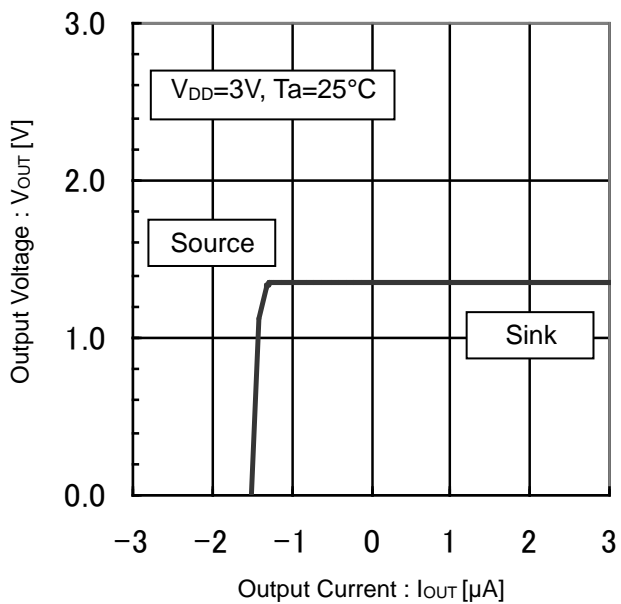


Figure 4. Output Voltage vs Output Current

## Typical Performance Curves – continued

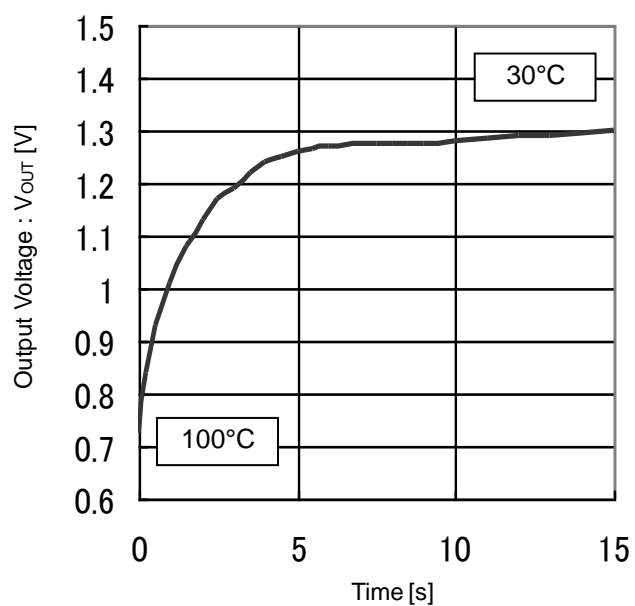


Figure 5. Start-up Response  
( $V_{OUT}$  response 100°C ~ 30°C in Atmosphere)

## Operational Notes

### 1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

### 2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

### 3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

### 4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

### 5. Thermal Consideration

Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

### 6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

### 7. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

### 8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

### 9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

### 10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

## Operational Notes – continued

**11. Unused Input Pins**

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

**12. Regarding the Input Pin of the IC**

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When  $GND > \text{Pin A}$  and  $GND > \text{Pin B}$ , the P-N junction operates as a parasitic diode.

When  $GND > \text{Pin B}$ , the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

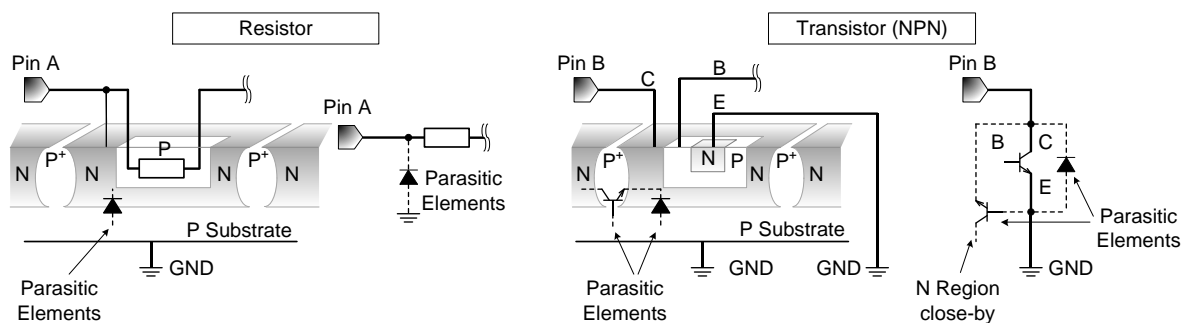


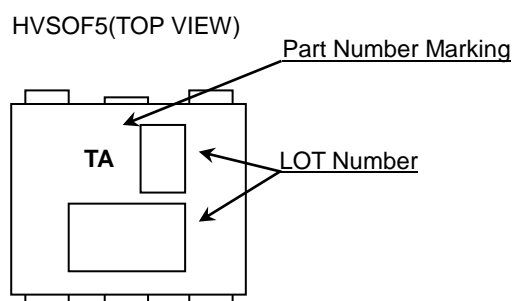
Figure 6. Example of monolithic IC structure

- 13.** Please connect it with the temperature measurement part (GND line usually) to make thermal conductivity with the mount board side the best though the PG pin (Pin No.2) is hindered and doesn't exist about OPEN even if it connects it with GND.

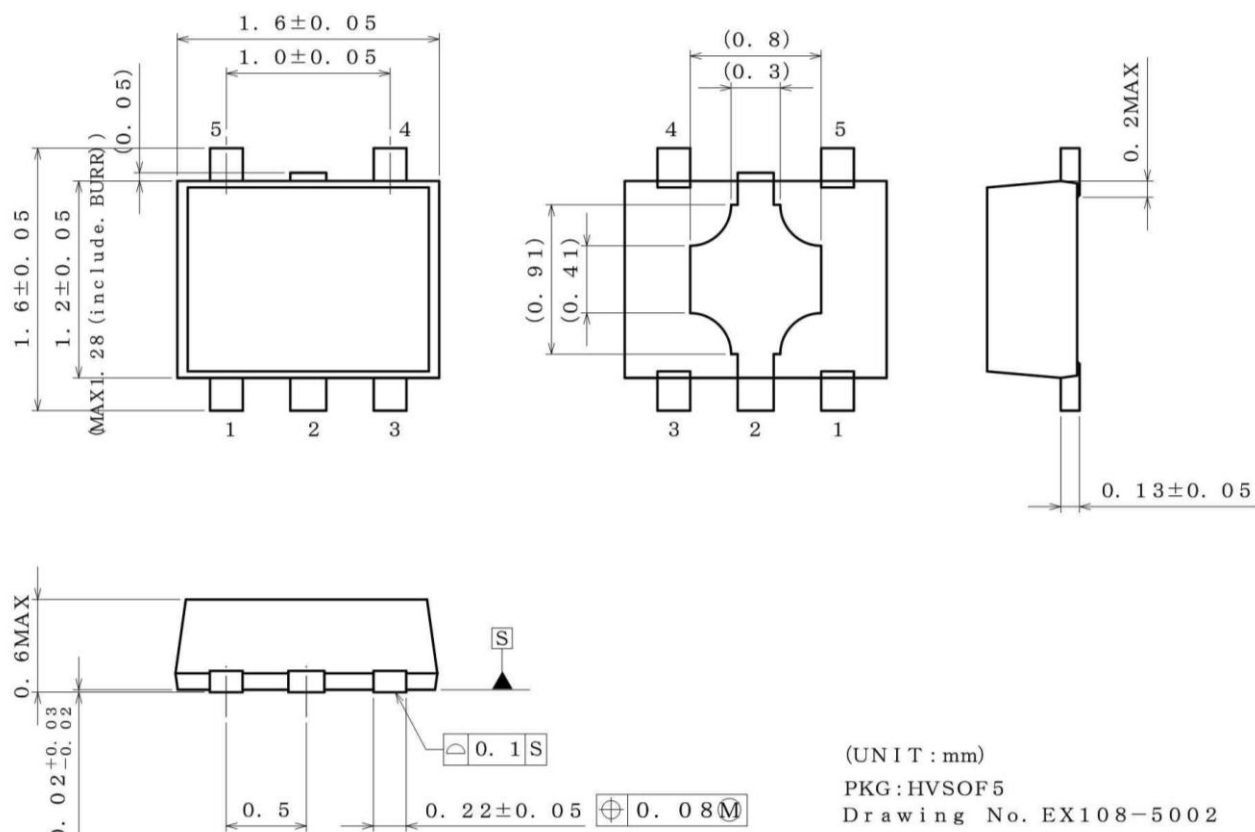
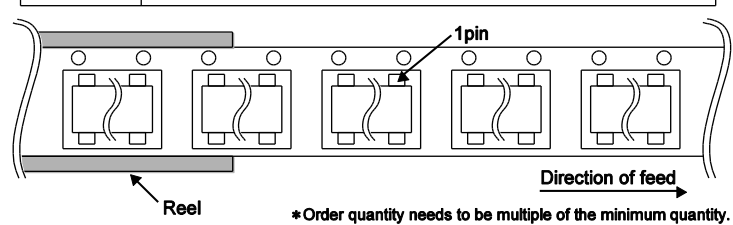
## Ordering Information

B	D	1	0	2	0	H	F	V	-	TR
Part Number									TR	
Package HFV:HVSOF5										
Packaging and forming specification TR: Embossed tape and reel (HVSOF5)										

## Marking Diagram



## Physical Dimension, Tape and Reel Information

Package Name	HVSOF5						
 <p>(UNIT : mm)          PKG : HVSOF5          Drawing No. EX108-5002</p>							
<p><b>&lt;Tape and Reel information&gt;</b></p> <table border="1"> <tr> <td>Tape</td><td>Embossed carrier tape</td></tr> <tr> <td>Quantity</td><td>3000pcs</td></tr> <tr> <td>Direction of feed</td><td>TR (The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand)</td></tr> </table>  <p>* Order quantity needs to be multiple of the minimum quantity.</p>		Tape	Embossed carrier tape	Quantity	3000pcs	Direction of feed	TR (The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand)
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Direction of feed	TR (The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand)						



## Revision History

Date	Revision	Changes
06.Nov.2015	001	New Release

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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  - Installation of protection circuits or other protective devices to improve system safety
  - Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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  - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - Sealing or coating our Products with resin or other coating materials
  - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

## Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

## Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

## Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

## Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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# Mouser Electronics

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- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
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