MOSFETs Silicon N-Channel MOS (DTMOSIV)

# **TK7Q60W**

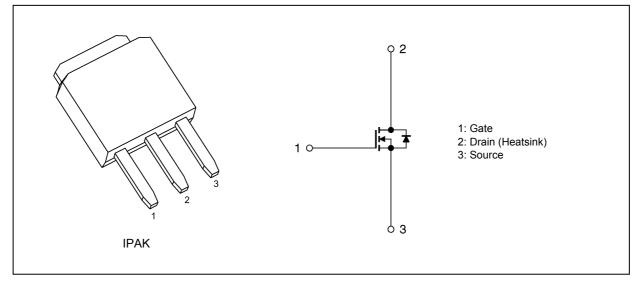
### 1. Applications

Switching Voltage Regulators

#### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 0.5 \Omega$  (typ.) by used to Super Junction Structure : DTMOS
- (2) Easy to control Gate switching
- (3) Enhancement mode:  $V_{th}$  = 2.7 to 3.7 V ( $V_{DS}$  = 10 V,  $I_D$  = 0.35 mA)

### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	600	V
Gate-source voltage		V <sub>GSS</sub>	±30	
Drain current (DC)	(Note 1)	Ι <sub>D</sub>	7.0	A
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	28	
Power dissipation $(T_c = 25^{\circ}C)$		PD	60	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	92	mJ
Avalanche current		I <sub>AR</sub>	1.8	A
Reverse drain current (DC)	(Note 1)	I <sub>DR</sub>	7.0	
Reverse drain current (pulsed)	(Note 1)	I <sub>DRP</sub>	28	
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	2.09	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	125	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V\_DD = 90 V, T\_ch = 25°C (initial), L = 49.9 mH, R\_G = 25  $\Omega$ , I\_AR = 1.8 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

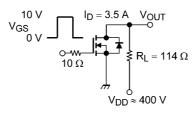
### 6. Electrical Characteristics

### 6.1. Static Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±30 V, $V_{DS}$ = 0 V	_	_	±1	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.35 mA	2.7	_	3.7	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.5 A		0.5	0.6	Ω

### 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Мах	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 300 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	490	_	pF
Reverse transfer capacitance	C <sub>rss</sub>			1.7	_	1
Output capacitance	C <sub>oss</sub>			13	_	1
Effective output capacitance	C <sub>o(er)</sub>	$V_{DS} = 0$ to 400 V, $V_{GS} = 0$ V		21	_	1
Gate resistance	r <sub>g</sub>	V <sub>DS</sub> = OPEN, f = 1 MHz		7.0	_	Ω
Switching time (rise time)	tr	See Figure 6.2.1		18	_	ns
Switching time (turn-on time)	t <sub>on</sub>	1		40	_	1
Switching time (fall time)	t <sub>f</sub>			7.0	_	1
Switching time (turn-off time)	t <sub>off</sub>			55	_	1
MOSFET dv/dt ruggedness	dv/dt	V <sub>DD</sub> = 0 to 400 V, I <sub>D</sub> = 3.5 A	25	_	_	V/ns



Duty  $\leq$  1%, t<sub>w</sub> = 10  $\mu$ s

Fig. 6.2.1 Switching Time Test Circuit

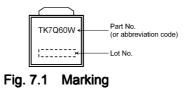
### 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD}\approx 400 \text{ V},  V_{GS} \text{ = } 10 \text{ V},  \text{I}_{D} \text{ = } 7.0 \text{ A}$	_	15	—	nC
Gate-source charge 1	Q <sub>gs1</sub>		_	3.2	_	
Gate-drain charge	Q <sub>gd</sub>		_	8.0	_	

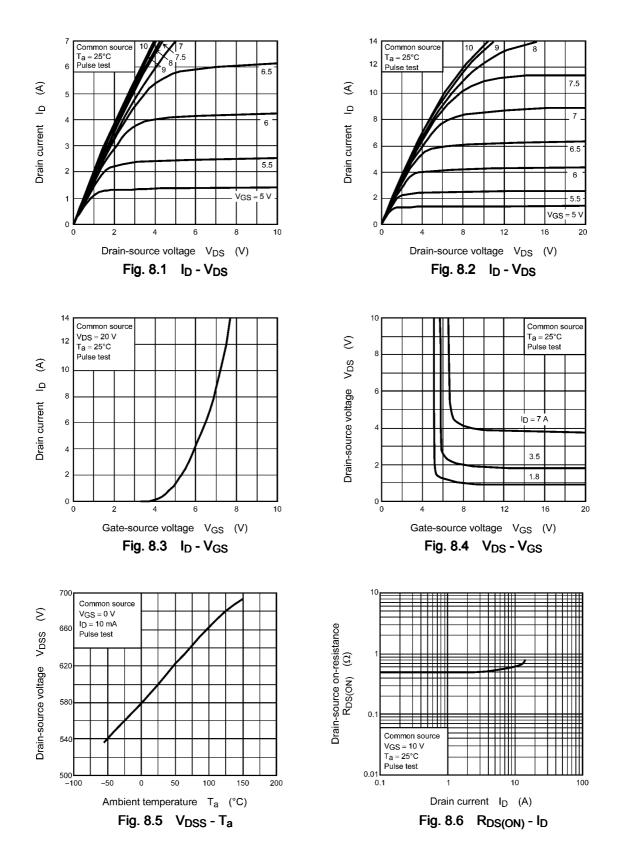
### 6.4. Source-Drain Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	V <sub>DSF</sub>	I <sub>DR</sub> = 7.0 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 3.5 A, V <sub>GS</sub> = 0 V	_	230	_	ns
Reverse recovery charge	Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 100 A/μs	_	1.7	_	μC
Peak reverse recovery current	I <sub>rr</sub>			16		А
Diode dv/dt ruggedness	dv/dt	I <sub>DR</sub> = 3.5 A, V <sub>GS</sub> = 0 V, V <sub>DD</sub> = 400 V	15		_	V/ns

### 7. Marking



### 8. Characteristics Curves (Note)



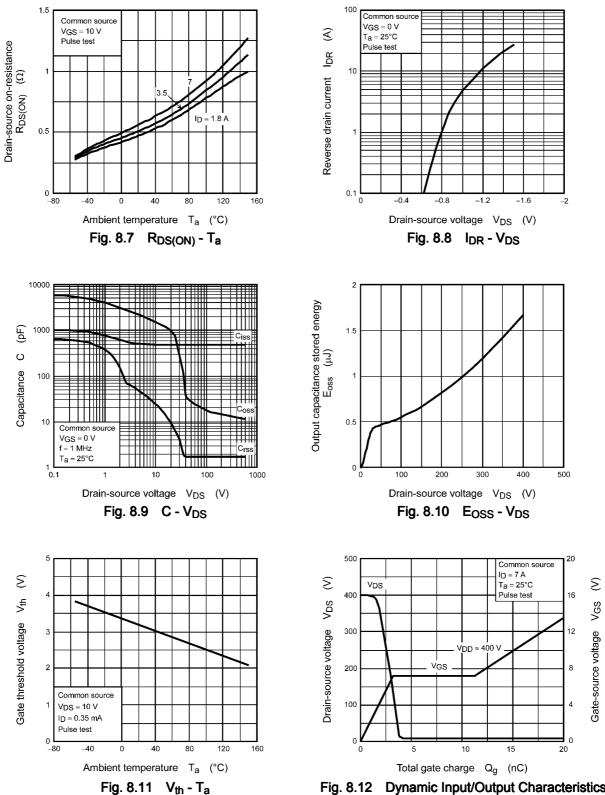


Fig. 8.12 Dynamic Input/Output Characteristics

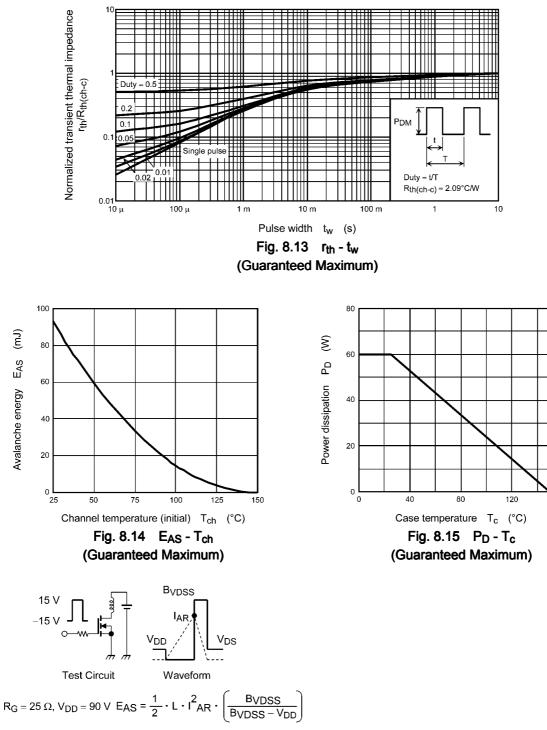
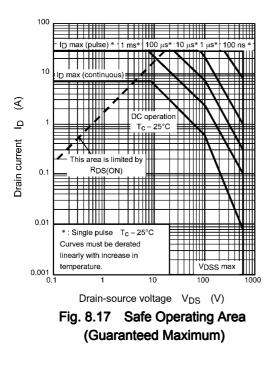


Fig. 8.16 Test Circuit/Waveform

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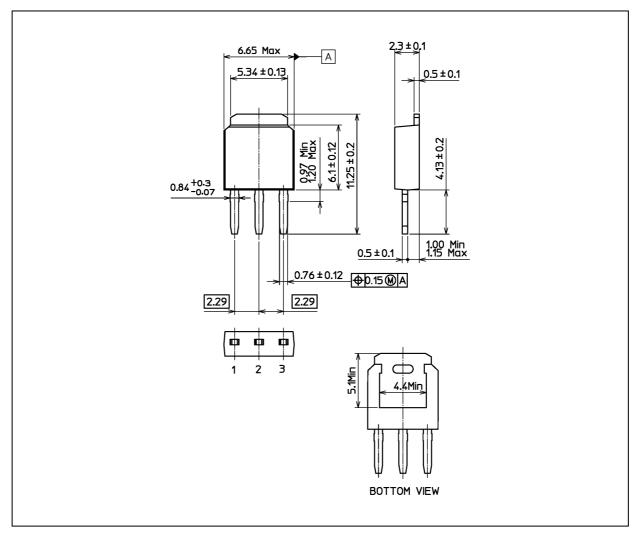
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



### TK7Q60W

#### **Package Dimensions**

Unit: mm



Weight: 0.337 g (typ.)

	Package Name(s)
TOSHIBA: 2-7L1A	
Nickname: IPAK	

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