
Programmable USB Power Delivery Controller

Highlights

- Integrated USB Power Delivery (PD) PHY
- Support for Power Delivery Message Protocol
- Integrated Voltage and Current ADC Inputs
- Configuration Profile Selection
- On-chip Microcontroller
- SPI Interface
- Commercial, Industrial, and Automotive Grade Temperature Support
- Available in 32-SQFN Package

Target Applications

- Notebooks
- Ultrabooks
- Desktop PCs
- Docking Stations
- Monitors
- Printers
- Automotive

Key Benefits

- Integrated USB Power Delivery (PD) PHY
 - Integrated receive termination
 - Requires minimal external components
- Support for Power Delivery Message Protocol
 - Message Generation/Consumption
 - Retry Generation
 - Error Handling
 - State Behavior
- Cable Detect Logic
 - Cable attachment type
- CFG_SEL pins allow selection of multiple profiles
 - Provider
 - Provider/Consumer
 - Consumer
 - Consumer/Provider
- Integrated Voltage (VMON) and Current (IMON) ADC Inputs
- Dead Battery Support
- On-chip Microcontroller
 - Manages I/Os and other signals
 - Implements power delivery policy engine and device policy manager
- Configuration Programming via OTP, or Vendor Defined Messaging
- Supports Low Power Modes
- Serial Peripheral Interface (SPI) Bus
- Internal 3.3 V and 1.8 V Voltage Regulators
- Integrated Oscillator Reduces BOM Costs
- Package
 - 32-pin SQFN (5 x 5 mm)
- Environmental
 - Commercial temperature range (0°C to +70°C)
 - Industrial temperature range (-40°C to +85°C)
 - Automotive temperature range (-40°C to +105°C)

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1.0 INTRODUCTION

1.1 General Description

The UPD1002 is a programmable USB Power Delivery (PD) controller designed to adhere to the *USB Power Delivery Specification*. USB Power Delivery allows a host (or device) to provide or consume up to 5 Amps and/or up to 20 Volts of power from a USB PD capable partner device on the other end of the USB cable. USB PD capable standard and custom cables/connectors are supported, which in most cases are backward compatible with standard USB connections.

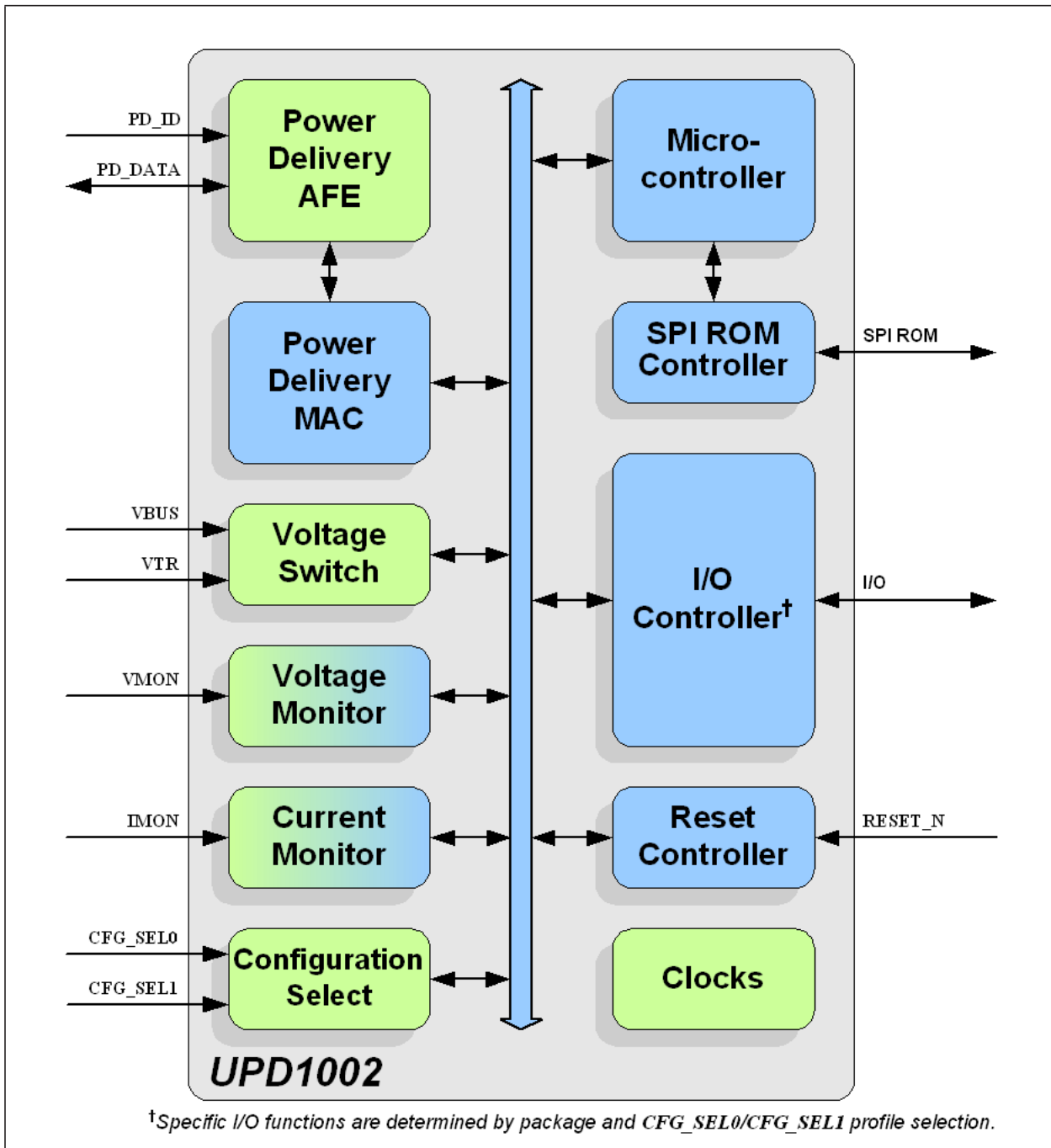
The UPD1002 provides a complete USB Power Delivery solution for notebooks/ultrabooks, desktop PCs, monitors, docking stations, and automotive applications. The functionality of the UPD1002 is selected via two configuration selection pins, `CFG_SEL0` and `CFG_SEL1`, which can be used to select unique PD and system configurations. Designing the UPD1002 into a system can be as simple as selecting a configuration, with no external EEPROM required. Advanced programmability options exist with an external EEPROM installed.

The integrated USB Power Delivery MAC and PHY support provider and consumer operation via the PD communication protocol, as specified in Revision 1.0 (Version 1.2) of the *USB Power Delivery Specification*. Monitoring of VBUS and battery charging is accomplished via the integrated voltage and current ADC inputs. The PHY supports cable ID detection/identification and loopback modes. The PHY includes a 24MHz FSK modulator/demodulator and provides integrated terminations. The USB PD MAC supports both USB PD insertion detection (cold socket) and dead battery cases. The device provides an integrated voltage switch which is used to detect whether the VBUS or VTR (battery) power supply is active, enabling selection of the appropriate power supply at any given time.

The on-chip microcontroller manages the IOs and implements the power delivery local policy engine and device manager. The SPI ROM controller is used by the microcontroller for optional external code execution from ROM. A One Time Programmable (OTP) ROM is integrated in the UPD1002. Integrated 3.3 V and 1.8 V regulators allow device operation from a single power supply. The UPD1002 is available in commercial (0°C to +70°C), industrial (-40°C to +85°C), and automotive (-40°C to +105°C) temperature ranges. An internal block diagram of the UPD1002 is shown in [Figure 1-1](#).

UPD1002

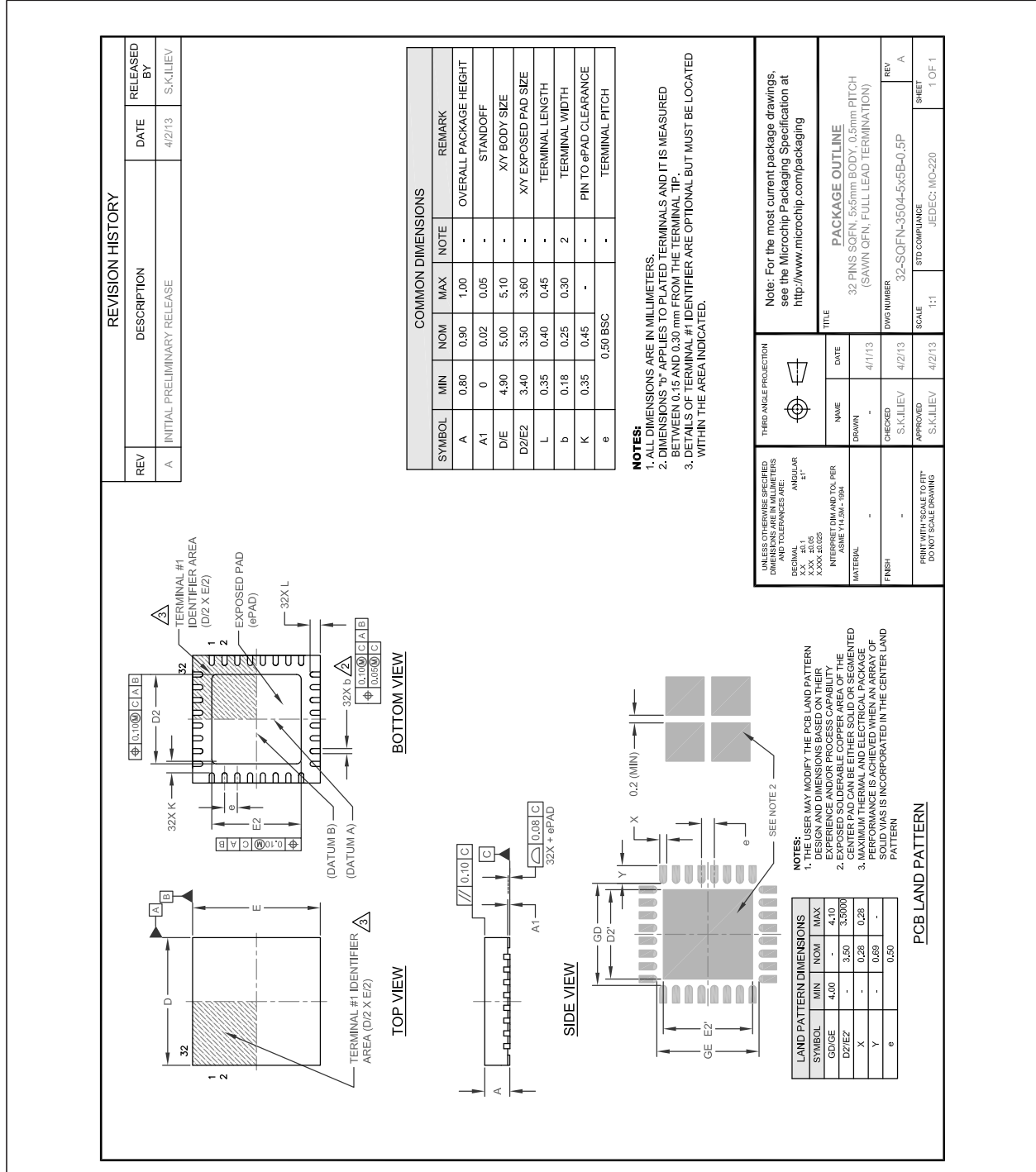
FIGURE 1-1: INTERNAL BLOCK DIAGRAM



2.0 PACKAGE OUTLINES

2.1 32-SQFN

FIGURE 2-1: 32-SQFN PACKAGE



COMMON DIMENSIONS				
SYMBOL	MIN	NOM	MAX	REMARK
A	0.80	0.90	1.00	OVERALL PACKAGE HEIGHT
A1	0	0.02	0.05	STANDOFF
D/E	4.90	5.00	5.10	XY BODY SIZE
D2/E2	3.40	3.50	3.60	XY EXPOSED PAD SIZE
L	0.35	0.40	0.45	TERMINAL LENGTH
b	0.18	0.25	0.30	TERMINAL WIDTH
K	0.35	0.45	-	PIN TO ePAD CLEARANCE
e	0.50	BSC	-	TERMINAL PITCH

NOTES:
 1. ALL DIMENSIONS ARE IN MILLIMETERS.
 2. DIMENSIONS 'b' APPLIES TO FLATTED TERMINALS AND IT IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP.
 3. DETAILS OF TERMINAL #1 IDENTIFIER ARE OPTIONAL BUT MUST BE LOCATED WITHIN THE AREA INDICATED.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS AND TOLERANCES ARE: DECIMALS: ±0.1 FRACTIONS: ±0.05 HOLE DIMENSIONS: ±0.05 INTERSECTIONS: ±0.05 MATERIAL: ASME Y14.5M - 1994	THIRD ANGLE PROJECTION NAME: S.J.KILLEV DRAWN: - DATE: 4/1/13 CHECKED: S.J.KILLEV APPROVED: S.J.KILLEV	Note: For the most current package drawings, see the Microchip Packaging Specification at http://www.microchip.com/packaging TITLE: PACKAGE OUTLINE 32 PINS SQFN, 5x5mm BODY, 0.5mm PITCH (SAVIN DFN, FULL LEAD TERMINATION) DWG NUMBER: 32-SQFN-3504-5x5B-0.5P SCALE: 1:1 STD COMPLIANCE: JEDEC: MO-220 SHEET: 1 OF 1
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PCB LAND PATTERN

NOTES:
 1. USER MAY MODIFY THE PCB LAND PATTERN DESIGN AND DIMENSIONS BASED ON THEIR EXPERIENCE AND/OR PROCESS CAPABILITY.
 2. EXPOSED SOLDERABLE COPPER AREA OF THE CENTER PAD CAN BE EITHER SOLID OR SEGMENTED.
 3. MAXIMUM TERMINAL LENGTH TO PACKAGE PERFORMANCE IS 0.45mm. OTHERWISE, SOLID VIAS IS INCORPORATED IN THE CENTER LAND PATTERN.

LAND PATTERN DIMENSIONS			
SYMBOL	MIN	NOM	MAX
GD/GE	4.00	-	4.10
D2/E2	-	3.50	3.5000
X	-	0.25	0.25
Y	-	0.69	-
e	-	0.50	-

UPD1002

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PART NO.	[X]	-	XX	/	XX
Device	Tape and Reel Option		Temperature Range		Package
Device:	UPD1002				
Tape and Reel Option:	Blank	=	Standard packaging (tray)		
	T	=	Tape and Reel ^(Note 1)		
Temperature Range:	A	=	0°C to +70°C (Commercial)		
	AI	=	-40°C to +85°C (Industrial)		
	AV	=	-40°C to +105°C (Automotive)		
Package:	MQ	=	32-pin SQFN		

Examples:

- a) UPD1002-A/MQ
Tray, Commercial temp., 32-pin SQFN
- b) UPD1002T-AI/MQ
Tape & reel, Industrial temp., 32-pin SQFN

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ISBN: 9781632762252

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