

# S-57M1 Series

# HIGH-SPEED BIPOLAR HALL EFFECT LATCH

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The S-57M1 Series, developed by CMOS technology, is a high-accuracy Hall IC that operates with a high-sensitivity, a highspeed detection and low current consumption.

The output voltage changes when the S-57M1 Series detects the intensity level of magnetic flux density and a polarity change. Using the S-57M1 Series with a magnet makes it possible to detect the rotation status in various devices. High-density mounting is possible by using the small SOT-23-3 package.

Due to its high-accuracy magnetic characteristics, the S-57M1 Series can make operation's dispersion in the system combined with magnet smaller.

Caution This product is intended to use in general electronic devices such as consumer electronics, office equipment, and communications devices. Before using the product in medical equipment or automobile equipment including car audio, keyless entry and engine control unit, contact to ABLIC Inc. is indispensable.

## Features

- Pole detection:
- Detection logic for magnetism<sup>\*1</sup>:
- Output form<sup>\*1</sup>:
- Magnetic sensitivity:
- Operation cycle (current consumption):
- Power supply voltage range:
- Operation temperature range:
- Lead-free (Sn 100%), halogen-free<sup>\*2</sup>

\*1. The option can be selected.

\*2. Refer to "
Product Name Structure" for details.

## Applications

- Motor
- Housing equipment
- Industrial equipment

## Package

• SOT-23-3

Nch open-drain output, CMOS output  $B_{OP}$  = 3.0 mT typ. Ta =  $-40^{\circ}$ C to  $+125^{\circ}$ C

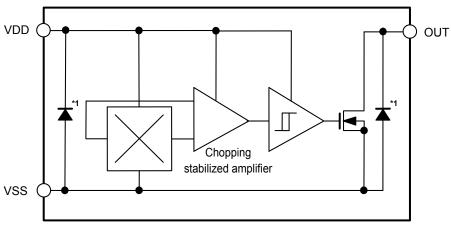
Rev.1.2 02

**Bipolar** latch

V<sub>OUT</sub> = "L" at S pole detection V<sub>OUT</sub> = "H" at S pole detection t<sub>CYCLE</sub> = 50 μs (1400 μA) typ.  $V_{DD}$  = 2.7 V to 5.5 V

# Block Diagrams

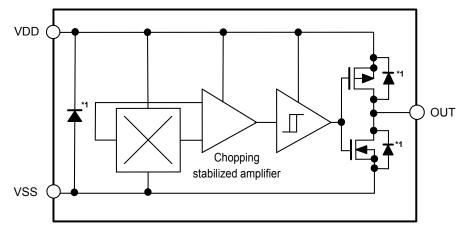
1. Nch open-drain output product



\*1. Parasitic diode



2. CMOS output product

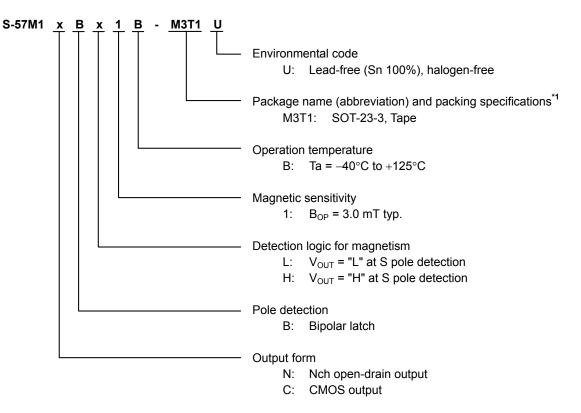


\*1. Parasitic diode

Figure 2

## Product Name Structure

1. Product name



\*1. Refer to the tape drawing.

#### 2. Package

Table 1 Package Drawing Codes

Package Name	Dimension	Таре	Reel
SOT-23-3	MP003-C-P-SD	MP003-C-C-SD	MP003-Z-R-SD

## 3. Product name list

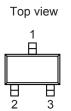
Table 2

Product Name	Output Form	Pole Detection	Detection Logic for Magnetism	Magnetic Sensitivity (B <sub>OP</sub> )
S-57M1NBL1B-M3T1U	Nch open-drain output	Bipolar latch	V <sub>OUT</sub> = "L" at S pole detection	3.0 mT typ.
S-57M1NBH1B-M3T1U	Nch open-drain output	Bipolar latch	V <sub>OUT</sub> = "H" at S pole detection	3.0 mT typ.
S-57M1CBH1B-M3T1U	CMOS output	Bipolar latch	V <sub>OUT</sub> = "H" at S pole detection	3.0 mT typ.

Remark Please contact our sales office for products other than the above.

# Pin Configuration

## 1. SOT-23-3



Pin No.	Symbol	Description
1	VSS	GND pin
2	VDD	Power supply pin
3	OUT	Output pin

Table 3

Figure 3

# Absolute Maximum Ratings

Table	4
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			(Ta = +25°C unless otherwi	ise specified)
Item		Symbol	Absolute Maximum Rating	Unit
Power supply voltage		V <sub>DD</sub>	$V_{\rm SS}-0.3$ to $V_{\rm SS}+7.0$	V
Output current		I <sub>OUT</sub>	±2.0	mA
Nch open-drain output           Output voltage         product		V <sub>OUT</sub>	$V_{SS}-0.3$ to $V_{SS}+7.0$	V
CMOS output product			$V_{SS} - 0.3$ to $V_{DD} + 0.3$	V
Power dissipation		PD	430 <sup>*1</sup>	mW
Operation ambient temperature		T <sub>opr</sub>	-40 to +125	°C
Storage temperature		T <sub>stg</sub>	-40 to +150	°C

**\*1.** When mounted on board

[Mounted board]

(1) Board size:  $114.3 \text{ mm} \times 76.2 \text{ mm} \times t1.6 \text{ mm}$ 

(2) Name: JEDEC STANDARD51-7

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

## Electrical Characteristics

Table 5
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(Ta = +25°C, V <sub>DD</sub> = 5.0 V, V <sub>SS</sub> = 0 V unless otherwise specified)								
Item	Symbol	Condition		Min.	Тур.	Max.	Unit	Test Circuit
Power supply voltage	$V_{DD}$	_		2.7	5.0	5.5	V	_
Current consumption	I <sub>DD</sub>	Average value		_	1400	2000	μA	1
		Nch open-drain output product	Output transistor Nch, I <sub>OUT</sub> = 2 mA	-	_	0.4	V	2
Output voltage	V <sub>OUT</sub>	CMOC subsut product	Output transistor Nch, I <sub>OUT</sub> = 2 mA	_	_	0.4	V	2
		CMOS output product	Output transistor Pch, I <sub>OUT</sub> = -2 mA	V <sub>DD</sub> - 0.4	_	_	V	3
Leakage current	$I_{\text{LEAK}}$	Nch open-drain output product Output transistor Nch, V <sub>OUT</sub> = 5.5 V		-	_	1	μA	4
Operation cycle	t <sub>CYCLE</sub>	_		_	50	100	μS	_

# Magnetic Characteristics

Table 6 (Ta = +25°C, V<sub>DD</sub> = 5.0 V, V<sub>SS</sub> = 0 V unless otherwise specified) Symbol Condition Тур. Max. Unit Test Circuit Item Min. Operation point\*1 S pole B<sub>OP</sub> 1.4 3.0 4.0 mT 5 \_ Release point<sup>\*2</sup> N pole -4.0 -3.0 -1.4 mΤ 5  $B_{RP}$ Hysteresis width\*3 B<sub>HYS</sub> B<sub>HYS</sub>  $= B_{OP} - B_{RP}$ 6.0 mT 5

**\*1.** B<sub>OP</sub>: Operation point

 $B_{OP}$  is the value of magnetic flux density when the output voltage (V<sub>OUT</sub>) changes after the magnetic flux density applied to the S-57M1 Series by the magnet (S pole) is increased (by moving the magnet closer).

 $V_{OUT}$  retains the status until a magnetic flux density of the N pole higher than  $B_{RP}$  is applied.

\*2. B<sub>RP</sub>: Release point

 $B_{RP}$  is the value of magnetic flux density when the output voltage (V<sub>OUT</sub>) changes after the magnetic flux density applied to the S-57M1 Series by the magnet (N pole) is increased (by moving the magnet closer).

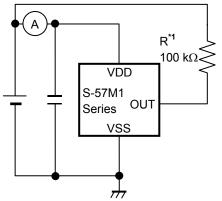
V<sub>OUT</sub> retains the status until a magnetic flux density of the S pole higher than B<sub>OP</sub> is applied.

\*3. B<sub>HYS</sub>: Hysteresis width

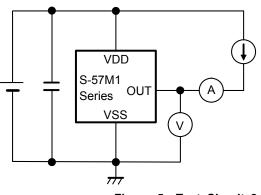
 $B_{\text{HYS}}$  is the difference of magnetic flux density between  $B_{\text{OP}}$  and  $B_{\text{RP}}.$ 

**Remark** The unit of magnetic density mT can be converted by using the formula 1 mT = 10 Gauss.

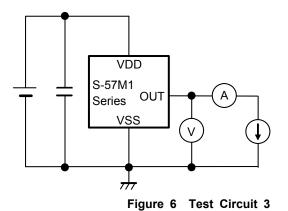
## Test Circuits



\*1. Resistor (R) is unnecessary for the CMOS output product. Figure 4 Test Circuit 1







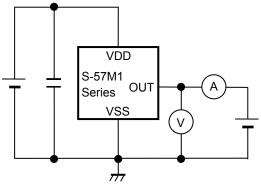
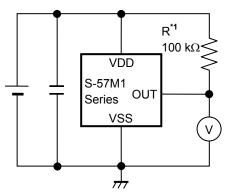
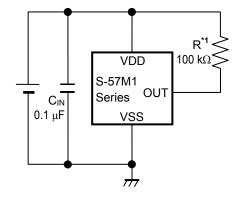


Figure 7 Test Circuit 4



\*1. Resistor (R) is unnecessary for the CMOS output product. Figure 8 Test Circuit 5

## Standard Circuit



\*1. Resistor (R) is unnecessary for the CMOS output product.

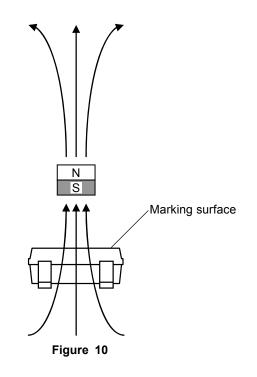
Figure 9

Caution The above connection diagram and constant will not guarantee successful operation. Perform t horough evaluation using the actual application to set the constant.

## Operation

#### 1. Direction of applied magnetic flux

The S-57M1 Series detects the magnetic flux density which is vertical to the marking surface. **Figure 10** shows the direction in which magnetic flux is being applied.

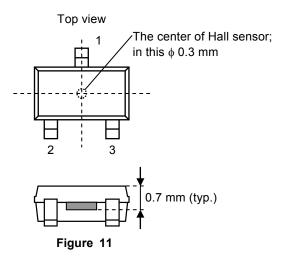


#### 2. Position of Hall sensor

Figure 11 shows the position of Hall sensor.

The center of this Hall sensor is located in the area indicated by a circle, which is in the center of a package as described below.

The following also shows the distance (typ. value) between the marking surface and the chip surface of a package.



#### 3. Basic operation

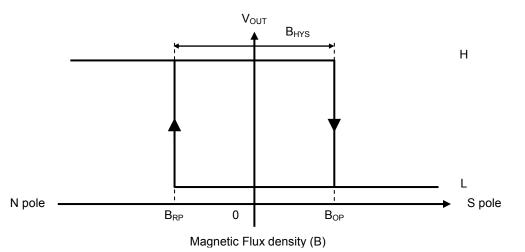
The S-57M1 Series changes the output voltage ( $V_{OUT}$ ) according to the level of the magnetic flux density and a polarity change (N pole or S pole) applied by a magnet.

Definition of the magnetic field is performed every operation cycle indicated in "
Electrical Characteristics".

#### 3.1 Product with V<sub>OUT</sub> = "L" at S pole detection

When the magnetic flux density of the S pole perpendicular to the marking surface exceeds the operation point ( $B_{OP}$ ) after the S pole of a magnet is moved closer to the marking surface of the S-57M1 Series, V<sub>OUT</sub> changes from "H" to "L". When the N pole of a magnet is moved closer to the marking surface of the S-57M1 Series and the magnetic flux density of the N pole is higher than the release point ( $B_{RP}$ ), V<sub>OUT</sub> changes from "L" to "H". In case of  $B_{RP} < B < B_{OP}$ , V<sub>OUT</sub> retains the status.

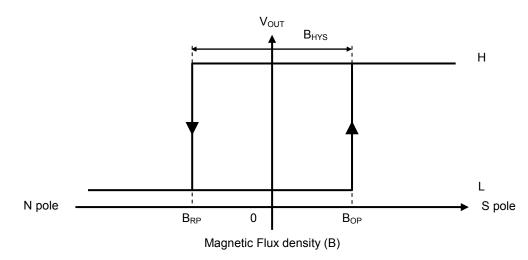
Figure 12 shows the relationship between the magnetic flux density and  $V_{OUT}$ .





#### 3. 2 Product with V<sub>OUT</sub> = "H" at S pole detection

When the magnetic flux density of the S pole perpendicular to the marking surface exceeds  $B_{OP}$  after the S pole of a magnet is moved closer to the marking surface of the S-57M1 Series,  $V_{OUT}$  changes from "L" to "H". When the N pole of a magnet is moved closer to the marking surface of the S-57M1 Series and the magnetic flux density of the N pole is higher than  $B_{RP}$ ,  $V_{OUT}$  changes from "H" to "L". In case of  $B_{RP} < B < B_{OP}$ ,  $V_{OUT}$  retains the status. **Figure 13** shows the relationship between the magnetic flux density and  $V_{OUT}$ .





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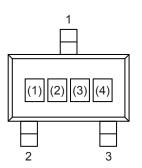
## Precautions

- If the impedance of the power supply is high, the IC may malfunction due to a supply voltage drop caused by feedthrough current. Take care with the pattern wiring to ensure that the impedance of the power supply is low.
- Note that the IC may malfunction if the power supply voltage rapidly changes.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- Large stress on this IC may affect on the magnetic characteristics. Avoid large stress which is caused by bend and distortion during mounting the IC on a board or handle after mounting.
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# Marking Specification

# 1. SOT-23-3

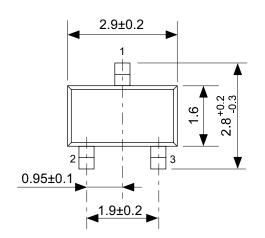
Top view

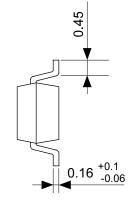


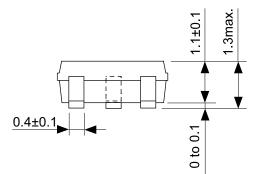
(1) to (3): Product code (Refer to **Product name vs. Product code**.)(4): Lot number

#### Product name vs. Product code

Product Name	Product Code			
FIGULE Name	(1)	(2)	(3)	
S-57M1NBL1B-M3T1U	W	7	Α	
S-57M1NBH1B-M3T1U	W	7	В	
S-57M1CBH1B-M3T1U	W	7	С	

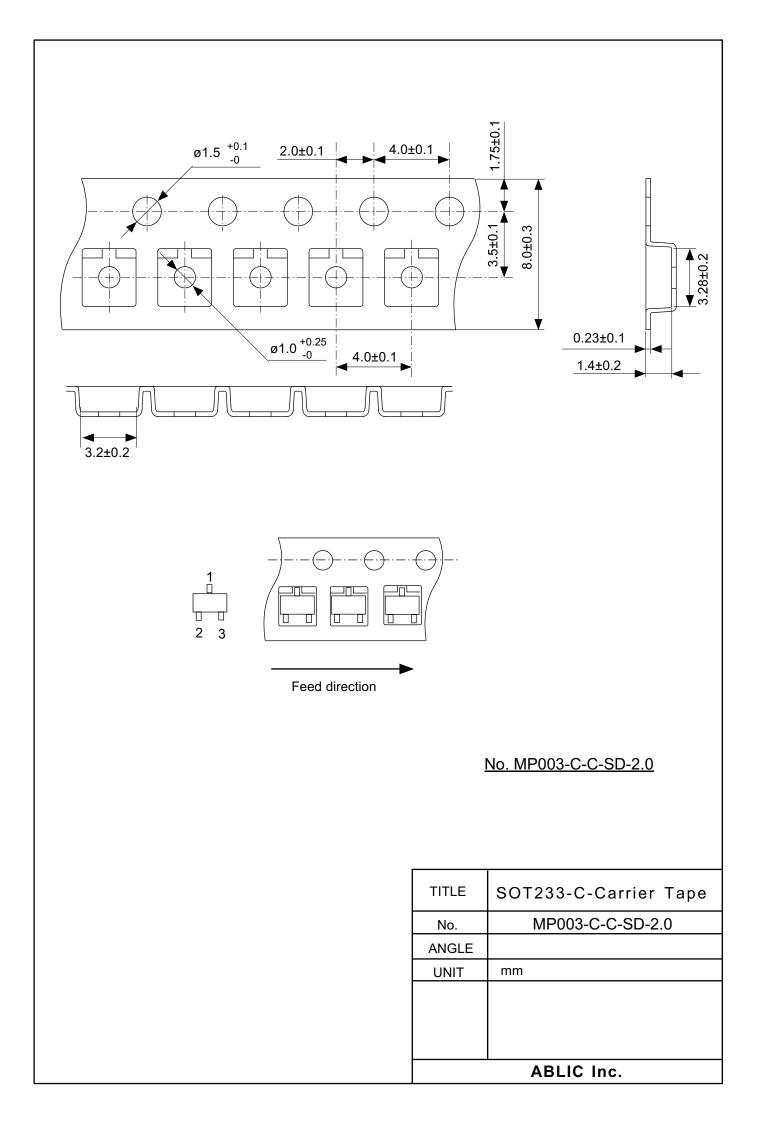


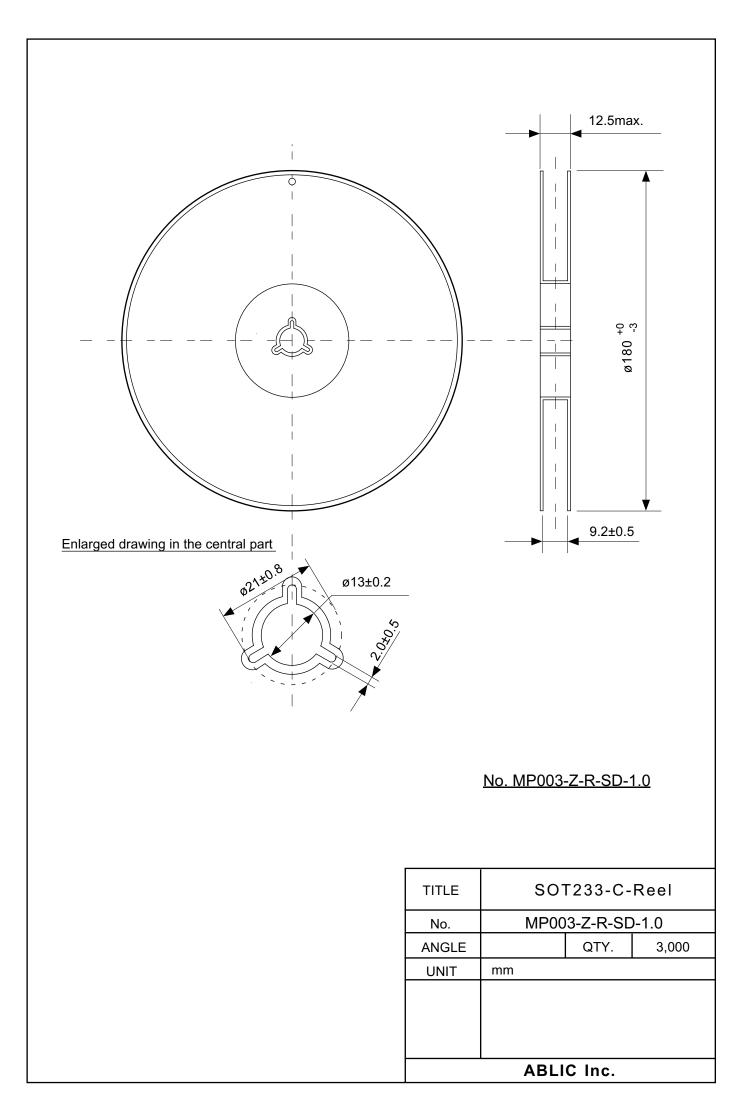




No. MP003-C-P-SD-1.1

TITLE	SOT233-C-PKG Dimensions
No.	MP003-C-P-SD-1.1
ANGLE	$\bigcirc$
UNIT	mm
	ABLIC Inc.





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2.4-2019.07

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