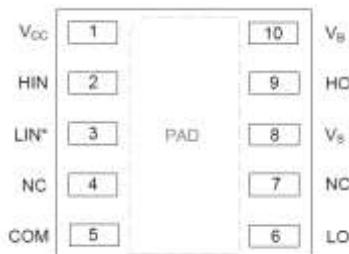


Pin Diagrams

(Top View)

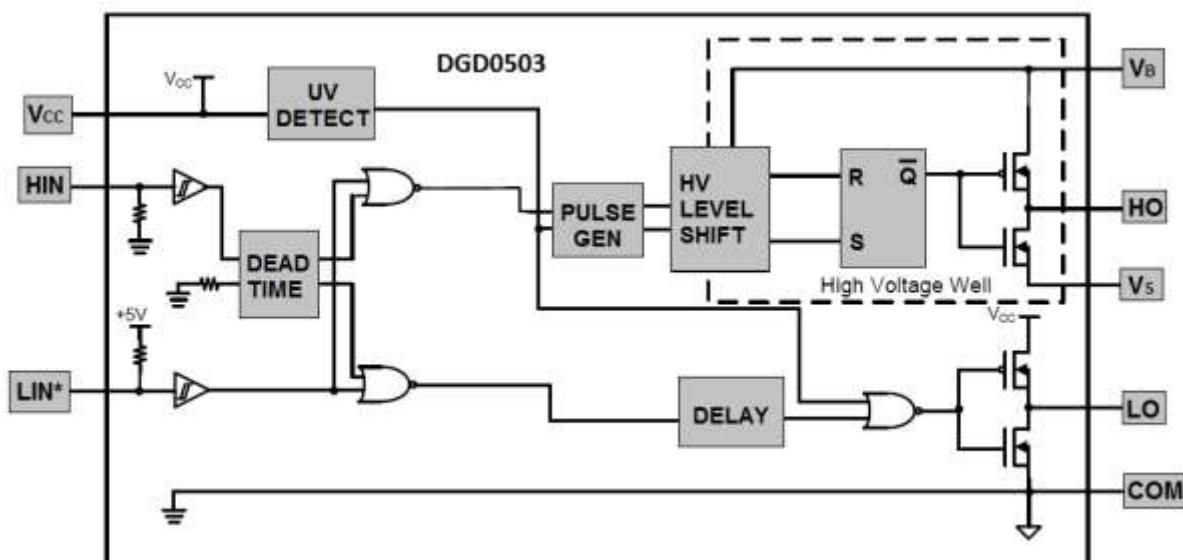


W-DFN3030-10 (Type TH)

Pin Descriptions

Pin Number	Pin Name	Function
1	V _{CC}	Logic and Low Side Supply
2	HIN	Logic Input for High-Side Gate Driver Output in Phase with HO
3	LIN*	Logic Input for Low-Side Gate Driver Output out of Phase with LO
4, 7	NC	No Connection (No Internal Connection)
5	COM	Low-Side and Logic Return
6	LO	Low-Side Gate Drive Output
8	V _S	High-Side Floating Supply Return
9	HO	High-Side Gate Drive Output
10	V _B	High-Side Floating Supply
PAD	Substrate	Connect to COM on PCB

Functional Block Diagram



Timing Waveforms

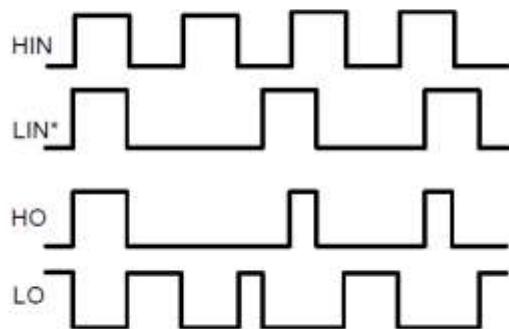


Figure 1. Input / Output Timing Diagram

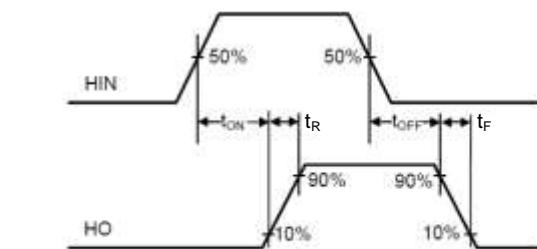
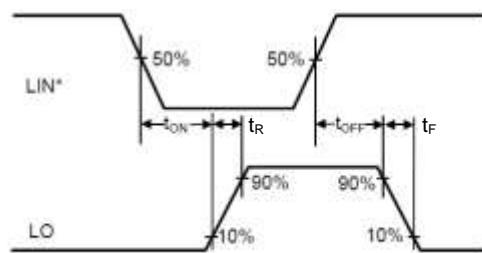


Figure 2. Switching Time Waveform Definitions

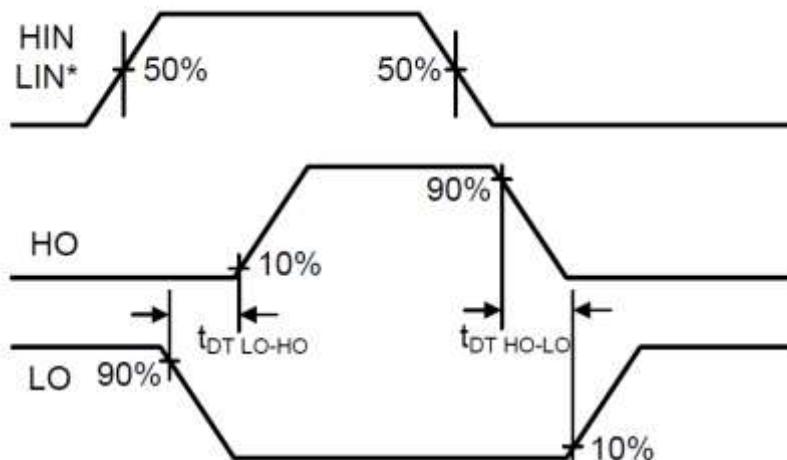
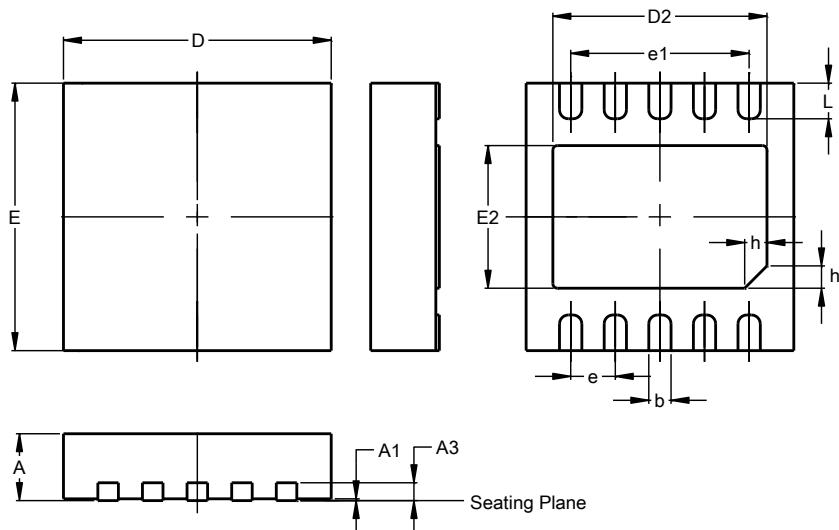


Figure 3. Deadtime Waveform Definitions

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

W-DFN3030-10 (Type TH)



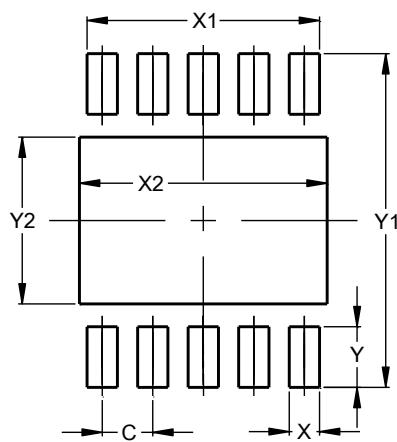
W-DFN3030-10 (Type TH)			
Dim	Min	Max	Typ
A	0.70	0.80	0.75
A1	--	0.05	0.02
A3	0.18	0.25	0.20
b	0.18	0.30	0.25
D	2.90	3.10	3.00
D2	2.40	2.60	2.50
e	0.50BSC		
e1	2.00BSC		
E	2.90	3.10	3.00
E2	1.45	1.65	1.55
h	0.20	0.30	0.25
L	0.30	0.50	0.40

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

W-DFN3030-10 (Type TH)



Dimensions	Value (in mm)
C	0.500
X	0.300
X1	2.300
X2	2.600
Y	0.600
Y1	3.300
Y2	1.650

Note : For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.

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ООО "ЛайфЭлектроникс"

"LifeElectronics" LLC

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