74HC1G125-Q100; 74HCT1G125-Q100

Bus buffer/line driver; 3-state

Rev. 1 — 18 June 2013

Product data sheet

1. General description

The 74HC1G125-Q100; 74HCT1G125-Q100 is a single buffer/line driver with 3-state output. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of $V_{\rm CC}$.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - ◆ Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Input levels:
 - ◆ For 74HC1G125-Q100: CMOS level
 - ♦ For 74HCT1G125-Q100: TTL level
- Symmetrical output impedance
- High noise immunity
- Low power consumption
- Balanced propagation delays
- ESD protection:
 - MIL-STD-883, method 3015 exceeds 2000 V
 - ♦ HBM JESD22-A114F exceeds 2000 V
 - ♦ MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)

3. Ordering information

Table 1. Ordering information

Type number	Package						
	Temperature range	Name	Description	Version			
74HC1G125GW-Q100	–40 °C to +125 °C	TSSOP5	,,	SOT353-1			
74HCT1G125GW-Q100			body width 1.25 mm				
74HC1G125GV-Q100	–40 °C to +125 °C	SC-74A	plastic surface mounted package; 5 leads	SOT753			
74HCT1G125GV-Q100	_						



4. Marking

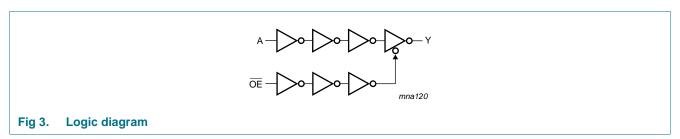
Table 2. Marking

Type number	Marking code ^[1]
74HC1G125GW-Q100	HM
74HCT1G125GW-Q100	TM
74HC1G125GV-Q100	H25
74HCT1G125GV-Q100	T25

^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

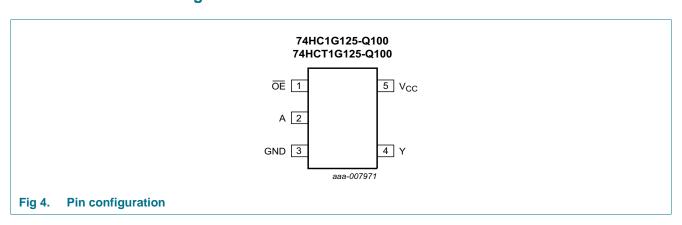
5. Functional diagram





6. Pinning information

6.1 Pinning



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6.2 Pin description

Table 3. Pin description

Symbol	Pin	Description
ŌĒ	1	output enable input (active LOW)
A	2	data input
GND	3	ground (0 V)
Υ	4	data output
V _{CC}	5	supply voltage

7. Functional description

7.1 Function table

Table 4. Function table[1]

Control OE	Input	Output
OE	A	Υ
L	L	L
L	Н	Н
Н	X	Z

^[1] H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7.0	V
I _{IK}	input clamping current	$V_I < -0.5 \text{ V or } V_I > V_{CC} + 0.5 \text{ V}$	<u>[1]</u> -	±20	mA
I _{OK}	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$	<u>[1]</u> -	±20	mA
Io	output current	$V_O = -0.5 \text{ V to } (V_{CC} + 0.5 \text{ V})$	<u>[1]</u> -	±35	mA
I _{CC}	supply current		-	70	mA
I_{GND}	ground current		-70	-	mA
T _{stg}	storage temperature		-65	+150	°C
P_{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$	[2] _	200	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

^[2] Above 55 °C the value of P_{tot} derates linearly with 2.5 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	mbol Parameter Conditions		74HC1G125-Q100		74HCT1G125-Q100			Unit	
			Min	Тур	Max	Min	Тур	Max	
V_{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V_{CC}	0	-	V_{CC}	V
Vo	output voltage		0	-	V_{CC}	0	-	V_{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
$\Delta t / \Delta V$	input transition rise	$V_{CC} = 2.0 \text{ V}$	-	-	625	-	-	-	ns/V
	and fall rate	$V_{CC} = 4.5 \text{ V}$	-	-	139	-	-	139	ns/V
		$V_{CC} = 6.0 \text{ V}$	-	-	83	-	-	-	ns/V

10. Static characteristics

Table 7. Static characteristics 74HC1G125-Q100

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +85 °C[1]					
V _{IH}	HIGH-level input voltage	V _{CC} = 2.0 V	1.5	1.2	-	V
		V _{CC} = 4.5 V	3.15	2.4	-	V
		V _{CC} = 6.0 V	4.2	3.2	-	V
V_{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	0.8	0.5	V
		V _{CC} = 4.5 V	-	2.1	1.35	V
		V _{CC} = 6.0 V	-	2.8	1.8	V
V_{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_{O} = -20 \mu A; V_{CC} = 2.0 V$	1.9	2.0	-	V
		$I_{O} = -20 \mu A; V_{CC} = 4.5 V$	4.4	4.5	-	V
		$I_{O} = -20 \mu A; V_{CC} = 6.0 V$	5.9	6.0	-	V
		$I_{O} = -6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.84	4.32	-	V
		$I_{O} = -7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	5.34	5.81	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A; V_{CC} = 2.0 V$	-	0	0.1	V
		$I_O = 20 \mu A; V_{CC} = 4.5 V$	-	0	0.1	V
		$I_O = 20 \mu A; V_{CC} = 6.0 \text{ V}$	-	0	0.1	V
		$I_O = 6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	0.15	0.33	V
		$I_O = 7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	0.16	0.33	V
II	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	1.0	μΑ
I _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	5	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0 \text{ V}$	-	-	10	μΑ
C _I	input capacitance		-	1.5	-	pF

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Table 7. Static characteristics 74HC1G125-Q100 ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +125 °C					
V_{IH}	HIGH-level input voltage	V _{CC} = 2.0 V	1.5	-	-	V
		V _{CC} = 4.5 V	3.15	-	-	V
		V _{CC} = 6.0 V	4.2	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	-	0.5	V
		V _{CC} = 4.5 V	-	-	1.35	V
		V _{CC} = 6.0 V	-	-	1.8	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_{O} = -20 \mu A; V_{CC} = 2.0 V$	1.9	-	-	V
		$I_{O} = -20 \mu A; V_{CC} = 4.5 V$	4.4	-	-	V
		$I_{O} = -20 \mu A; V_{CC} = 6.0 V$	5.9	-	-	V
		$I_{O} = -6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.7	-	-	V
		$I_{O} = -7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	5.2	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A; V_{CC} = 2.0 V$	-	-	0.1	V
		$I_O = 20 \mu A; V_{CC} = 4.5 V$	-	-	0.1	V
		$I_O = 20 \mu A; V_{CC} = 6.0 \text{ V}$	-	-	0.1	V
		$I_{O} = 6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.4	V
		$I_{O} = 7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	-	0.4	V
I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	1.0	μΑ
OZ	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	10	μΑ
СС	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0 \text{ V}$	-	-	20	μΑ

^[1] All typical values are measured at T_{amb} = 25 °C.

Table 8. Static characteristics 74HCT1G125-Q100

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +85 °C[1]					
V_{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	1.6	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	1.2	0.8	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
		$I_{O} = -20 \mu A$	4.4	4.5	-	V
		$I_{O} = -6.0 \text{ mA}$	3.84	4.32	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
		I _O = 20 μA	-	0	0.1	V
		$I_{O} = 6.0 \text{ mA}$	-	0.16	0.33	V
l _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	5	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	10	μΑ
Δl _{CC}	additional supply current	$V_I = V_{CC} - 2.1 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 4.5 \text{ V} \text{ to } 5.5 \text{ V}$	-	-	500	μА
Cı	input capacitance		-	1.5	-	pF
T _{amb} = -	40 °C to +125 °C					
V_{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	-	0.8	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
		$I_{O} = -20 \mu A$	4.4	-	-	V
		$I_{O} = -6.0 \text{ mA}$	3.7	-	-	V
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
		I _O = 20 μA	-	-	0.1	V
		$I_{O} = 6.0 \text{ mA}$	-	-	0.4	V
l _l	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	10	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	20	μА
Δl _{CC}	additional supply current	$V_1 = V_{CC} - 2.1 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 4.5 \text{ V} \text{ to } 5.5 \text{ V}$	-	-	850	μΑ

^[1] All typical values are measured at $T_{amb} = 25$ °C.

11. Dynamic characteristics

Table 9. Dynamic characteristics 74HC1G125-Q100

Voltages are referenced to GND (ground = 0 V); CL = 50 pF unless otherwise specified; for test circuit see Figure 7

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	0 °C to +85 °C[1]					
t _{pd}	propagation delay	A to Y; see Figure 5	[2]			
		V _{CC} = 2.0 V	-	24	125	ns
		V _{CC} = 4.5 V	-	10	25	ns
		$V_{CC} = 5 \text{ V}; C_L = 15 \text{ pF}$	-	9	-	ns
		V _{CC} = 6.0 V	-	8	21	ns
t _{en}	enable time	OE to Y; see Figure 6	[2]			
		V _{CC} = 2.0 V	-	19	155	ns
		V _{CC} = 4.5 V	-	9	31	ns
		$V_{CC} = 6.0 \text{ V}$	-	7	26	ns
t _{dis}	disable time	OE to Y; see Figure 6	[2]			
		V _{CC} = 2.0 V	-	18	155	ns
		V _{CC} = 4.5 V	-	12	31	ns
		V _{CC} = 6.0 V	-	11	26	ns
C _{PD}	power dissipation capacitance	$V_I = GND$ to V_{CC}	<u>[3]</u> _	30	-	pF
T _{amb} = -4	0 °C to +125 °C					
t _{pd}	propagation delay	A to Y; see Figure 5	[2]			
		V _{CC} = 2.0 V	-	-	150	ns
		V _{CC} = 4.5 V	-	-	30	ns
		V _{CC} = 6.0 V	-	-	26	ns
t _{en}	enable time	OE to Y; see Figure 6	[2]			
		V _{CC} = 2.0 V	-	-	190	ns
		$V_{CC} = 4.5 \text{ V}$	-	-	38	ns
		$V_{CC} = 6.0 \text{ V}$	-	-	32	ns
t _{dis}	disable time	OE to Y; see Figure 6	[2]			
		$V_{CC} = 2.0 \text{ V}$	-	-	190	ns
		$V_{CC} = 4.5 \text{ V}$	-	-	38	ns
		V _{CC} = 6.0 V	-	-	32	ns

^[1] All typical values are measured at T_{amb} = 25 °C.

 t_{en} is the same as t_{PZL} and $t_{\text{PZH}}.$

 t_{dis} is the same as t_{PLZ} and $t_{\text{PHZ}}.$

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 f_0 = output frequency in MHz;

C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

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^[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

Table 10. Dynamic characteristics 74HCT1G125-Q100

Voltages are referenced to GND (ground = 0 V); CL = 50 pF unless otherwise specified; for test circuit see Figure 7

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
$T_{amb} = -40$	0 °C to +85 °C					
t _{pd}	propagation delay	A to Y; see Figure 5	[2]			
		V _{CC} = 4.5 V	-	11	30	ns
		V _{CC} = 5 V; C _L = 15 pF	-	10	-	ns
t _{en}	enable time	$V_{CC} = 4.5 \text{ V}; \overline{OE} \text{ to Y};$ see Figure 6	[2] -	10	35	ns
t _{dis}	disable time	$V_{CC} = 4.5 \text{ V}; \overline{OE} \text{ to Y};$ see Figure 6	[2] -	11	31	ns
C _{PD}	power dissipation capacitance	V_I = GND to V_{CC} – 1.5 V	[3] _	27	-	pF
T _{amb} = -40	0 °C to +125 °C					
t _{pd}	propagation delay	$V_{CC} = 4.5 \text{ V}$; A to Y; see Figure 5	[2] _	-	36	ns
t _{en}	enable time	$V_{CC} = 4.5 \text{ V}; \overline{OE} \text{ to Y};$ see Figure 6	[2] -	-	42	ns
t _{dis}	disable time	$V_{CC} = 4.5 \text{ V}; \overline{OE} \text{ to Y};$ see Figure 6	[2] _	-	38	ns

[1] All typical values are measured at T_{amb} = 25 °C.

 $\begin{array}{ll} \hbox{ t_{pd} is the same as t_{PLH} and t_{PHL}.} \\ \hbox{ t_{en} is the same as t_{PZL} and t_{PZH}.} \end{array}$

 t_{dis} is the same as t_{PLZ} and t_{PHZ} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

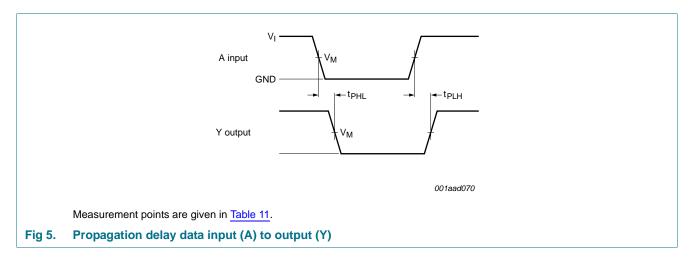
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

12. Waveforms



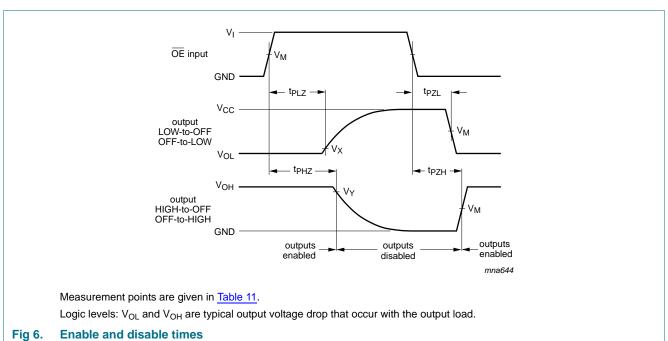


Table 11. Measurement points

Туре	Input	Output				
	V _M	V _M	V _X	V _Y		
74HC1G125-Q100	0.5V _{CC}	0.5V _{CC}	$V_{OL} + 0.3 V$	V _{OH} – 0.3 V		
74HCT1G125-Q100	1.3 V	1.3 V	$V_{OL} + 0.3 V$	V _{OH} – 0.3 V		

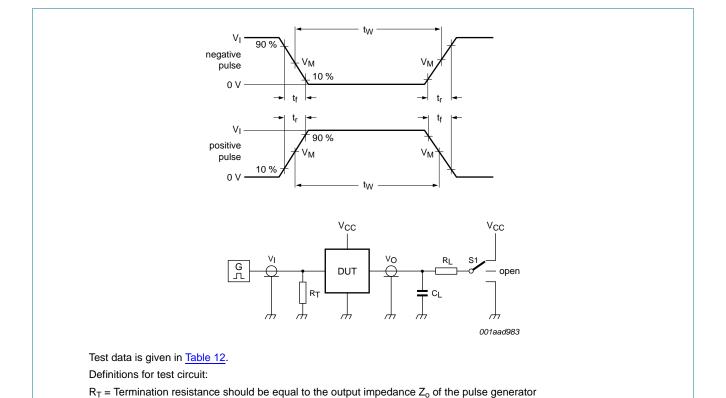


Fig 7. Test circuit for measuring switching times

 C_L = Load capacitance including jig and probe capacitance

Table 12. Test data

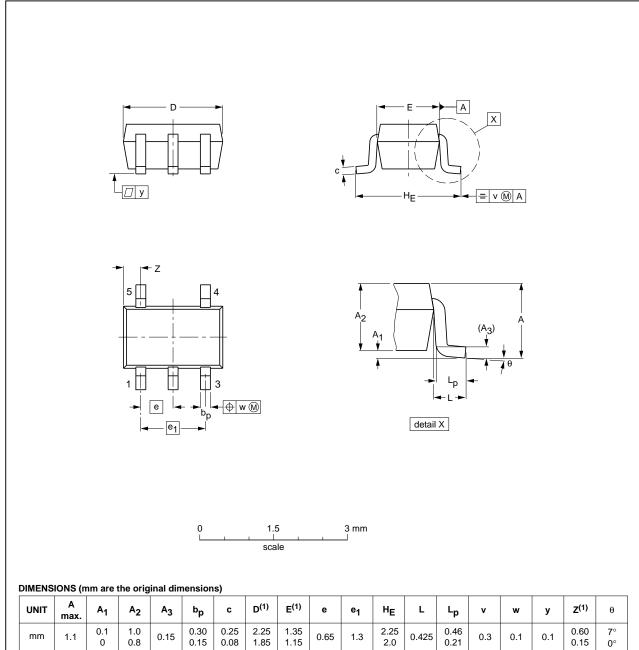
R_L = Load resistor S1 = Test selection switch

Туре	Input		Load		S1 position		
	V_{I}	t _r , t _f	CL	R_L	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
74HC1G125-Q100	V_{CC}	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V_{CC}
74HCT1G125-Q100	3 V	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}

13. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT353-1		MO-203	SC-88A			-00-09-01- 03-02-19

Fig 8. Package outline SOT353-1 (TSSOP5)

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Plastic surface-mounted package; 5 leads

SOT753

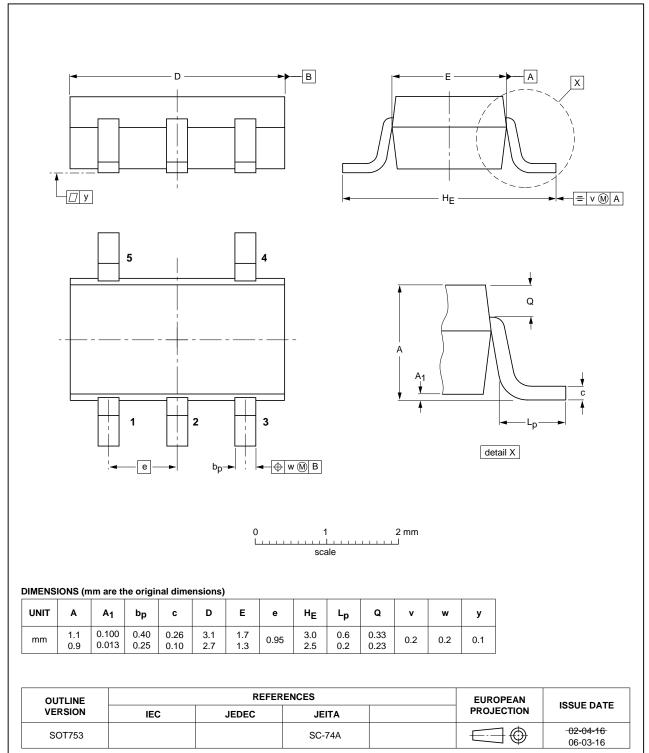


Fig 9. Package outline SOT753 (SC-74A)

14. Abbreviations

Table 13. Abbreviations

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
ESD	ElectroStatic Discharge
НВМ	Human Body Model
TTL	Transistor-Transistor Logic
MM	Machine Model

15. Revision history

Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74HC_HCT1G125_Q100 v.1	20130618	Product data sheet	-	-

16. Legal information

16.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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74HC1G125-Q100; 74HCT1G125-Q100

Nexperia

Bus buffer/line driver; 3-state

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- Техническую поддержку проекта.
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- Изготовление тестовой платы монтаж и пусконаладочные работы.



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