

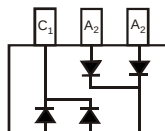
Features

- Fast Switching Speed
- Ultra-Small Surface Mount Package
- For General Purpose Switching Applications
- High Conductance
- Two "BAV70" Circuits In One Package
- **Lead Free/RoHS Compliant (Note 3)**
- **"Green" Device (Note 4 and 5)**



TOP VIEW

SOT-363


 TOP VIEW
 Internal Schematic

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Orientation: See Diagram
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.006 grams (approximate)

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	V _{RM}	100	V
Peak Repetitive Reverse Voltage	V _{RRM}	75	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
RMS Reverse Voltage	V _{R(RMS)}	53	V
Forward Continuous Current (Note 1)	I _{FM}	300	mA
Average Rectified Output Current (Note 1)	I _O	150	mA
Non-Repetitive Peak Forward Surge Current	I _{FSM}	2.0	A
		1.0	
		@ t = 1.0μs	
		@ t = 1.0s	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	P _D	200	mW
Thermal Resistance Junction to Ambient Air (Note 1)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	V _{(BR)R}	75	—	V	I _F = 2.5μA
Forward Voltage	V _F	—	0.715	V	I _F = 1.0mA
			0.855		I _F = 10mA
			1.0		I _F = 50mA
			1.25		I _F = 150mA
Reverse Current (Note 2)	I _R	—	2.5	μA	V _R = 75V
			50	μA	V _R = 75V, T _J = 150°C
			30	μA	V _R = 25V, T _J = 150°C
			25	nA	V _R = 20V
Total Capacitance	C _T	—	2.0	pF	V _R = 0, f = 1.0MHz
Reverse Recovery Time	t _{rr}	—	4.0	ns	I _F = I _R = 10mA, I _{rr} = 0.1 x I _R , R _L = 100Ω

- Notes:
1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
 2. Short duration pulse test used to minimize self-heating effect.
 3. No purposefully added lead.
 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 5. Product manufactured with Date Code UO (week 40, 2007) and never are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.

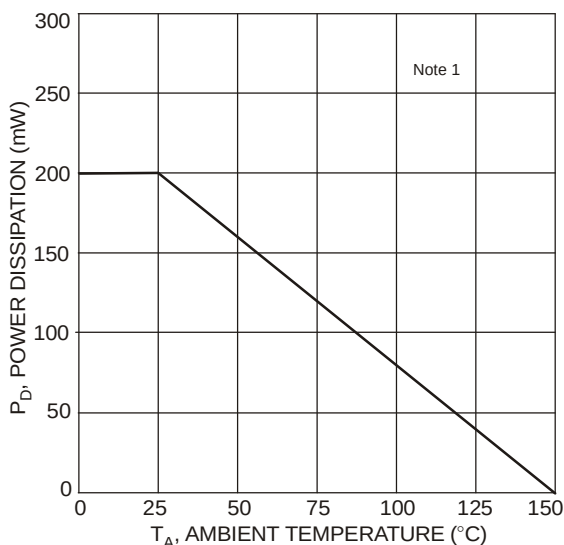


Fig. 1 Power Derating Curve, Total Package

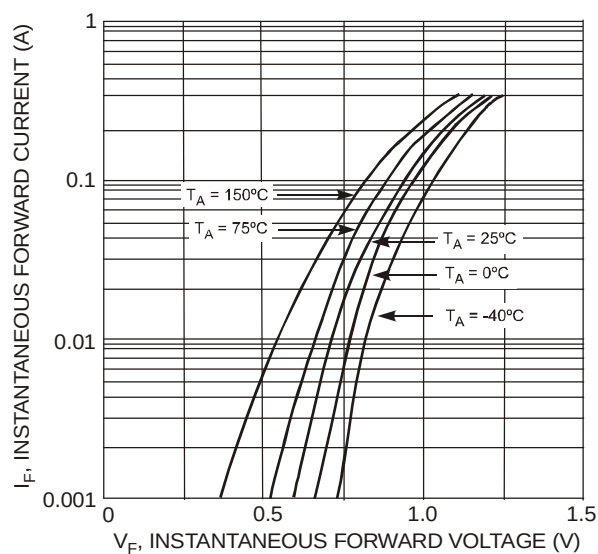


Fig. 2 Typical Forward Characteristics, Per Element

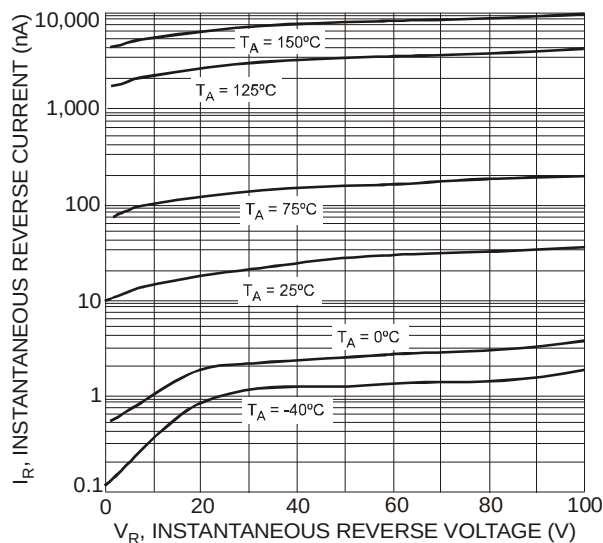


Fig. 3 Typical Reverse Characteristics, Per Element

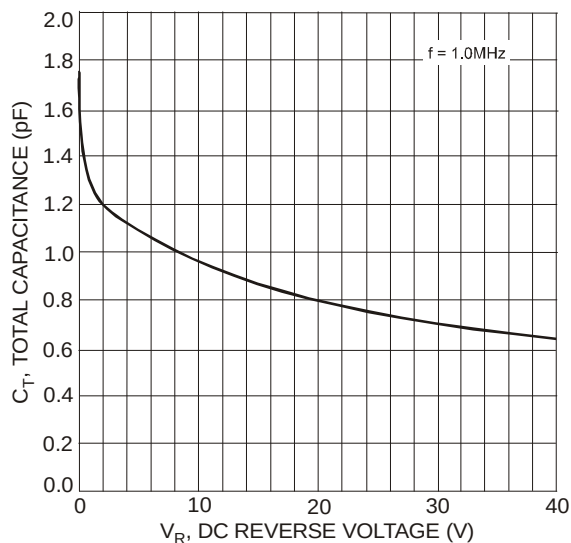


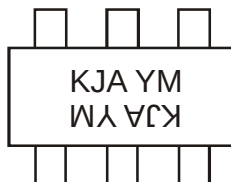
Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

Ordering Information (Note 6)

Part Number	Case	Packaging
BAV70DW-7-F	SOT-363	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



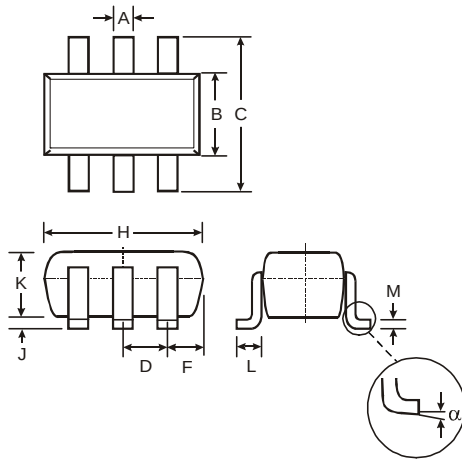
KJA = Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: N = 2002
 M = Month ex: 9 = September

Date Code Key

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2111	2012
Code	M	N	P	R	S	T	U	V	W	X	Y	Z

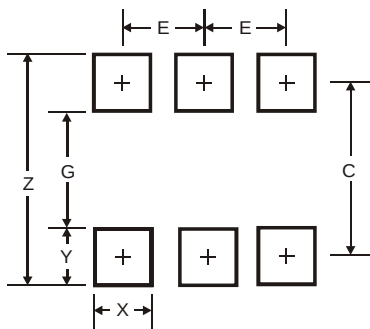
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SOT-363		
Dim	Min	Max
A	0.10	0.30
B	1.15	1.35
C	2.00	2.20
D	0.65 Nominal	
F	0.30	0.40
H	1.80	2.20
J	—	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.25
α	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C	1.9
E	0.65

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- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
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- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
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- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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