Document Number: MPXHZ6130A

Rev. 1.2, 06/2015



## MPXHZ6130A, 15 to 130 kPa, Absolute, Integrated Pressure Sensor

The MPXHZ6130A series sensor integrates on-chip, bipolar op amp circuitry and thin film resistor networks to provide a high output signal and temperature compensation. The sensor's packaging has been designed to provide resistance to high humidity conditions as well as common automotive media. The small form factor and high reliability of on-chip integration make this pressure sensor a logical choice for the system designer.

The MPXHZ6130A series piezoresistive transducer is a state-of-the-art, monolithic, signal conditioned, silicon pressure sensor. This sensor combines advanced micromachining techniques, thin film metallization, and bipolar semiconductor processing to provide an accurate, high level analog output signal that is proportional to applied pressure.

#### **Features**

- 1.5% maximum error over 0 °C to 85 °C
- Resistant to high humidity and common automotive media
- Improved accuracy at high temperature
- · Ideally suited for microprocessor or microcontroller-based systems
- Temperature compensated from -40 °C to +125 °C
- Durable thermoplastic (PPS) surface mount package

#### Typical applications

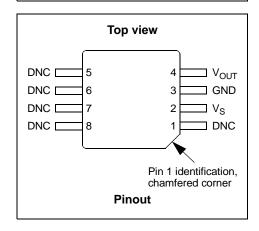
- · Aviation altimeters
- Industrial controls
- Engine control/manifold absolute pressure (MAP)
- · Weather stations and weather reporting devices

## MPXHZ6130A

# Super small outline packages

MPXHZ6130A6U Case 98ARH99066A

MPXHZ6130AC6U Case 98ARH99089A



Ordering information									
Part number	Chinning	Dookowa		# of Ports		Pressure type			Device
Part number	Shipping	Package	None	Single	Dual	Gauge	Differential	Absolute	marking
Small Outline Packag	Small Outline Package (MPXHZ6130A series)								
MPXHZ6130A6U	Rail	98ARH99066A	•					•	MPXHZ6130A
MPXHZ6130AC6U	Rail	98ARH99089A		•				•	MPXHZ6130A

Freescale reserves the right to change the detail specifications as may be required to permit improvements in the design of its products.

© 2006-2010, 2012, 2015 Freescale Semiconductor, Inc. All rights reserved.





## **Contents**

1	General Description	3
	1.1 Block diagram	3
	1.2 Pinout	3
2	Mechanical and Electrical Specifications	4
	2.1 Maximum ratings	4
	2.2 Operating characteristics	4
3	On-chip Temperature Compensation and Calibration	!
4	Package Information.	7
	4.1 Minimum recommended footprint for super small packages	7
	4.2 Package dimensions	8
	Revision History	

#### **Related Documentation**

The MPXHZ6130A device features and operations are described in a variety of reference manuals, user guides, and application notes. To find the most-current versions of these documents:

1. Go to the Freescale homepage at:

http://www.freescale.com/

- 2. In the Keyword search box at the top of the page, enter the device number MPXHZ6130A.
- 3. In the Refine Your Result pane on the left, click on the Documentation link.



## 1 General Description

## 1.1 Block diagram

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

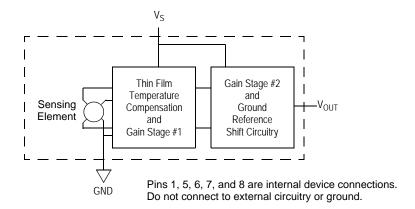


Figure 1. Fully integrated pressure sensor schematic

#### 1.2 Pinout

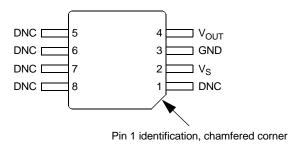


Figure 2. Device pinout (top view)

**Table 1. Pin functions** 

Pin	Name	Function
1	DNC	Do not connect to external circuitry or ground. Pin 1 is denoted by chamfered corner.
2	V <sub>S</sub>	Voltage supply
3	GND	Ground
4	V <sub>OUT</sub>	Output voltage
5	DNC	Do not connect to external circuitry or ground.
6	DNC	Do not connect to external circuitry or ground.
7	DNC	Do not connect to external circuitry or ground.
8	DNC	Do not connect to external circuitry or ground.



## 2 Mechanical and Electrical Specifications

### 2.1 Maximum ratings

Table 2. Maximum ratings<sup>(1)</sup>

Rating	Symbol	Value	Units
Maximum pressure (P1 > P2)	P <sub>max</sub>	400	kPa
Storage temperature	T <sub>stg</sub>	-40 to +125	°C
Operating temperature	T <sub>A</sub>	-40 to +125	°C
Output source current @ full-scale output <sup>(2)</sup>	l <sub>o</sub> +	0.5	mAdc
Output sink current @ minimum pressure offset <sup>(2)</sup>	I <sub>o</sub> -	-0.5	mAdc

<sup>1.</sup> Exposure beyond the specified limits may cause permanent damage or degradation to the device.

### 2.2 Operating characteristics

Table 3. Operating characteristics ( $V_S = 5.0 \text{ Vdc}$ ,  $T_A = 25 ^{\circ}\text{C}$  unless otherwise noted, P1 > P2).

Characteristic	Symbol	Min	Тур	Max	Unit
Pressure range	P <sub>OP</sub>	15	_	130	kPa
Supply voltage <sup>(1)</sup>	V <sub>S</sub>	4.75	5.0	5.25	Vdc
Supply current	Io	_	6.0	10	mAdc
Minimum pressure offset <sup>(2)</sup> @ V <sub>S</sub> = 5.0 Volts (0 °C to 85 °C)	V <sub>off</sub>	0.132	0.200	0.268	Vdc
Full-scale output <sup>(3)</sup> @ V <sub>S</sub> = 5.0 Volts (0 °C to 85 °C)	V <sub>FSO</sub>	4.632	4.700	4.768	Vdc
Full-scale span <sup>(4)</sup> @ V <sub>S</sub> = 5.0 Volts (0 °C to 85 °C)	V <sub>FSS</sub>	4.365	4.500	4.635	Vdc
Accuracy <sup>(5)</sup> @ V <sub>S</sub> = 5.0 Volts (0 °C to 85 °C)	_	_	_	±1.5	%V <sub>FSS</sub>
Sensitivity	V/P	_	39.2	_	mV/kPa
Response time <sup>(6)</sup>	t <sub>R</sub>	_	1.0	_	ms
Warm-up time <sup>(7)</sup>	_	_	20	_	ms
Offset stability <sup>(8)</sup>	_	_	±0.25	_	%V <sub>FSS</sub>

<sup>1.</sup>Device is ratiometric within this specified excitation range.

Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.

Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to

and from the minimum or maximum operating temperature points, with zero differential pressure applied.

Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from minimum

or maximum rated pressure at 25 °C.

TcSpan: Output deviation over the temperature range of 0 °C to 85 °C, relative to 25 °C.

TcOffset: Output deviation with minimum pressure applied, over the temperature range of 0 °C to 85 °C, relative to 25 °C.

- 6.Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to at specified step change in pressure.
- 7. Warm-up time is defined as the time required for the product to meet the specified output voltage after the pressure has been stabilized.
- 8.Offset Stability is the product's output deviation when subjected to 1000 cycles of Pulsed Pressure, Temperature Cycling with Bias Test.

<sup>2.</sup> Maximum output current is controlled by effective impedance from V<sub>OUT</sub> to Gnd or V<sub>OUT</sub> to V<sub>S</sub> in the application circuit.

<sup>2.</sup>Offset (Voff) is defined as the output voltage at the minimum rated pressure.

<sup>3.</sup>Full-scale output ( $V_{FSO}$ ) is defined as the output voltage at the maximum or full rated pressure.

<sup>4.</sup> Full-scale span (V<sub>FSS</sub>) is defined as the algebraic difference between the output voltage at full-rated pressure and the output voltage at the minimum rated pressure.

<sup>5.</sup>Accuracy is the deviation in actual output from nominal output over the entire pressure range and temperature range as a percent of span at 25°C due to all sources of error including the following:



## 3 On-chip Temperature Compensation and Calibration

Figure 5 shows the sensor output signal relative to pressure input. Typical minimum and maximum output curves are shown for operation over 0 to 85°C temperature range. The output will saturate outside of the rated pressure range.

A gel die coat isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the sensor diaphragm. The gel die coat and durable polymer package provide a media resistant barrier that allows the sensor to operate reliably in high humidity conditions as well as environments containing common automotive media. Contact the factory for more information regarding media compatibility in your specific application.

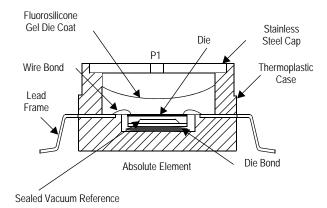


Figure 3. Cross-sectional diagram SSOP (not to scale)

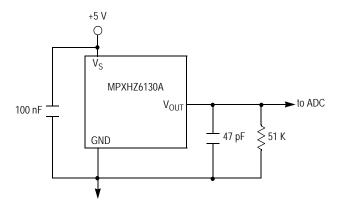


Figure 4. Recommended power supply decoupling and output filtering

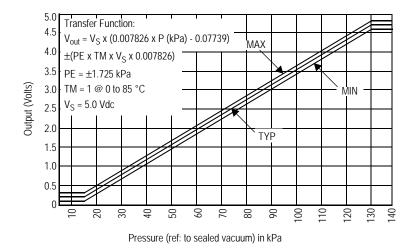


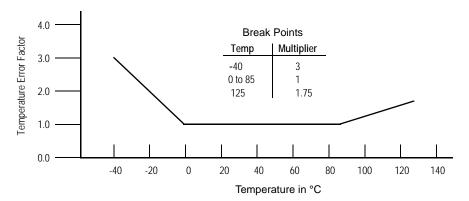
Figure 5. Output vs. absolute pressure



Nominal Transfer Value:  $V_{OUT} = V_S \times (0.007826 \times P \text{ (kPa)} - 0.07739) \pm (Pressure Error \times Temp. factor \times 0.007826 \times V_S)$ 

 $V_S = 5.0 \pm 0.25 \text{ Vdc}$ 

Figure 6. Transfer function



NOTE: The temperature multiplier is a linear response from 0 °C to -40 °C and from 85 °C to 125 °C

Figure 7. Temperature error band

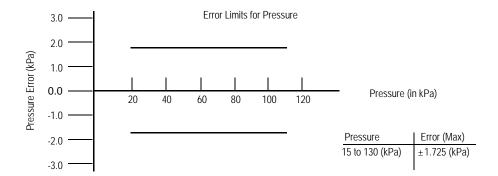


Figure 8. Pressure error band



## 4 Package Information

## 4.1 Minimum recommended footprint for super small packages

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor package must be the correct size to ensure proper solder connection interface between the board and the package. With the correct pad geometry, the packages will self-align when subjected to a solder reflow process. It is always recommended to fabricate boards with a solder mask layer to avoid bridging and/or shorting between solder pads, especially on tight tolerances and/or tight layouts.

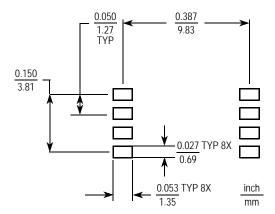
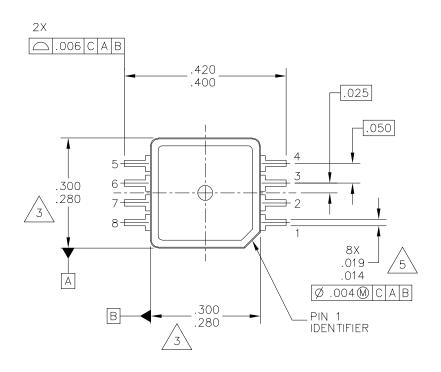


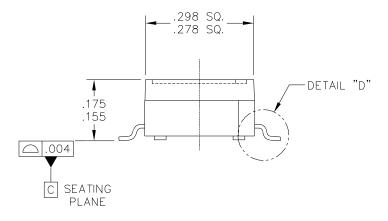
Figure 9. SSOP footprint



## 4.2 Package dimensions

This drawing is located at http://cache.freescale.com/files/shared/doc/package\_info/98ARH99066A.pdf.

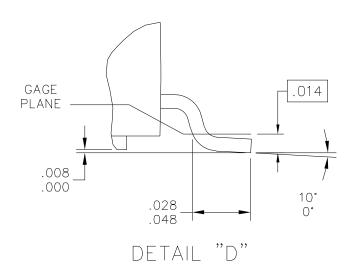




	ALE SEMICONDUCTOR, INC. RIGHTS RESERVED.	MECHANICAL OU	TLINE	PRINT VERSION NO	T TO SCALE
TITLE:	8 LFAD		DOCUME	NT NO: 98ARH99066A	REV: H
	SSAP		CASE NU	JMBER: 1317-04	13 APR 2012
	3301		STANDAF	RD: NON-JEDEC	

#### Case 98ARH99066A, 8-lead super small outline package





	ALE SEMICONDUCTOR, INC. RIGHTS RESERVED.	MECHANICAL OU	TLINE	PRINT VERSION NO	T TO SCALE
TITLE:	8 I FAD		DOCUME	NT NO: 98ARH99066A	REV: H
	CCUD		CASE NU	JMBER: 1317-04	13 APR 2012
	3301		STANDAF	RD: NON-JEDEC	

## Case 98ARH99066A, 8-lead super small outline package



#### NOTES:

- 1. ALL DIMENSIONS IN INCHES.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.



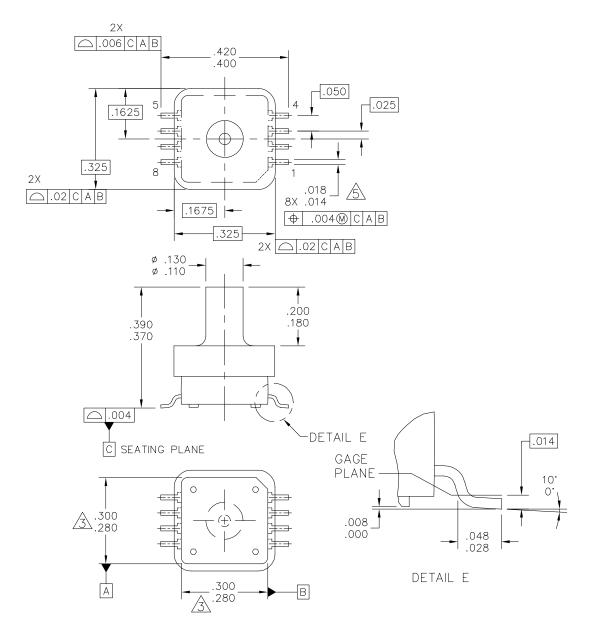
4. ALL VERTICAL SURFACES TO BE 5' MAXIMUM.

DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION.
ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 INCHES MAXIMUM.

© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICAL OUTLIN	NE PRINT VERSION NO	T TO SCALE
TITLE: 8   FAD	DOC	CUMENT NO: 98ARH99066A	REV: H
	CAS	SE NUMBER: 1317-04	13 APR 2012
3306	STA	ANDARD: NON-JEDEC	

## Case 98ARH99066A, 8-lead super small outline package





© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICAL OU	TLINE	PRINT VERSION NOT TO SCALE
TITLE:		DOCUMEN	NT NO: 98ARH99089A REV: G
8 LD, PORTED	SS0P	STANDAF	RD: NON-JEDEC
			09 NOV 2012

## Case 98ARH99089A, 8-lead ported super small outline package



#### NOTES:

- 1. ALL DIMENSIONS IN INCHES.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSION SHALL NOT EXCEED .006 INCHES PER SIDE.

4. ALL VERTICAL SURFACES TO BE 5' MAXIMUM.

DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION.
ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 INCHES MAXIMUM.

© FREESCALE SEMICONDUCTOR, INC. ALL RIGHTS RESERVED.	MECHANICAL OU	TLINE	PRINT VERSION NOT	TO SCALE
TITLE:		DOCUMEN	NT NO: 98ARH99089A	REV: G
8 LD, PORTED	SSOP	STANDAF	RD: NON-JEDEC	
			09	9 NOV 2012

Case 98ARH99089A, 8-lead ported super small outline package



# 5 Revision History

## Table 4. Revision history

Revision number	Revision date	Description
1.1	05/2012	Updated package drawing 98ARH99066A was Rev. F, updated to Rev. H.
1.2	06/2015	<ul><li>Updated format.</li><li>Updated package drawing 98ARH99089A was Rev D, updated to Rev G.</li></ul>



How to Reach Us:

Home Page: freescale.com

Web Support:

freescale.com/support

Information in this document is provided solely to enable system and software implementers to use Freescale products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document.

Freescale reserves the right to make changes without further notice to any products herein. Freescale makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. Freescale does not convey any license under its patent rights nor the rights of others. Freescale sells products pursuant to standard terms and conditions of sale, which can be found at the following address: freescale.com/salestermsandconditions.

Freescale and the Freescale logo are trademarks of Freescale Semiconductor, Inc., Reg. U.S. Pat. & Tm. Off. All other product or service names are the property of their respective owners.

© 2006-2010, 2012, 2015 Freescale Semiconductor, Inc.

Document Number: MPXHZ6130A

Rev. 1.2 06/2015





OOO «ЛайфЭлектроникс" "LifeElectronics" LLC

ИНН 7805602321 КПП 780501001 P/C 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

#### Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



Тел: +7 (812) 336 43 04 (многоканальный) Email: org@lifeelectronics.ru