

ABSOLUTE MAXIMUM RATINGS

(Note 1)

V_{IN} , V_{OUT} Voltages	-0.3V to 6V
USBP, MODE, CHP Voltage	-0.3V to 6V
I_{VOUT} (Note 5)	2A
Operating Ambient Temperature Range	
LTC4410E (Notes 3, 4)	-40°C to 85°C
Storage Temperature Range	-65°C to 150°C
Lead Temperature (Soldering, 10 sec)	300°C

PACKAGE/ORDER INFORMATION

	ORDER PART NUMBER
	LTC4410ES6
	S6 PART MARKING
	LTK8

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS

The ● denotes specifications that apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_{IN} = 5\text{V}$ unless otherwise noted.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Operating V_{IN} Range		● 4.35		5.5	V
Quiescent Current—Active	100mA Mode (MODE = Low) 500mA Mode (MODE = High)		500 80	700 125	μA μA
CHP Compliance Voltage	MODE = 5V, $I_{VOUT} = 500\text{mA}$ MODE = 0V, $I_{VOUT} = 0\text{mA}$	0 0		4.0 3.5	V V
Reverse Current	$V_{OUT} = 4\text{V}$, $V_{IN} = 0\text{V}$		1	5	μA
MODE Input Current	MODE = 0V MODE = 5V		0.1 0.1	5 5	μA μA
MODE Input High		● 1.2			V
MODE Input Low		●		0.4	V
USBP Output High	1M Resistor to GND, $V_{IN} = 5\text{V}$	4.8	4.98		V
USBP Output Low	1M Resistor to V_{IN} , $V_{IN} = 3.5\text{V}$		20	100	mV
UVLO and USBP Threshold	Measured on V_{IN} Rising	● 4.05	4.2	4.35	V
UVLO and USBP Hysteresis	Measured on V_{IN}	80	100	120	mV
CHP Output Current	MODE = High, $I_{VOUT} = 0\text{mA}$ (Note 2)		0	30	μA
	MODE = High, $I_{VOUT} = 500\text{mA}$ (Note 2)		470	530	μA
	MODE = High, $I_{VOUT} = 500\text{mA}$, $T_A \geq 0^\circ\text{C}$, (Note 2)	●	460	540	μA
	MODE = High, $I_{VOUT} = 500\text{mA}$, $T_A < 0^\circ\text{C}$, (Note 2)	●	450	550	μA
	MODE = Low, $I_{VOUT} = 0\text{mA}$ (Note 2)		430	370	μA
	MODE = Low, $I_{VOUT} = 100\text{mA}$ (Note 2)		470	510	μA

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: Current flows out of CHP, voltage at CHP = 0V.

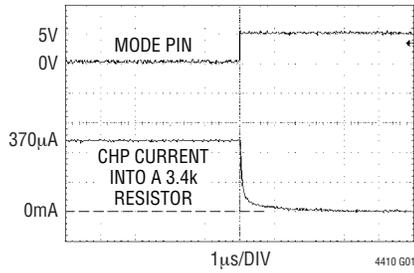
Note 3: The LTC4410E is guaranteed to meet performance specifications from 0°C to 70°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

Note 4: This IC includes overtemperature protection that is intended to protect the device during momentary overload conditions. Junction temperature will exceed 125°C when overtemperature protection is active. Continuous operation above the specified maximum operating junction temperature may impair device reliability.

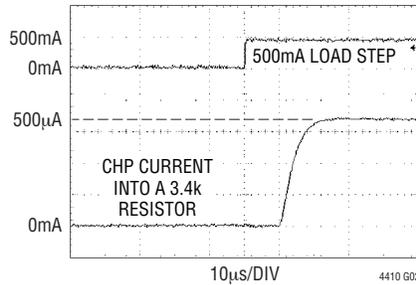
Note 5: Based on long-term current density limitations.

TYPICAL PERFORMANCE CHARACTERISTICS

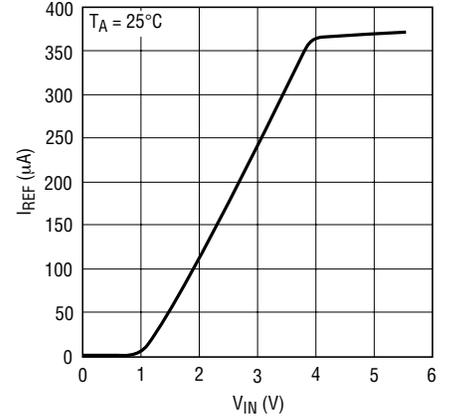
I_{REF} Turn-Off Response



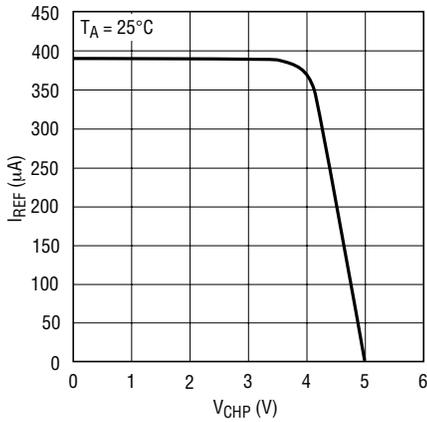
CHP Current with I_{LOAD} Step



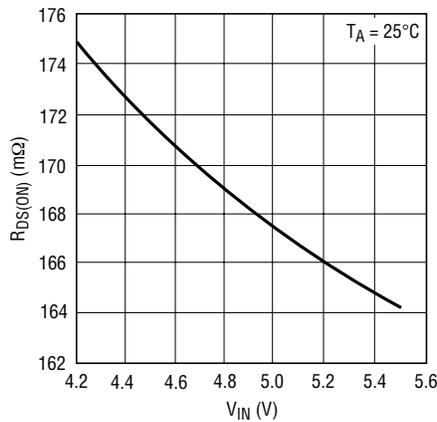
I_{REF} vs V_{IN} (MODE = 0)



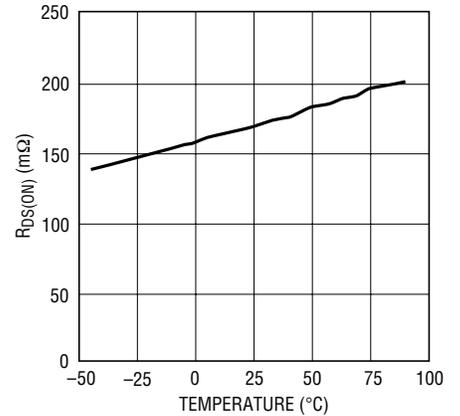
I_{REF} vs V_{CHP} (MODE = 0)



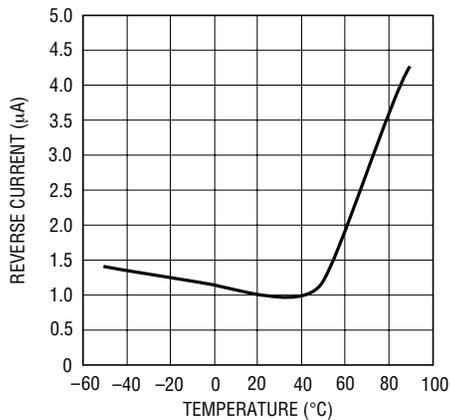
R_{DS(ON)} vs V_{IN}



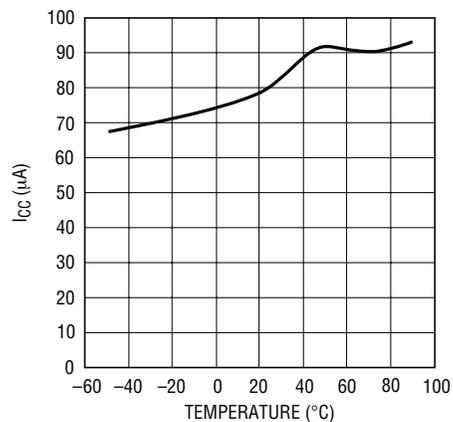
R_{DS(ON)} vs Temperature



Reverse Current vs Temperature



I_{CC} vs Temperature



PIN FUNCTIONS

V_{IN} (Pin 1): Positive Input Supply. When $V_{IN} > 4.2V$, the internal undervoltage lockout enables the main switch that connects V_{IN} to V_{OUT} . Bypass V_{IN} with a $10\mu F$ ceramic capacitor and a 1Ω resistor in series or use a $10\mu F$ capacitor with at least 1Ω minimum ESR. This minimizes the voltage transient that can occur when the input is hot switched.

GND (Pin 2): Signal Ground for the LTC4410.

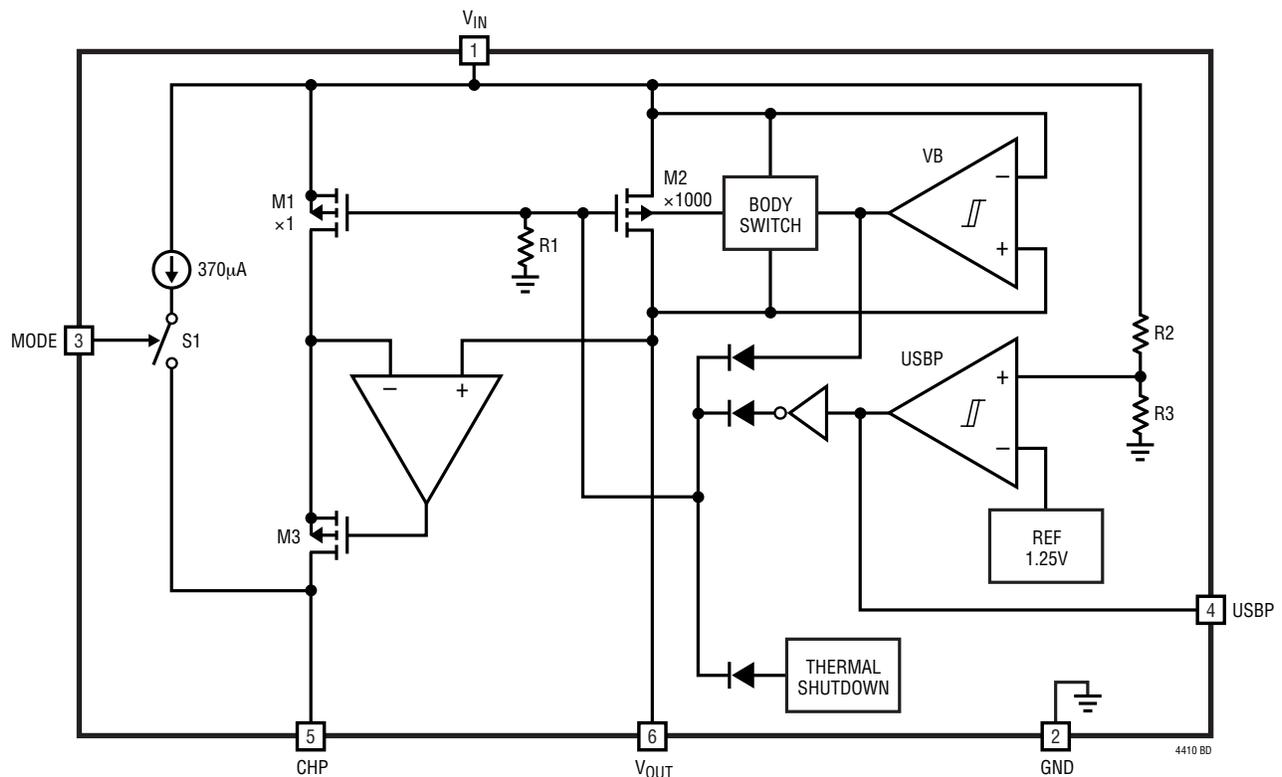
MODE (Pin 3): Mode Select Input. This pin selects the maximum USB port current of either 100mA or 500mA. When MODE is high, the current out of CHP will be $I_{V_{OUT}}/1000$. When MODE is low, the current out of CHP will be $I_{V_{OUT}}/1000 + 370\mu A$ (typical).

USBP (Pin 4): USB Voltage Present Output. This pin goes high when V_{IN} exceeds the undervoltage lockout threshold ($4.2V$ typical).

CHP (Pin 5): Charger Program Output. This pin sources a current that is used by the battery charger to control charge current.

V_{OUT} (Pin 6): LTC4410 Output. Bypass this pin with a $10\mu F$ or larger X5R ceramic capacitor. This capacitor may be omitted if other circuitry connected to V_{OUT} contains a bypass capacitor. When $V_{OUT} > V_{IN}$, the internal switch is held off, resulting in very low battery drain current ($1\mu A$ typical).

BLOCK DIAGRAM



OPERATION

The LTC4410 manages the total current consumption between a battery charger and a USB portable device. In a system using the LTC4410, a battery charger can be set up to charge at the maximum available current from the USB port without any need to reserve current for the device load. As the USB device current increases, the LTC4410 decreases the battery charge current so as not to exceed the maximum allowable current from the USB port. This allows for simultaneous battery charging and device operation. The LTC4410 can only reduce the battery charge current to zero. It is the responsibility of the USB device load to not exceed the USB power limits.

The LTC4410 uses an internal power MOSFET to sense load current. This MOSFET is held off when $V_{OUT} > V_{IN}$ or $V_{IN} < 4.2V$ (typ). A replica of the I_{VOUT} current equal to $I_{VOUT}/1000$ is sourced out of the CHP pin with an accuracy of $\pm 6\%$. This current can be summed into the PROG node of a battery charger to reduce the charge current. The LTC4410 is primarily designed to interface with battery chargers that use a program pin to set the maximum charge current, with a charge current to program pin current ratio of 1000:1. The voltage on CHP can be externally monitored to signal overcurrent conditions.

When $V_{IN} > 4.2V$, the internal USB present comparator forces the USBP pin high. This signal can be used to detect

when the USB voltage is present and drive an external switch to connect or disconnect a battery from the USB peripheral.

The MODE pin controls the state of an internal $370\mu A$ current reference (I_{REF}). When active (MODE pin low), the total current sourced out of CHP is $I_{VOUT}/1000 + I_{REF}$. The primary function of the current reference is to offset the battery charger charge current for 100mA mode. When MODE is high, the current reference is disabled. The total current out of CHP with MODE high is $I_{VOUT}/1000$. The MODE controlled current offset makes it possible to configure the battery charger and the LTC4410 to support the USB specification 1.0 and 2.0 required 100mA and 500mA modes of operation.

The low quiescent current ($80\mu A$, when MODE is high) of the LTC4410 makes the system easily compliant with the USB specifications 1.0 and 2.0 SUSPEND MODE current consumption requirements.

When $V_{OUT} > V_{IN}$, the LTC4410 transitions to low power mode, draining $1\mu A$ (typical) from the Lithium-Ion battery. This condition occurs when the USB device is operating off of its internal battery and not connected to the USB port.

APPLICATIONS INFORMATION

USB Power Management and Wall Adapter Power

With the addition of a few components, the LTC4410 allows for simultaneous device operation and battery charging while connected to USB port or wall adapter. The LTC4410 will proportionally reduce the battery charge current to keep the total current within the current rated for the wall adapter. Figure 1 shows an example of how to combine the USB power and wall adapter inputs. A P-channel MOSFET, MP1, is used to prevent back conducting into the USB port when a wall adapter is present. The pull-down resistor, R1, is to assure that MP1 is on

when there is no wall adapter. The Schottky diode, D1, is used to prevent USB power loss through R1. If the wall adapter used has more than 500mA capability, a resistor, R3, connected in parallel with R2 using N-channel MOSFET MN1 will increase the charge current when the wall adapter is present. In this example the battery charge current is 340mA without the wall adapter (see the LTC4053 data sheet for details on how to program the battery charge current). When the wall adapter is present, the charge current is 750mA.

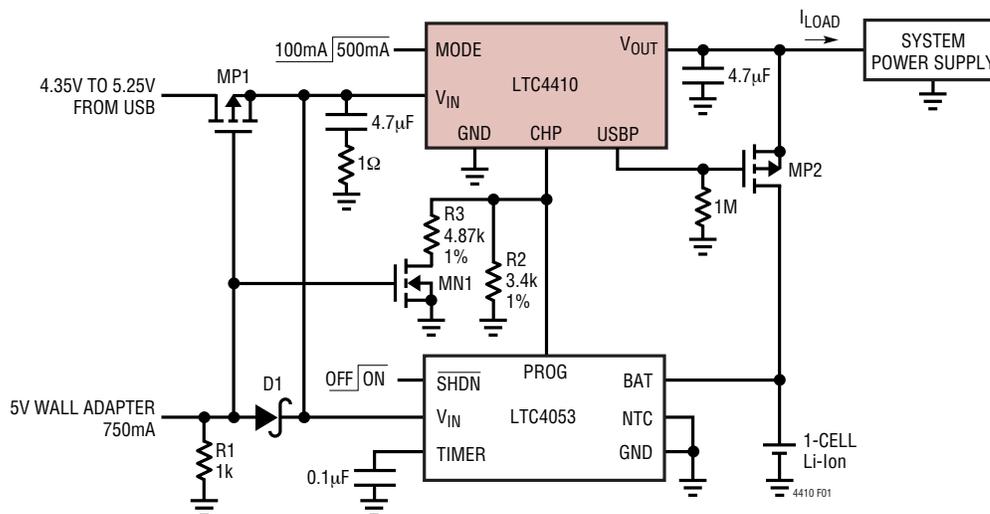
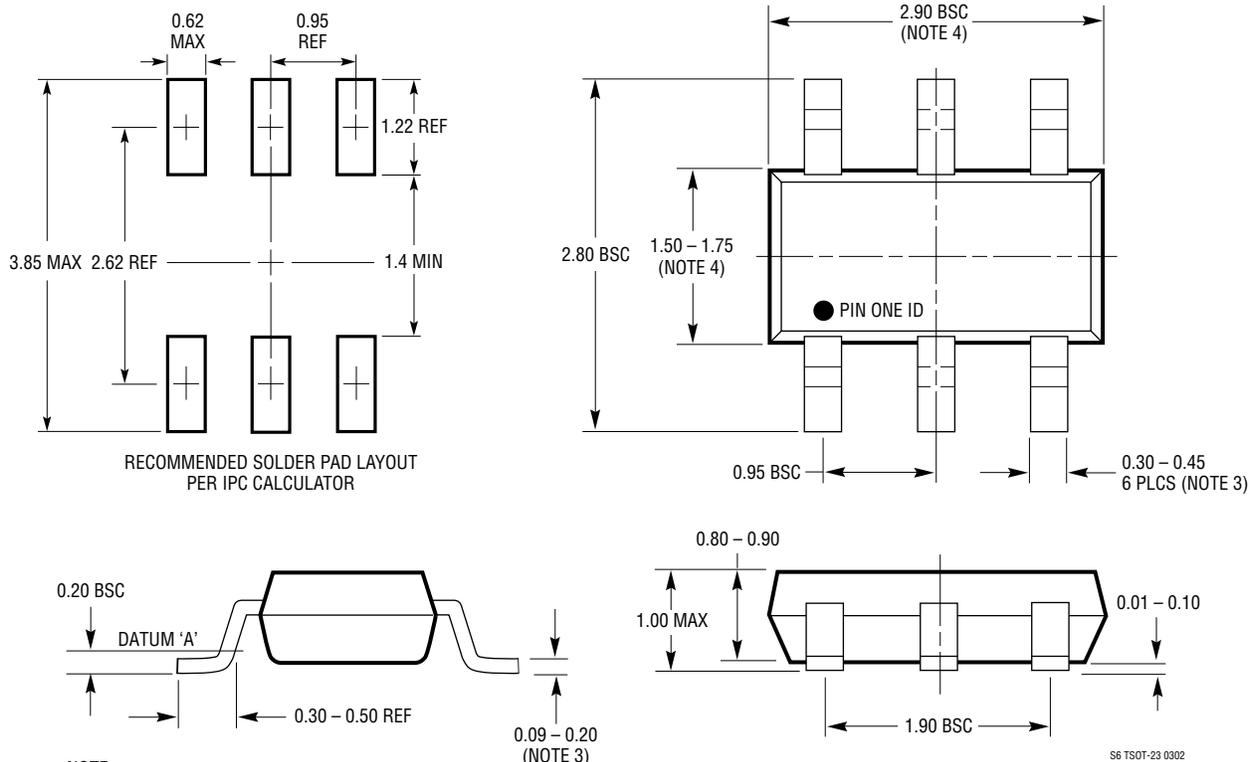


Figure 1. USB Power Management and Wall Adapter Power

PACKAGE DESCRIPTION

S6 Package
6-Lead Plastic TSOT-23
 (Reference LTC DWG # 05-08-1636)



- NOTE:
1. DIMENSIONS ARE IN MILLIMETERS
 2. DRAWING NOT TO SCALE
 3. DIMENSIONS ARE INCLUSIVE OF PLATING
 4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR
 5. MOLD FLASH SHALL NOT EXCEED 0.254mm
 6. JEDEC PACKAGE REFERENCE IS MO-193

RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LTC1733	Li-Ion Linear Charger with Thermal Regulation	Standalone Charger, No External MOSFET, Diode or Sense Resistor
LTC1734	200mA to 700mA Li-Ion Charger in ThinSOT	Tiny Design, No Blocking Diode, No Sense Resistor
LTC1734L	50mA to 180mA Li-Ion Charger in ThinSOT	Tiny Design, High Accuracy for Low Charge Current, No Blocking Diode, No Sense Resistor
LTC4053	USB Li-Ion Battery Charger	Operates from USB Port, 100mA/500mA Charge Capability, Standalone Charger
LTC4054	800mA Li-Ion Battery Charger in ThinSOT	Standalone Charger, Thermal Regulation, C/10 Charge Termination
LTC4055	USB Power Controller and Li-Ion Linear Charger	Load Dependent Charging Guarantees USB Input Current Compliance, Automatic Battery Switchover when Supply is Removed, Tiny 4mm x 4mm QFN Package
LTC4412	PowerPath™ Controller in ThinSOT	More Efficient than Diode OR'ing, Automatic Switching Between DC Sources, Simplified Load Sharing, $3V \leq V_{IN} \leq 28V$

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- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
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