

Property Rootes

DC Brushless Motor Drivers for Cooling Fans Two-phase Full-wave DC Brushless Fan Motor Drivers

BA6901F

General description

BA6901F is two-phase half-wave fan motor driver. They incorporate lock protection, automatic restart circuit and FG/AL output. Some of them have variable speed control function.

Features

- Current Limit circuit
- PWM control (PWM pulse signal input)
- Incorporates lock protection and automatic restart
- circuit
- Lock alarm signal(AL) output
- Rotation speed pulse signal (FG) output

SOP-16

W (Typ.) x D (Typ.) x H (Max.) 10.00mm x 6.20mm x 1.71mm



Application

For desktop PC, server, general consumer equipment, communication equipment and industrial equipment.

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Supply voltage	Vcc	36	V
Power dissipation	Pd	625*	mW
Operating temperature	Topr	-40 ~ +100	
Storage temperature	Tstg	-55 ~ +150	
Output current	Iomax	70	mA
FG signal output current	lfg	15	mA
FG signal output voltage	Vfg	36	V
AL signal output current	lal	15	mA
AL signal output voltage	Val	36	V
Junction temperature	Tjmax	150	

Reduce by 5.0mW/°C over Ta=25°C. (On 70.0mm×70.0mm×1.6mm glass epoxy board)

Recommended operating conditions

Parameter	Symbol	Limit	Unit
Operating supply voltage range	Vcc	3.5 ~ 28.0	V
Hall input voltage range	VH	0 ~ Vcc-2.2	V

Pin configuration

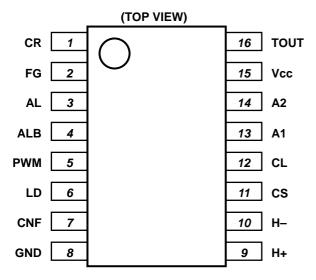


Fig. 1 Pin configuration

Pin	description	
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n descri	puon			
P/No.	T/Name	Function		
1	CR	Charging and discharging pulse circuit capacitor and resistor connecting terminal		
2	FG	Rotating speed pulse signal output terminal		
3	AL	Lock alarm signal output terminal		
4	ALB	Lock alarm signal terminal (inversion signal of AL)		
5 PWM		PWM input terminal (H or OPEN:Output ON, L:Output OFF)		
6	6 LD Lock detection and automatic restart capacitor connecting termin			
7	CNF	Phase compensating capacitor connecting terminal		
8	GND	GND terminal		
9	H+	Hall input terminal +		
10	H-	Hall input terminal -		
11	CS	Current detecting input terminal		
12	CL	Current limiting input terminal		
13	A1	Output terminal 1		
14	A2	Output terminal 2		
15	Vcc	Power terminal		
16	TOUT	Charging and discharging pulse output terminal		

Block diagram

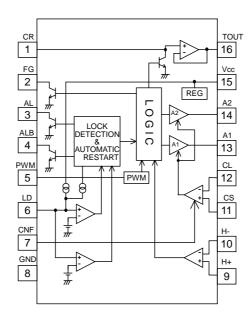


Fig.2 Block diagram

I/O truth table

Hall input			Driver output			
H+	H-	PWM	A1	A2	FG	
Н	L	H, OPEN	H(Output Tr ON)	L(Output Tr OFF)	H (Output Tr OFF)	
L	н	H, OPEN	L(Output Tr OFF)	H(Output Tr ON)	L(Output Tr ON)	
н	L	L	L(Output Tr OFF)	L(Output Tr OFF)	H(Output Tr OFF)	
L	н	L	L(Output Tr OFF)	L(Output Tr OFF)	L(Output Tr ON)	
н	L	H, OPEN	H(Output Tr ON)	L(Output Tr OFF)	H(Output Tr OFF)	

H; High, L; Low, Hi-Z; High impedance FG output is open-drain type.

AL
L
Hi-Z

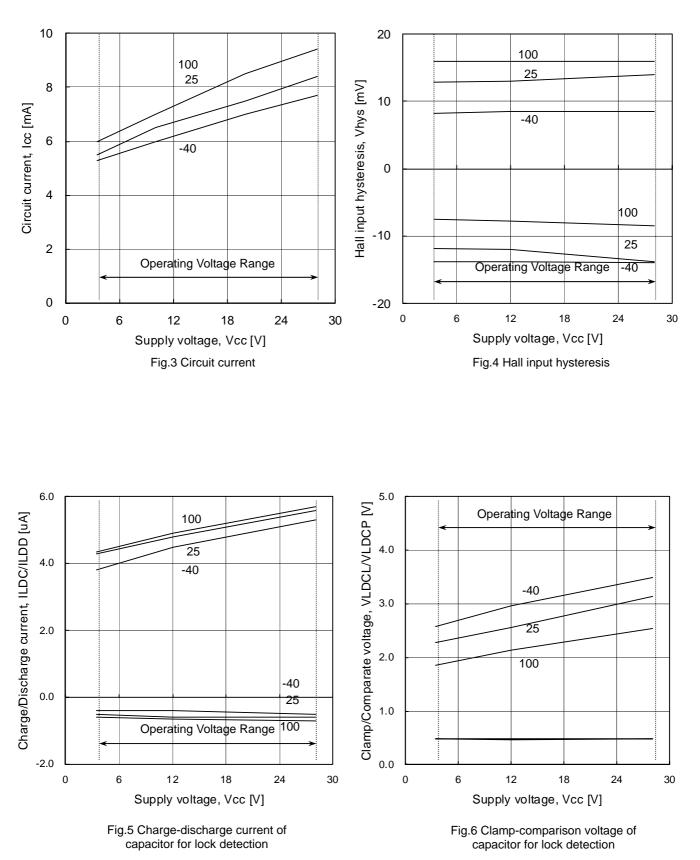
L; Low, Hi-Z; High impedance

AL output is open-drain type.

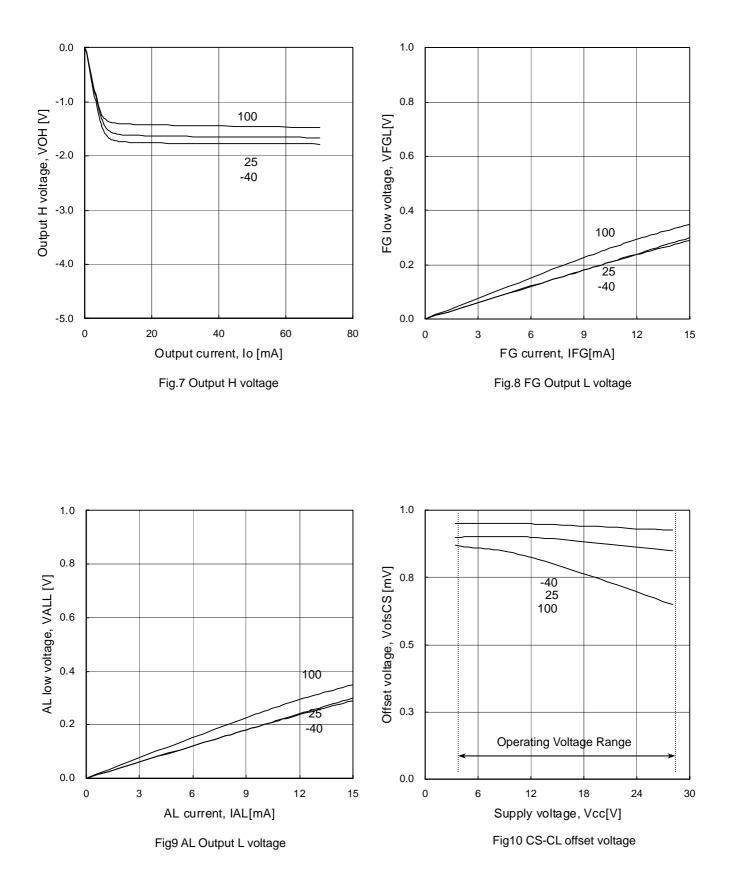
Electrical characteristics(Unless otherwise specified Ta=25°C, Vcc=12V)

Deremeter	Symbol		Limit		Unit	Conditions	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Circuit current	lcc	3.0	7.0	12.0	mA	At output OFF	
Hall input hysteresis	Vhys	±4	±10	±20	mV		
Charge current of capacitor for lock detection	lldc	2.0	5.0	8.0	μA	VId=1.5V	
Discharge current of capacitor for lock detection	lldd	0.2	0.5	0.8	μA	VId=1.5V	
Charge-discharge current ratio of capacitor for lock detection	rCD	4	10	16	-	rCD=IIdc/IIId	
Clamp voltage of capacitor for lock detection	Vldcl	1.60	2.40	3.20	V		
Comparison voltage of capacitor for lock detection	Vldcp	0.25	0.60	0.95	V		
Output H voltage	Voh	-	1.5	2.0	V	Io=-10mA Voltage between output and Vcc	
FG output L voltage	Vfgl	-	0.10	0.50	V	IFG=5mA	
AL output L voltage	Vall	-	0.10	0.50	V	IAL,IALB=5mA	
CL-CS offset voltage	Vofscs	75.0	92.0	99.5	mV	CL=100mV	
Response time for current limit	Tcs	-	50	150	Msec		
PWM input voltage H	Vpwmh	2.0	-	-	V	At output ON	
PWM input voltage L	Vpwml	-	-	0.8	V	At output OFF	
Charge-discharge pulse comparison voltage	Vcrcp	0.26	0.35	0.44	V	· · · · · · · · · · · · · · · · · · ·	
Charge-discharge pulse output voltage H	Vtoh	0.7	1.0	1.3	V	ITO=-0.5mA Voltage between output and Vcc	
Charge-discharge pulse output voltage L	Vtol	0.7	1.0	1.3	V	ITO=0.5mA	

Typical performance curves(Reference data)



Typical performance curves(Reference data)



Typical performance curves(Reference data)

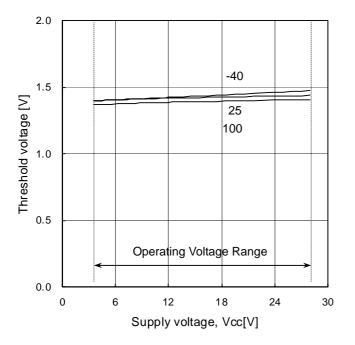


Fig.11 PWM input threshold voltage

Application circuit example(Constant values are for reference)

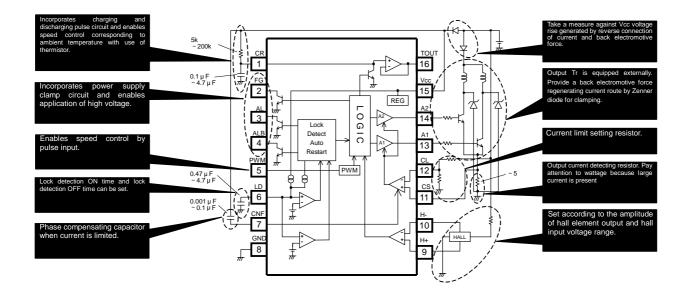


Fig.12 application circuit

Substrate design note

- a) IC power, motor outputs, and motor ground lines are made as fat as possible.
- b) IC ground (signal ground) line is common with the application ground except motor ground (i.e. hall ground etc.), and arranged near to (–) land.
- c) The bypass capacitor and/or Zenner diode are arrangement near to Vcc terminal.
- d) H+ and H– lines are arranged side by side and made from the hall element to IC as shorter as possible, because it is easy for the noise to influence the hall lines.

Description of operations

1) Lock protection and automatic restart

CR timer system

Charging and discharging time at LD terminal depends on the capacitor equipped externally on LD terminal. Charging and discharging time is determined as follows:

TON(charging time) =		C × (VLDCL-VLDCP)		
		ILDC		
TOFF(discharging time)=		C × (VLDCL-VLDCP)		
		ILDD		
С	: Capacit	ty of capacitor equipped externa	lly on LD terminal	
V LDCL	: LD term	LD terminal clamping voltage		
V LDCP	: LD term	LD terminal comparator voltage		
I LDC	: LD term	rminal charging current		
I LDD	: LD term	minal discharging current		

Timing chart of LD terminal is shown in Fig.13.

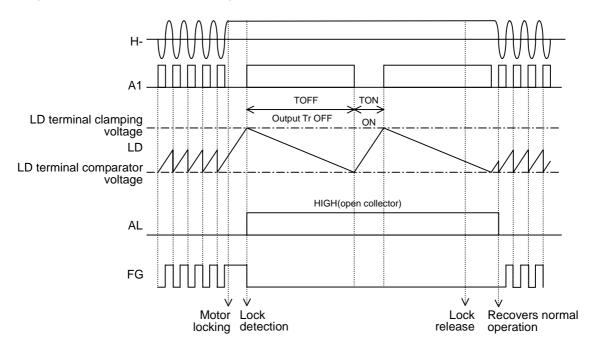


Fig.13 Lock protection (CR timer system) timing chart

2) PWM terminal

The signal input to PWM terminal is below L (0.8V or less), output (A1 and A2) turns off. And when it is above H (2.0V or more), output turns on. PWM terminal is pulled up by resistor (30k :typ.) inside IC. When it is open, the output is in operating mode.

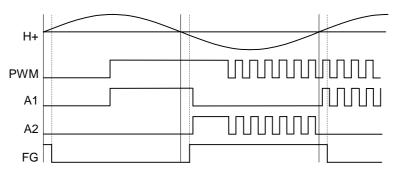


Fig.14 Timing chart in PWM control

3) Charging and discharging pulse circuit compatible with temperature variable speed control

When an external capacitor and resistor are connected to CR terminal, saw wave is generated by charging and discharging of capacitor corresponding to the cycle of hall signal. Saw wave of CR terminal changes with the external capacitor and resistor. Waveform of CR terminal is output to TOUT by buffer amplifier.
CR terminal is variable from VCRCP (0.35V:typ., see the electric characteristics) to Vcc. When CR voltage is above Vcc-VTOH (1V:typ., see the electric characteristics), CR terminal signal is not output to TOUT terminal as shown in Fig.15.

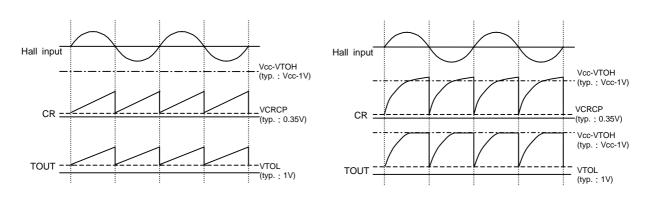


Fig.15 CR terminal and TOUT terminal timing chart

4) Variable speed control application

This is an example of the application which makes the fan motor rotating speed variable corresponding to ambient temperature with thermistor by use of charging and discharging pulse circuit and PWM input.

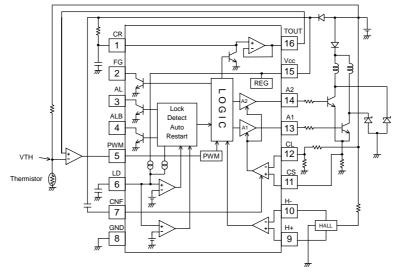


Fig. 16 Example of temperature variable speed application

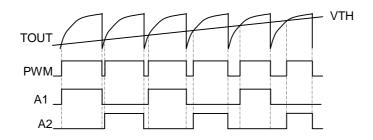


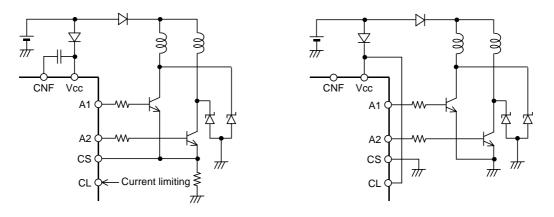
Fig. 17 Temperature variable speed timing chart

When the temperature becomes the lower and the thermistor terminal voltage the higher, PWM pulse becomes the shorter and speed is reduced as shown in Fig. 17.

5) Current limiting circuit < BA6901F >

Output current limitation can be set by the voltage (VCL) input to CL terminal. Connect a resistor (RNF) for detecting output current between the emitter of external output transistor and GND, and input the voltage generated by resistor to CS terminal, thereby detecting the output current. The output current is limited so that CL terminal and CS terminal has the same potential. There is an offset between CL terminal and CS terminal. Current limiting value can be calculated by the formula below:

When limiting the output current, capacitor for phase compensation must be connected between CNF terminal and Vcc terminal. When the output current is not to be limited, fix CL terminal voltage to High level (Vcc) and CS terminal to Low level (GND).



(a) When current limiting is applied

(b) When current limiting is not applied

Fig.18 External circuit of output

Power dissipation

Power dissipation (total loss) indicates the power that can be consumed by IC at $Ta = 25^{\circ}C$ (normal temperature). IC is heated when it consumes power, and the temperature of IC chip becomes higher than ambient temperature. The temperature that can be accepted by IC chip depends on circuit configuration, manufacturing process, etc, and consumable power is limited. Power dissipation is determined by the temperature allowed in IC chip (maximum junction temperature) and thermal resistance of package (heat dissipation capability). The maximum junction temperature is in general equal to the maximum value in the storage temperature range.

Heat generated by consumed power of IC is radiated from the mold resin or lead frame of package. The parameter which indicates this heat dissipation capability (hardness of heat release) is called heat resistance, represented by the symbol

ja [/W]. The temperature of IC inside the package can be estimated by this heat resistance. Fig.19 shows the model of heat resistance of the package.

Heat resistance θ ja, ambient temperature Ta, junction temperature Tj, and power consumption P can be calculated by the equation below:

 $\theta ja = (Tj - Ta) / P [^{\circ}C/W]$

Thermal derating curve indicates power that can be consumed by IC with reference to ambient temperature. Power that can be consumed by IC begins to attenuate at certain ambient temperature. This gradient is determined by thermal resistance θ_{ja} .

Thermal resistance θja depends on chip size, power consumption, package ambient temperature, packaging condition, wind velocity, etc., even when the same package is used. Thermal derating curve indicates a reference value measured at a specified condition. Fig.20 shows a thermal derating curve (Value when mounting FR4 glass epoxy board 70 [mm] x 70 [mm] x 1.6 [mm] (copper foil area below 3 [%])) Pd(mW)

800

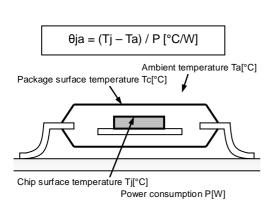
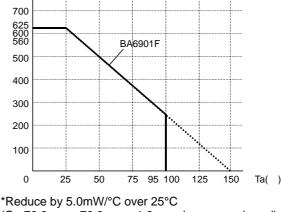
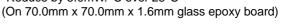
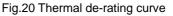


Fig.19 Thermal resistance



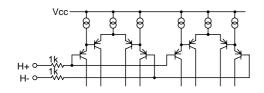




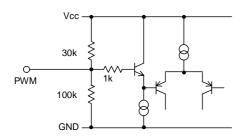
I/O equivalence circuit(Resistance values are typical)

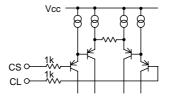
1) Hall input terminal

2) Current limiting input terminal Output current detecting terminal Charge-discharge pulse output terminal

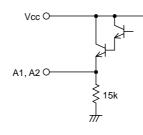


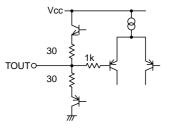
4) PWM input terminal



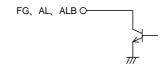


5) Output terminal





6) Signal output terminal



Safety measure

1) Reverse connection protection diode

Reverse connection of power results in IC destruction as shown in Fig 21. When reverse connection is possible, reverse connection protection diode must be added between power supply and Vcc.

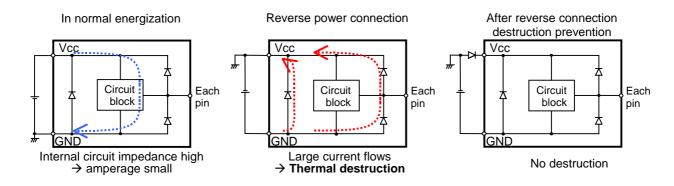


Fig.21 Current flow when power is connected reversely

2) About measures of voltage rise by back electromotive force

The voltage of output terminal rises by back electromotive force. The diode D1 of Fig.22 is necessary to divide a power supply line of motor with small signal line, so that the voltage of the output does not affect a power supply line.

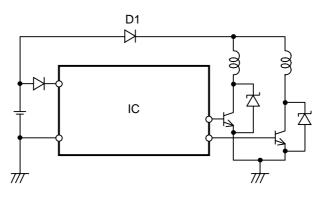


Fig.22 Separation of a power supply line

3) FG/AL output

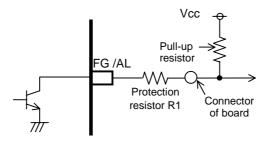


Fig.23 Protection of FG and AL terminal

FG and AL output is an open collector and requires pull-up resistor.

The IC can be protected by adding resistor R1. An excess of absolute maximum rating, when FG or AL output terminal is directly connected to power supply, could damage the IC.

4) Problem of GND line PWM switching

Do not perform PWM switching of GND line because GND terminal potential cannot be kept to a minimum.

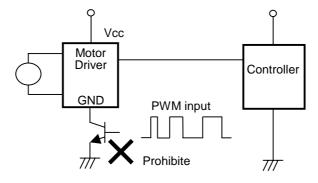


Fig.24 GND Line PWM switching prohibited

Operational Notes

1) Absolute maximum ratings

An excess in the absolute maximum rations, such as supply voltage, temperature range of operating conditions, etc., can break down the devices, thus making impossible to identify breaking mode, such as a short circuit or an open circuit. If any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.

- Connecting the power supply connector backward Connecting of the power supply in reverse polarity can damage IC. Take precautions when connecting the power supply lines. An external direction diode can be added.
- 3) Power supply line

Back electromotive force causes regenerated current to power supply line, therefore take a measure such as placing a capacitor between power supply and GND for routing regenerated current. And fully ensure that the capacitor characteristics have no problem before determine a capacitor value. (when applying electrolytic capacitors, capacitance characteristic values are reduced at low temperatures)

4) GND potential

The potential of GND pin must be minimum potential in all operating conditions. Also ensure that all terminals except GND terminal do not fall below GND voltage including transient characteristics. However, it is possible that the motor output terminal may deflect below GND because of influence by back electromotive force of motor. Malfunction may possibly occur depending on use condition, environment, and property of individual motor. Please make fully confirmation that no problem is found on operation of IC.

- Thermal design Use a thermal design that allows for a sufficient margin in light of the power dissipation(Pd) in actual operating conditions.
- 6) Inter-pin shorts and mounting errors Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.
- Actions in strong electromagnetic field Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.
- 8) ASO

When using the IC, set the output transistor so that it does not exceed absolute maximum rations or ASO. 9) Thermal shut down circuit

The IC incorporates a built-in thermal shutdown circuit (TSD circuit). Operation temperature is 175 (typ.) and has a hysteresis width of 25 (typ.). When IC chip temperature rises and TSD circuit works, the output terminal becomes an open state. TSD circuit is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operation this circuit or use the IC in an environment where the operation of this circuit is assumed.

10) Testing on application boards

When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.

11) GND wiring pattern

When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.

- 12) Capacitor between output and GND When a large capacitor is connected between output and GND, if Vcc is shorted with 0V or GND for some cause, it is possible that the current charged in the capacitor may flow into the output resulting in destruction. Keep the capacitor between output and GND below 100uF.
- 13) IC terminal input

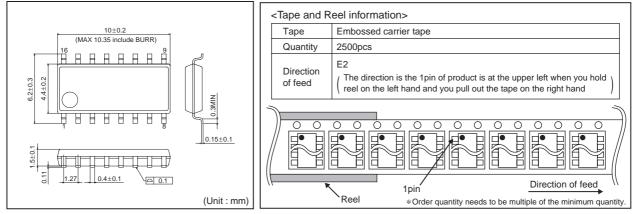
When Vcc voltage is not applied to IC, do not apply voltage to each input terminal. When voltage above Vcc or below GND is applied to the input terminal, parasitic element is actuated due to the structure of IC. Operation of parasitic element causes mutual interference between circuits, resulting in malfunction as well as destruction in the last. Do not use in a manner where parasitic element is actuated.

14) In use

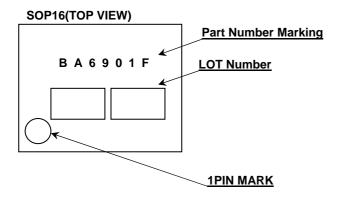
We are sure that the example of application circuit is preferable, but please check the character further more in application to a part which requires high precision. In using the unit with external circuit constant changed, consider the variation of externally equipped parts and our IC including not only static character but also transient character and allow sufficient margin in determining.

Physical dimension tape and reel information

SOP16



Marking diagram



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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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ООО "ЛайфЭлектроникс"

ИНН 7805602321 КПП 780501001 Р/С 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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