# Low-power configurable multiple function gate Rev. 8 — 23 September 2015 P

**Product data sheet** 

#### **General description** 1.

The 74AUP1G98 provides configurable multiple functions. The output state is determined by eight patterns of 3-bit input. The user can choose the logic functions MUX, AND, OR, NAND, NOR, inverter and buffer. All inputs can be connected to  $\ensuremath{V_{\text{CC}}}$  or GND.

This device ensures a very low static and dynamic power consumption across the entire V<sub>CC</sub> range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using I<sub>OFF</sub>.

The I<sub>OFF</sub> circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

The 74AUP1G98 has Schmitt trigger inputs making it capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

The inputs switch at different points for positive and negative-going signals. The difference between the positive voltage  $V_{T+}$  and the negative voltage  $V_{T-}$  is defined as the input hysteresis voltage V<sub>H</sub>.

#### **Features and benefits** 2.

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- ESD protection:
  - ◆ HBM JESD22-A114F exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption;  $I_{CC} = 0.9 \mu A$  (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



### Low-power configurable multiple function gate

### 3. Ordering information

Table 1. Ordering information

Type number	Package						
	Temperature range	Name	Description	Version			
74AUP1G98GW	–40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363			
74AUP1G98GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1.45 $\times$ 0.5 mm	SOT886			
74AUP1G98GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1 $\times$ 0.5 mm	SOT891			
74AUP1G98GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115			
74AUP1G98GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202			
74AUP1G98GX	–40 °C to +125 °C	X2SON6	plastic thermal extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 0.8 $\times$ 0.35 mm	SOT1255			

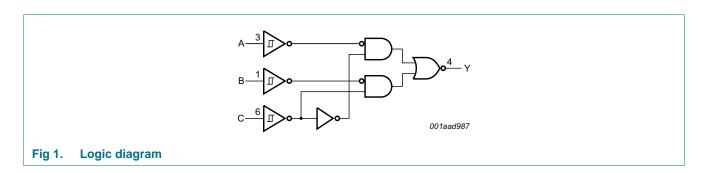
### 4. Marking

#### Table 2. Marking

Type number	Marking code <sup>[1]</sup>
74AUP1G98GW	a9
74AUP1G98GM	a9
74AUP1G98GF	a9
74AUP1G98GN	a9
74AUP1G98GS	a9
74AUP1G98GX	a9

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

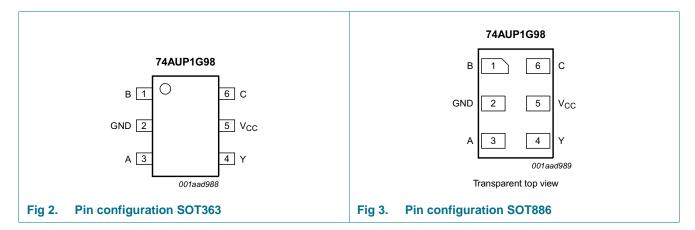
### 5. Functional diagram



Low-power configurable multiple function gate

### 6. Pinning information

#### 6.1 Pinning





### 6.2 Pin description

Table 3. Pin description

Symbol	Pin	Description
В	1	data input
GND	2	ground (0 V)
A	3	data input
Υ	4	data output
Vcc	5	supply voltage
С	6	data input

#### Low-power configurable multiple function gate

### 7. Functional description

Table 4. Function table[1]

Input			Output
С	В	A	Υ
L	L	L	Н
L	L	Н	Н
L	Н	L	L
L	Н	Н	L
Н	L	L	Н
Н	L	Н	L
Н	Н	L	Н
Н	Н	Н	L

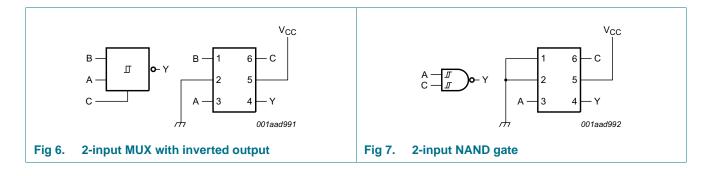
<sup>[1]</sup> H = HIGH voltage level;

L = LOW voltage level.

### 7.1 Logic configurations

Table 5. Function selection table

Logic function	Figure
2-input MUX with inverted output	see Figure 6
2-input NAND	see Figure 7
2-input NOR with one input inverted	see Figure 8
2-input AND with one input inverted	see Figure 8
2-input NAND with one input inverted	see Figure 9
2-input OR with one input inverted	see Figure 9
2-input NOR	see Figure 10
Buffer	see Figure 11
Inverter	see Figure 12



#### Low-power configurable multiple function gate

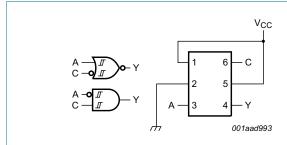


Fig 8. 2-input AND gate with input A inverted or 2-input NOR gate with inverted C input

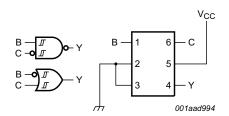
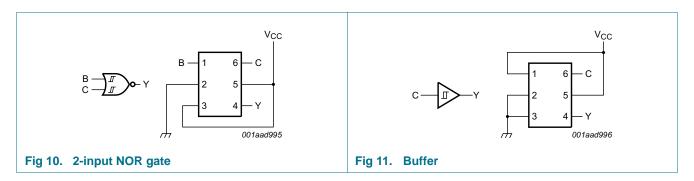
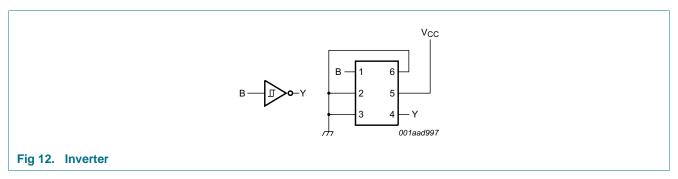


Fig 9. 2-input OR gate with input B inverted or 2-input NAND gate with input C inverted





### 8. Limiting values

#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down 11 mode	-0.5	+4.6	V
Io	output current	$V_O = 0 \text{ V to } V_{CC}$	-	±20	mA
I <sub>CC</sub>	supply current		-	50	mA

#### Low-power configurable multiple function gate

Table 6. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$	-	250	mW

<sup>[1]</sup> The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

### 9. Recommended operating conditions

Table 7. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C

#### 10. Static characteristics

Table 8. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
<b>T</b> <sub>amb</sub> = 2	5 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O} = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.1	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	0.75V <sub>CC</sub>	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	2.05	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6		V	
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_O = 20 \mu A$ ; $V_{CC} = 0.8 \text{ V}$ to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-		V	
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-		V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-		V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.31	V
		$I_{O} = -20 \ \mu \text{A}; \ V_{CC} = 0.8 \ \text{V to } 3.6 \ \text{V} \qquad V_{CC} - 0.1 \qquad - \qquad $	0.44	V		
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V

74AUP1G98

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<sup>[2]</sup> For SC-88 packages: above 87.5 °C the value of P<sub>tot</sub> derates linearly with 4.0 mW/K.
For X2SON6 and XSON6 packages: above 118 °C the value of P<sub>tot</sub> derates linearly with 7.8 mW/K.

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 Table 8.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
l <sub>l</sub>	input leakage current	$V_{I} = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μΑ
$\Delta I_{CC}$	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ [1] $V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
Cı	input capacitance	$V_{CC} = 0 \text{ V to } 3.6 \text{ V; } V_I = \text{GND or } V_{CC}$	-	1.1	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF
T <sub>amb</sub> = -	40 °C to +85 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O} = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7V <sub>CC</sub>	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.03	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.30	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.97	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O} = 20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
III IOFF ΔIOFF ICC  ΔICC  T <sub>amb</sub> = -4  VOL  III IOFF ΔΙΟΓΓ		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
l <sub>l</sub>	input leakage current	$V_I$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	V <sub>1</sub> or V <sub>O</sub> = 0 V to 3.6 V; V <sub>CC</sub> = 0 V to 0.2 V	-	-	±0.6	μΑ
I <sub>CC</sub>	supply current	V <sub>I</sub> = GND or V <sub>CC</sub> ; I <sub>O</sub> = 0 A; V <sub>CC</sub> = 0.8 V to 3.6 V	-	-	0.9	μΑ
$\Delta I_{CC}$	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	50	μΑ

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**Table 8.** Static characteristics ...continued
At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +125 °C					_
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_O = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.11	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	0.6V <sub>CC</sub>	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.17	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.77	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.67	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O} = 20 \mu A; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	1.67	V		
I <sub>I</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	2.30 0.11 0.33V <sub>CC</sub> 0.41 0.39 0.36 0.50 0.36 0.50 10.50 10.50 10.75			
I <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.75	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1.4	μΑ
Δl <sub>CC</sub>	additional supply current	$V_1 = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

<sup>[1]</sup> One input at  $V_{CC}$  – 0.6 V, other input at  $V_{CC}$  or GND.

### Low-power configurable multiple function gate

## 11. Dynamic characteristics

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 14.

Symbol	Parameter	Conditions	T <sub>amb</sub> = 25 °C			$T_{amb} = -40$ °C to +125 °C			Unit
			Min	Typ[1]	Max	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 5 pl	F			•			·		
t <sub>pd</sub>	propagation delay	A, B, C to Y; see Figure 13							
		V <sub>CC</sub> = 0.8 V	-	23.3	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.9	6.7	12.9	2.7	13.2	13.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.4	4.8	7.7	2.4	8.3	8.7	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.2	4.0	6.3	1.9	7.0	7.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.0	3.2	4.6	1.8	5.2	5.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.9	2.9	4.0	1.6	4.2	4.4	ns
C <sub>L</sub> = 10	ρF			1				1	-1
t <sub>pd</sub>	propagation delay	A, B, C to Y; see Figure 13							
		V <sub>CC</sub> = 0.8 V	-	27.1	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.3	7.6	14.5	3.0	15.1	15.3	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.7	5.4	8.8	2.8	9.5	9.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.5	4.6	7.2	2.3	8.0	8.4	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.4	3.8	5.3	2.2	5.9	6.2	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.3	3.5	4.7	2.0	4.9	5.2	ns
C <sub>L</sub> = 15	ρF								
t <sub>pd</sub>	propagation delay	A, B, C to Y; see Figure 13							
		V <sub>CC</sub> = 0.8 V	-	30.6	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.6	8.4	16.1	3.3	16.9	17.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	6.0	9.7	3.1	10.5	11.0	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.8	5.1	7.9	2.5	8.9	9.3	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	2.7	4.2	5.9	2.5	6.6	7.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.5	3.9	5.2	2.2	5.5	5.8	ns

#### Low-power configurable multiple function gate

 Table 9.
 Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 14.

Symbol	Parameter	Conditions	T <sub>amb</sub> = 25 °C			T <sub>amb</sub> =	–40 °C to	+125 °C	Unit
			Min	Typ[1]	Max	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 30 p	ρF				'				
t <sub>pd</sub>	propagation delay	A, B, C to Y; see Figure 13							
		V <sub>CC</sub> = 0.8 V	-	38.7	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.5	10.7	21.1	4.1	22.0	22.4	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.8	7.6	12.3	3.8	13.5	14.2	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	3.5	6.3	10.1	3.1	11.3	11.9	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	3.4	5.3	7.5	3.2	8.4	8.9	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	3.2	5.0	6.7	2.9	7.1	7.5	ns
C <sub>L</sub> = 5 pl	F, 10 pF, 15 pF and	30 pF			'				-
C <sub>PD</sub>	power dissipation	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]							
	capacitance	V <sub>CC</sub> = 0.8 V	-	2.7	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.9	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	3.0	-	-	-	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.2	-	-	-	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V		3.8	-	-	-	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.4	-	-	-	-	pF

- [1] All typical values are measured at nominal V<sub>CC</sub>.
- [2]  $\;\;t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$
- [3]  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ).

 $P_D = C_{PD} \times V_{CC}{}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}{}^2 \times f_o) \text{ where:}$ 

 $f_i$  = input frequency in MHz;

f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

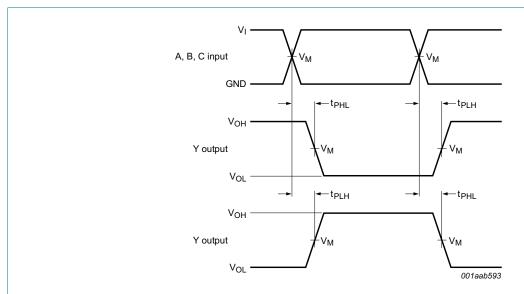
V<sub>CC</sub> = supply voltage in V;

N = number of inputs switching;

 $\Sigma (C_L \times V_{CC}{}^2 \times f_o)$  = sum of the outputs.

#### Low-power configurable multiple function gate

### 12. Waveforms



Measurement points are given in Table 10.

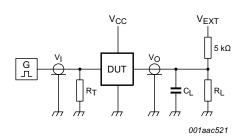
 $V_{\text{OL}}$  and  $V_{\text{OH}}$  are typical output voltage levels that occur with the output load.

Fig 13. Input A, B and C to output Y propagation delay times.

Table 10. Measurement points

Supply voltage	Output	Input		
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>	V <sub>I</sub>	$t_r = t_f$
0.8 V to 3.6 V	0.5V <sub>CC</sub>	0.5V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns

#### Low-power configurable multiple function gate



Test data is given in Table 11.

Definitions for test circuit:

R<sub>L</sub> = Load resistance.

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_o$  of the pulse generator.

V<sub>EXT</sub> = External voltage for measuring switching times.

Fig 14. Test circuit for measuring switching times

Table 11. Test data

Supply voltage	Load		V <sub>EXT</sub>			
V <sub>CC</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>	
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	$5 k\Omega$ or $1 M\Omega$	open	GND	2V <sub>CC</sub>	

<sup>[1]</sup> For measuring enable and disable times  $R_L = 5 \text{ k}\Omega$ , for measuring propagation delays, setup and hold times and pulse width  $R_L = 1 \text{ M}\Omega$ .

#### 13. Transfer characteristics

Table 12. Transfer characteristics

Voltages are referenced to GND (ground = 0 V; for test circuit see Figure 14.

Symbol	Parameter	Conditions	Tar	T <sub>amb</sub> = 25 °C			$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$		
			Min	Тур	Max	Min	Max (85 °C)	Max (125 °C)	
$V_{T+}$	positive-going	see Figure 15 and Figure 16							
	threshold voltage	V <sub>CC</sub> = 0.8 V	0.30	-	0.60	0.30	0.60	0.62	V
		V <sub>CC</sub> = 1.1 V	0.53	-	0.90	0.53	0.90	0.92	V
		V <sub>CC</sub> = 1.4 V	0.74	-	1.11	0.74	1.11	1.13	V
		V <sub>CC</sub> = 1.65 V	0.91	-	1.29	0.91	1.29	1.31	V
		V <sub>CC</sub> = 2.3 V	1.37	-	1.77	1.37	1.77	1.80	V
		V <sub>CC</sub> = 3.0 V	1.88	-	2.29	1.88	2.29	2.32	V
$V_{T-}$	negative-going threshold voltage	see Figure 15 and Figure 16							
		V <sub>CC</sub> = 0.8 V	0.10	-	0.60	0.10	0.60	0.60	V
		V <sub>CC</sub> = 1.1 V	0.26	-	0.65	0.26	0.65	0.65	V
		V <sub>CC</sub> = 1.4 V	0.39	-	0.75	0.39	0.75	0.75	V
		V <sub>CC</sub> = 1.65 V	0.47	-	0.84	0.47	0.84	0.84	V
		V <sub>CC</sub> = 2.3 V	0.69	-	1.04	0.69	1.04	1.04	V
		V <sub>CC</sub> = 3.0 V	0.88	-	1.24	0.88	1.24	1.24	V

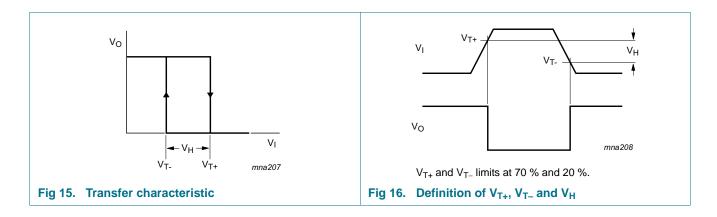
#### Low-power configurable multiple function gate

Table 12. Transfer characteristics ... continued

Voltages are referenced to GND (ground = 0 V; for test circuit see Figure 14.

Symbol	Parameter	Conditions	Tai	T <sub>amb</sub> = 25 °C			$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$			
			Min	Тур	Max	Min	Max (85 °C)	Max (125 °C)		
V <sub>H</sub>	hysteresis voltage	$(V_{T+} - V_{T-})$ ; see Figure 15, Figure 16, Figure 17 and Figure 18								
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	0.07	0.50	0.50	V	
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	0.08	0.46	0.46	V	
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	0.18	0.56	0.56	V	
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	0.27	0.66	0.66	V	
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	0.53	0.92	0.92	V	
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	0.79	1.31	1.31	V	

### 14. Waveforms transfer characteristics



### Low-power configurable multiple function gate

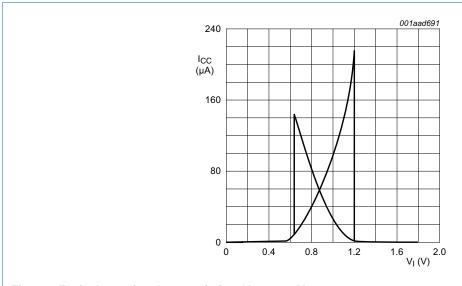


Fig 17. Typical transfer characteristics;  $V_{CC} = 1.8 \text{ V}$ 

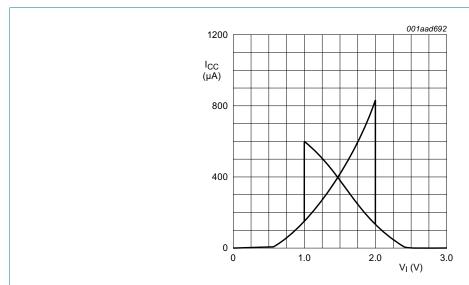


Fig 18. Typical transfer characteristics;  $V_{CC} = 3.0 \text{ V}$ 

### 15. Package outline

#### **SOT363** Plastic surface-mounted package; 6 leads Α X = v (M) A ΗE ⊕ w M B е detail X scale **DIMENSIONS** (mm are the original dimensions) Α1 UNIT D Q Α С Ε ٧ e<sub>1</sub> $H_{\mathsf{E}}$ $L_{\mathbf{p}}$ w у max 0.25 0.30 0.25 0.10 1.35 1.15 2.2 2.0 0.45 1.1 2.2 0.1 0.8 0.20 1.8 0.15 0.15 REFERENCES EUROPEAN OUTLINE ISSUE DATE VERSION JEDEC **PROJECTION** IEC JEITA 04-11-08 SOT363 SC-88 $\bigcirc$ 06-03-16

Fig 19. Package outline SOT363 (SC-88)

74AUP1G98

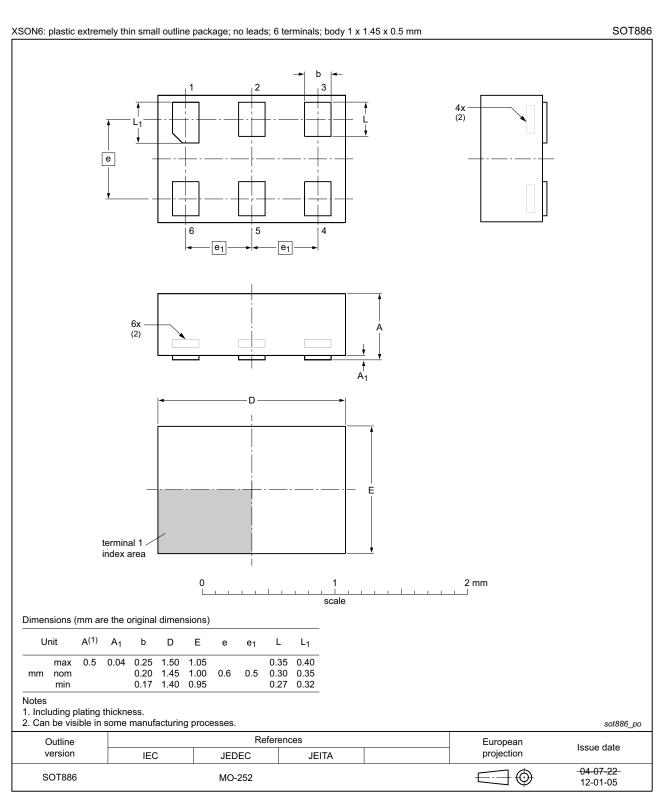


Fig 20. Package outline SOT886 (XSON6)

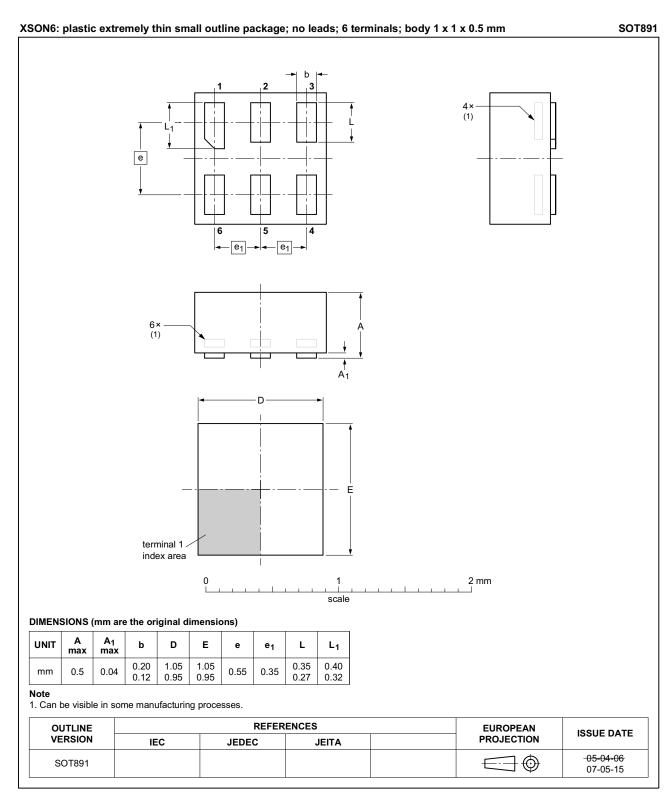


Fig 21. Package outline SOT891 (XSON6)

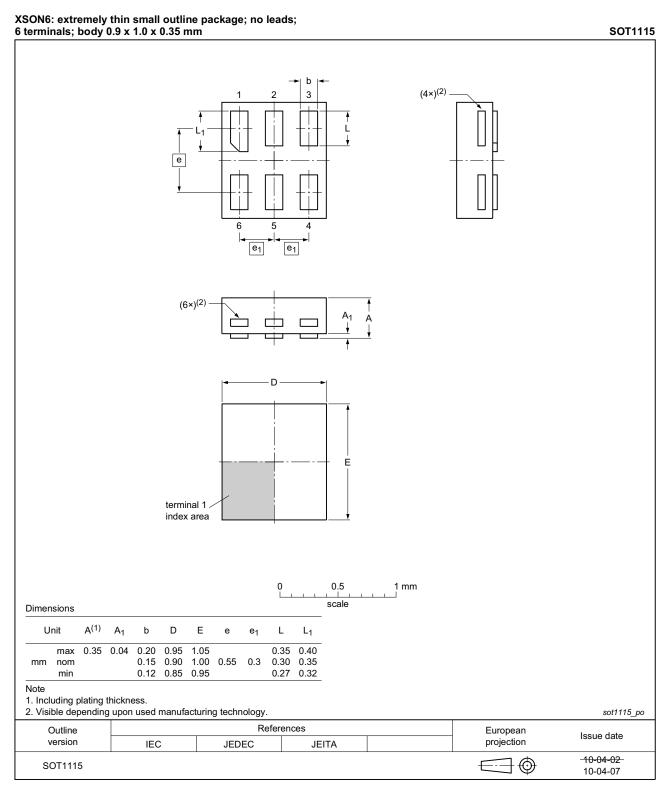


Fig 22. Package outline SOT1115 (XSON6)

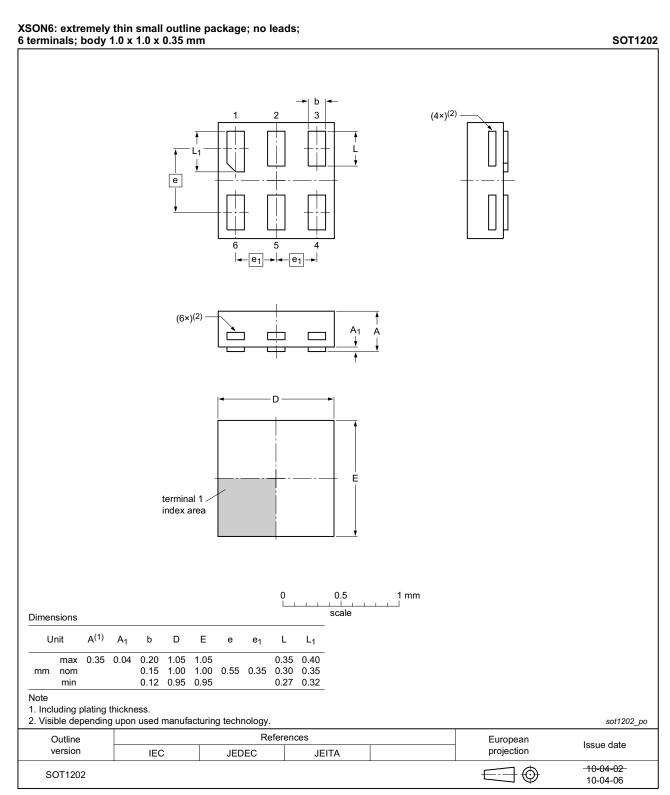


Fig 23. Package outline SOT1202 (XSON6)

**Product data sheet** 

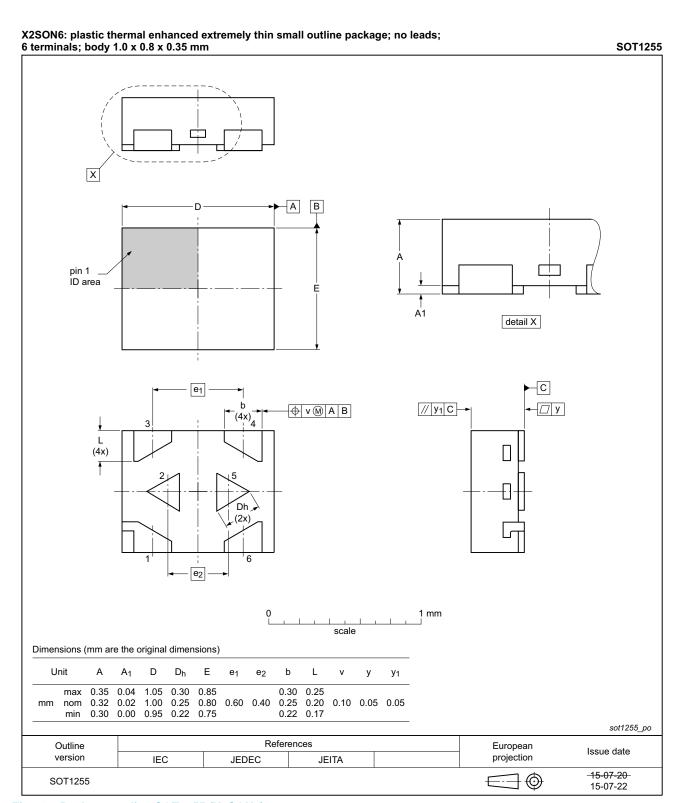


Fig 24. Package outline SOT1255 (X2SON6)

### Low-power configurable multiple function gate

### 16. Abbreviations

#### Table 13. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

### 17. Revision history

#### Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G98 v.8	20150923	Product data sheet	-	74AUP1G98 v.7
Modifications:	Added type nu	ımber 74AUP1G98GX (SOT1	255/X2SON6).	
74AUP1G98 v.7	20120815	Product data sheet	-	74AUP1G98 v.6
Modifications:	Package outling	ne drawing of SOT886 (Figure	20) modified.	
74AUP1G98 v.6	20111128	Product data sheet	-	74AUP1G98 v.5
74AUP1G98 v.5	20110105	Product data sheet	-	74AUP1G98 v.4
74AUP1G98 v.4	20101012	Product data sheet	-	74AUP1G98 v.3
74AUP1G98 v.3	20090629	Product data sheet	-	74AUP1G98 v.2
74AUP1G98 v.2	20090402	Product data sheet	-	74AUP1G98 v.1
74AUP1G98 v.1	20061108	Product data sheet	-	-

#### Low-power configurable multiple function gate

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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#### Low-power configurable multiple function gate

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### **Nexperia**

Low-power configurable multiple function gate

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