

DDTC (LO-R1) U

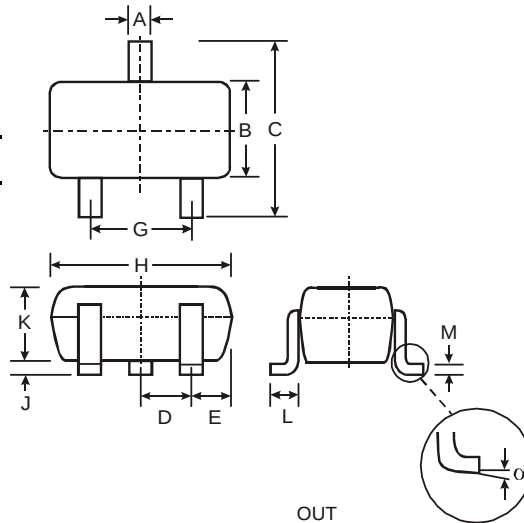
NPN PRE-BIASED 100 mA SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistors
- Lead Free/RoHS Compliant (Note 2)
- "Green" Device (Note 3 & 4)

Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Marking Information: See Table Below & Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)



SOT-323		
Dim	Min	Max
A	0.25	0.40
B	1.15	1.35
C	2.00	2.20
D	0.65 Nominal	
E	0.30	0.40
G	1.20	1.40
H	1.80	2.20
J	0.0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.18
α	0°	8°
All Dimensions in mm		

P/N	R1 (NOM)	R2 (NOM)	Type Code
DDTC122LU	0.22K Ω	10K Ω	N81
DDTC142JU	0.47K Ω	10K Ω	N82
DDTC122TU	0.22K Ω	OPEN	N83
DDTC142TU	0.47K Ω	OPEN	N84

Schematic and Pin Configuration

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (3) to (2)	V _{CC}	50	V
Input Voltage, (1) to (2)	V _{IN}	-5 to +6	V
Input Voltage, (2) to (1)	V _{EBO} (MAX)	5	V
Output Current	I _C	100	mA
Power Dissipation (Note 1)	P _d	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

- Notes:
1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.
 2. No purposefully added lead.
 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 4. Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

Electrical Characteristics

@T_A = 25°C unless otherwise specified

R1, R2 Types

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Input Voltage	DDTC122LU DDTC142JU	V _{I(off)}	0.3 0.3	—	—	V	V _{CC} = 5V, I _O = 100μA
	DDTC122LU DDTC142JU	V _{I(on)}	—	—	2.0 2.0	V	V _O = 0.3V, I _O = 20mA V _O = 0.3V, I _O = 20mA
Output Voltage		V _{O(on)}	—	—	0.3V	V	I _O /I _I = 5mA/0.25mA
Input Current	DDTC122LU DDTC142JU	I _I	—	—	28 13	mA	V _I = 5V
Output Current		I _{O(off)}	—	—	0.5	μA	V _{CC} = 50V, V _I = 0V
DC Current Gain	DDTC122LU DDTC142JU	G _I	56 56	—	—	—	V _O = 5V, I _O = 10mA
Gain-Bandwidth Product*		f _T	—	200	—	MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz

* Transistor - For Reference Only

Electrical Characteristics

@T_A = 25°C unless otherwise specified

R1-Only

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		BV _{CBO}	50	—	—	V	I _C = 50μA
Collector-Emitter Breakdown Voltage		BV _{CEO}	40	—	—	V	I _C = 1mA
Emitter-Base Breakdown Voltage	DDTC122TU DDTC142TU	BV _{EBO}	5	—	—	V	I _E = 50μA I _E = 50μA
Collector Cutoff Current		I _{CBO}	—	—	0.5	μA	V _{CB} = 50V
Emitter Cutoff Current	DDTC122TU DDTC142TU	I _{EBO}	— —	—	0.5 0.5	μA	V _{EB} = 4V
Collector-Emitter Saturation Voltage		V _{CE(sat)}	—	—	0.3	V	I _C = 5mA, I _B = 0.25mA
DC Current Transfer Ratio	DDTC122TU DDTC142TU	h _{FE}	100 100	250 250	600 600	—	I _C = 1mA, V _{CE} = 5V
Gain-Bandwidth Product*		f _T	—	200	—	MHz	V _{CE} = 10V, I _E = -5mA, f = 100MHz

* Transistor - For Reference Only

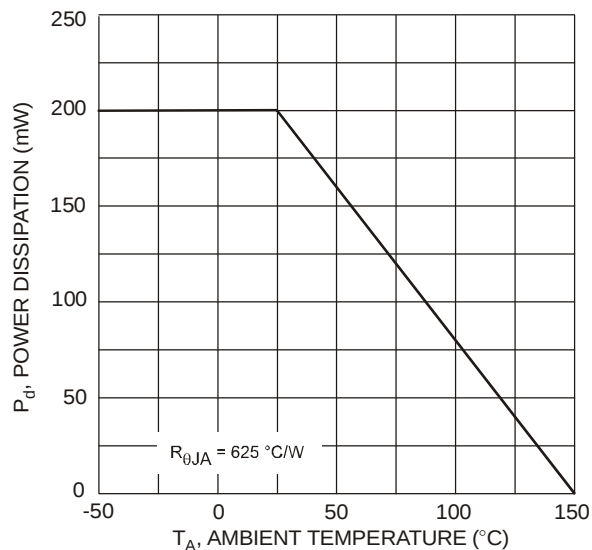


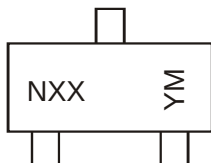
Fig. 1 Power Derating Curve

Ordering Information (Note 4 & 5)

Device	Packaging	Shipping
DDTC122LU-7-F	SOT-323	3000/Tape & Reel
DDTC142JU-7-F	SOT-323	3000/Tape & Reel
DDTC122TU-7-F	SOT-323	3000/Tape & Reel
DDTC142TU-7-F	SOT-323	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



NXX = Product Type Marking Code, See Table on Page 1

YM = Date Code Marking

Y = Year ex: T = 2006

M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	N	P	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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