



# PBSS4240Z

40 V, 2 A NPN low  $V_{CEsat}$  (BISS) transistor

16 October 2014

Product data sheet

## 1. General description

NPN low  $V_{CEsat}$  Breakthrough In Small Signal (BISS) transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5240Z

## 2. Features and benefits

- Low collector-emitter saturation voltage  $V_{CEsat}$
- High collector current capability  $I_C$  and  $I_{CM}$
- High energy efficiency due to less heat generation
- AEC-Q101 qualified

## 3. Applications

- DC-to-DC conversion
- Supply line switching
- Battery charger
- LCD backlighting
- Driver in low supply voltage applications (e.g. lamps and LEDs)
- Inductive load driver (e.g. relays, buzzers and motors)

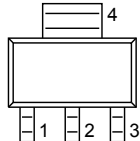
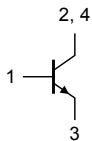
## 4. Quick reference data

Table 1. Quick reference data

| Symbol      | Parameter                               | Conditions   | Min | Typ | Max | Unit       |
|-------------|---|--|-----|-----|-----|------------|
| $V_{CEO}$   | collector-emitter voltage               | open base  | -   | -   | 40  | V          |
| $I_C$       | collector current                       |  | -   | -   | 2   | A          |
| $I_{CM}$    | peak collector current                  | $t_p \leq 1$ ms; single pulse  | -   | -   | 3   | A          |
| $R_{CEsat}$ | collector-emitter saturation resistance | $I_C = 1$ A; $I_B = 100$ mA; pulsed;<br>$t_p \leq 300$ $\mu$ s; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C | -   | -   | 275 | m $\Omega$ |

## 5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline  | Graphic symbol  |
|-----|--------|-------------|---|---|
| 1   | B      | base        |  <p>SC-73 (SOT223)</p> |  <p>sym016</p> |
| 2   | C      | collector   |   |   |
| 3   | E      | emitter     |   |   |
| 4   | C      | collector   |   |   |

## 6. Ordering information

Table 3. Ordering information

| Type number | Package |  |         |
|-------------|---------|--|---------|
|             | Name    | Description  | Version |
| PBSS4240Z   | SC-73   | plastic surface-mounted package with increased heatsink; 4 leads | SOT223  |

## 7. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PBSS4240Z   | S4240Z       |

## 8. Limiting values

**Table 5. Limiting values**

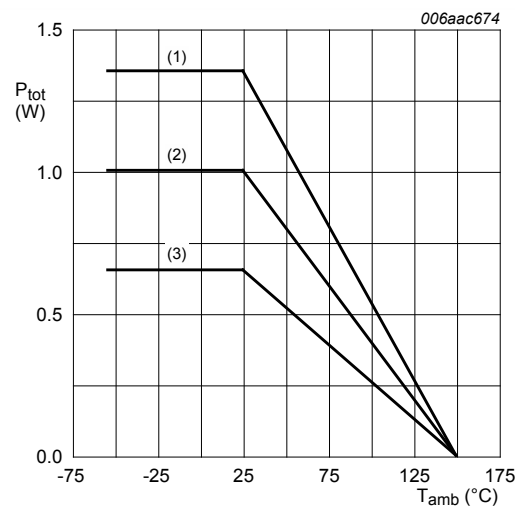
In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol    | Parameter                 | Conditions                    |     | Min | Max  | Unit |
|-----------|---------------------------|-------------------------------|-----|-----|------|------|
| $V_{CBO}$ | collector-base voltage    | open emitter                  |     | -   | 40   | V    |
| $V_{CEO}$ | collector-emitter voltage | open base                     |     | -   | 40   | V    |
| $V_{EBO}$ | emitter-base voltage      | open collector                |     | -   | 7    | V    |
| $I_C$     | collector current         |                               |     | -   | 2    | A    |
| $I_{CM}$  | peak collector current    | $t_p \leq 1$ ms; single pulse |     | -   | 3    | A    |
| $I_B$     | base current              |                               |     | -   | 300  | mA   |
| $I_{BM}$  | peak base current         | $t_p \leq 1$ ms; single pulse |     | -   | 1    | A    |
| $P_{tot}$ | total power dissipation   | $T_{amb} \leq 25$ °C          | [1] | -   | 0.65 | W    |
|           |                           |                               | [2] | -   | 1    | W    |
|           |                           |                               | [3] | -   | 1.35 | W    |
| $T_j$     | junction temperature      |                               |     | -   | 150  | °C   |
| $T_{amb}$ | ambient temperature       |                               |     | -55 | 150  | °C   |
| $T_{stg}$ | storage temperature       |                               |     | -65 | 150  | °C   |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



(1) FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>

(2) FR4 PCB, mounting pad for collector 1 cm<sup>2</sup>

(3) FR4 PCB, standard footprint

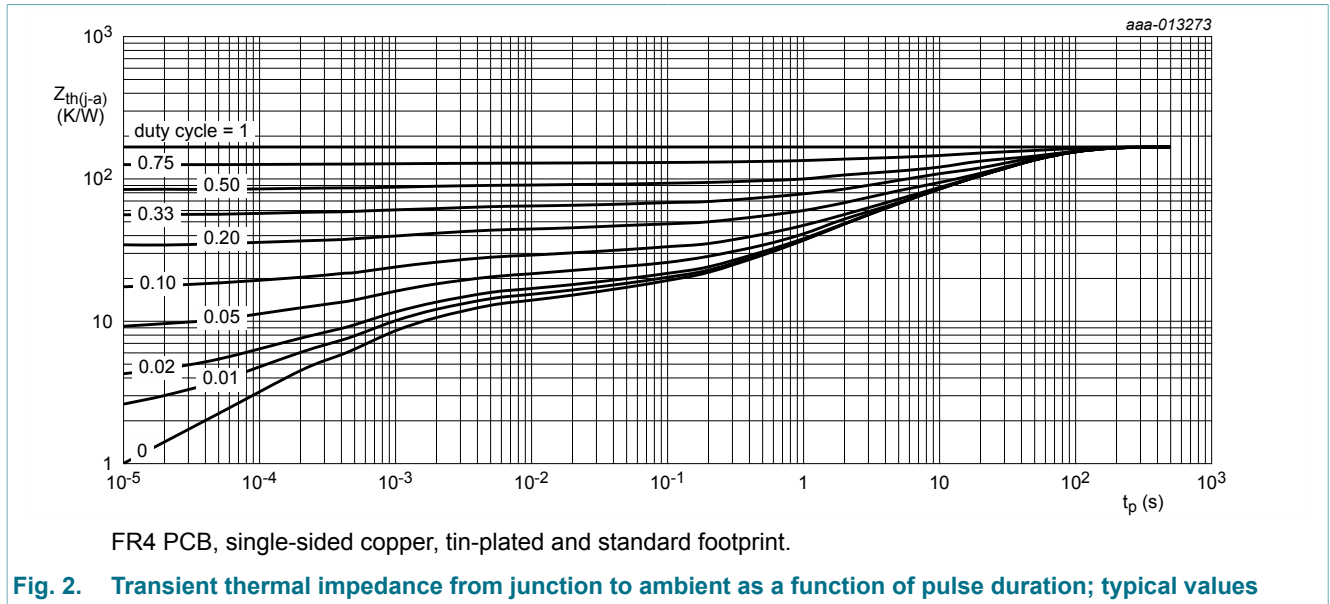
**Fig. 1. Power derating curves**

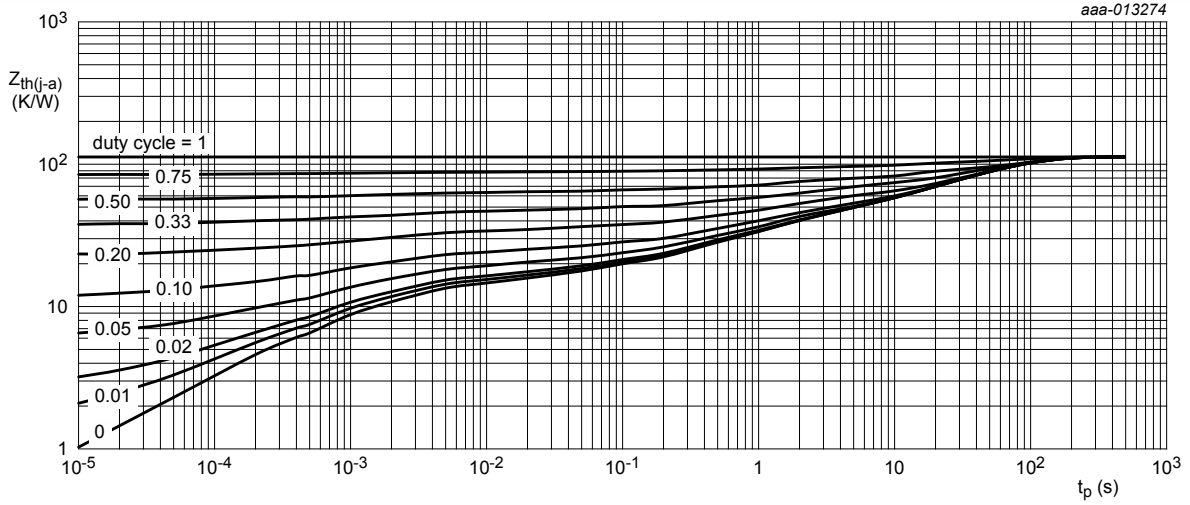
## 9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol         | Parameter  | Conditions  |     | Min | Typ | Max | Unit |
|----------------|--|-------------|-----|-----|-----|-----|------|
| $R_{th(j-a)}$  | thermal resistance from junction to ambient      | in free air | [1] | -   | -   | 192 | K/W  |
|                |  |             | [2] | -   | -   | 125 | K/W  |
|                |  |             | [3] | -   | -   | 93  | K/W  |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point |             |     | -   | -   | 16  | K/W  |

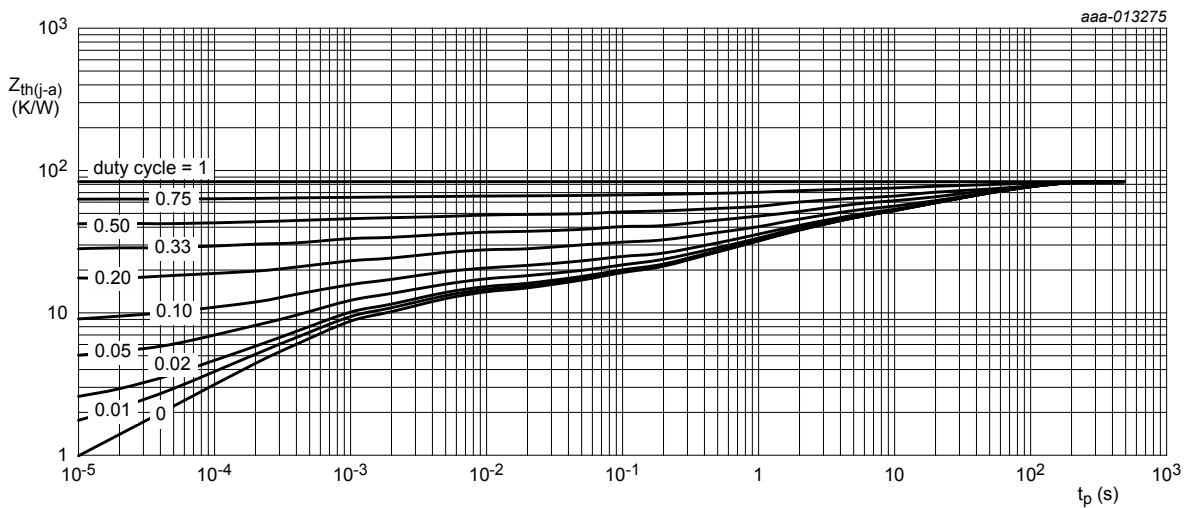
- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.





FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



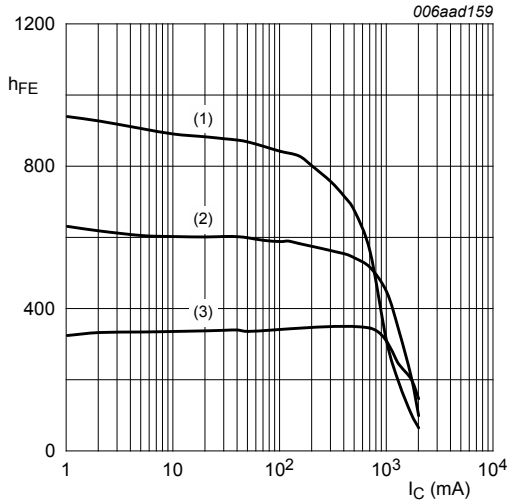
FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 10. Characteristics

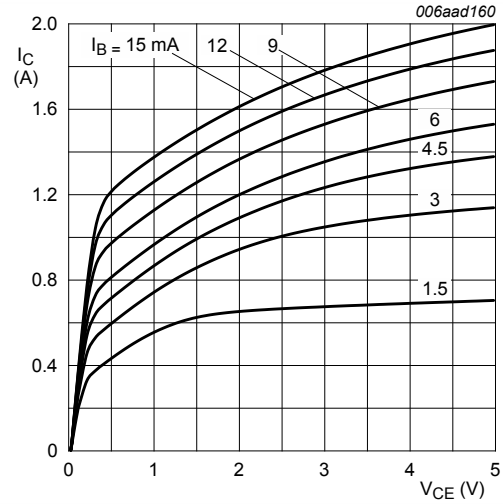
Table 7. Characteristics

| Symbol             | Parameter                               | Conditions   | Min | Typ | Max | Unit |
|--------------------|---|--|-----|-----|-----|------|
| I <sub>CBO</sub>   | collector-base cut-off current          | V <sub>CB</sub> = 32 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C   | -   | -   | 100 | nA   |
|                    |   | V <sub>CB</sub> = 32 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C  | -   | -   | 50  | μA   |
| I <sub>CES</sub>   | collector-emitter cut-off current       | V <sub>CE</sub> = 32 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C  | -   | -   | 100 | nA   |
| I <sub>EBO</sub>   | emitter-base cut-off current            | V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C  | -   | -   | 100 | nA   |
| h <sub>FE</sub>    | DC current gain                         | V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C   | 300 | -   | -   |      |
|                    |   | V <sub>CE</sub> = 5 V; I <sub>C</sub> = 500 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C        | 300 | -   | -   |      |
|                    |   | V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 A; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C           | 200 | -   | -   |      |
|                    |   | V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 A; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C   | 75  | -   | -   |      |
| V <sub>CEsat</sub> | collector-emitter saturation voltage    | I <sub>C</sub> = 100 mA; I <sub>B</sub> = 1 mA; T <sub>amb</sub> = 25 °C   | -   | -   | 80  | mV   |
|                    |   | I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C       | -   | -   | 150 | mV   |
|                    |   | I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C | -   | -   | 275 | mV   |
|                    |   | I <sub>C</sub> = 2 A; I <sub>B</sub> = 200 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C | -   | -   | 550 | mV   |
| R <sub>CEsat</sub> | collector-emitter saturation resistance | I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C | -   | -   | 275 | mΩ   |
| V <sub>BEsat</sub> | base-emitter saturation voltage         | I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C | -   | -   | 1.2 | V    |
| V <sub>BEon</sub>  | base-emitter turn-on voltage            | V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 A; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C           | -   | -   | 1.1 | V    |
| f <sub>T</sub>     | transition frequency                    | V <sub>CE</sub> = 10 V; I <sub>C</sub> = 50 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C                              | 150 | -   | -   | MHz  |
| C <sub>c</sub>     | collector capacitance                   | V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C            | -   | -   | 10  | pF   |



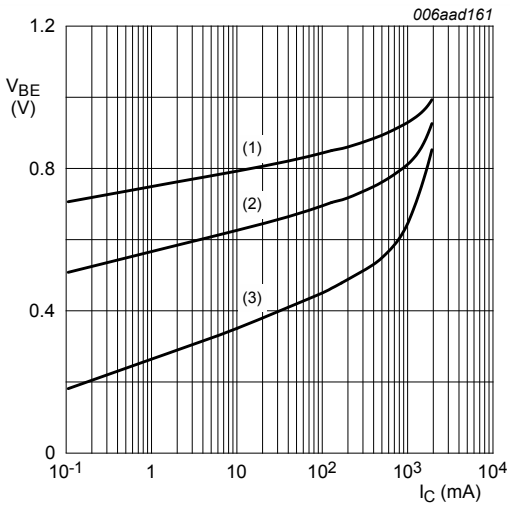
$V_{CE} = 5\text{ V}$   
 (1)  $T_{amb} = 150\text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

**Fig. 5. DC current gain as a function of collector current; typical values**



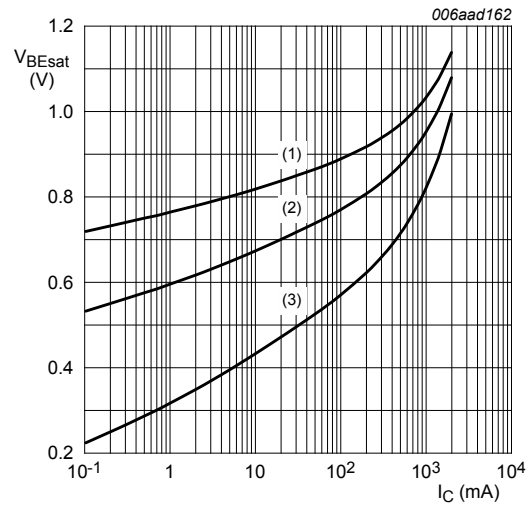
$T_{amb} = 25\text{ }^{\circ}\text{C}$

**Fig. 6. Collector current as a function of collector-emitter voltage; typical values**



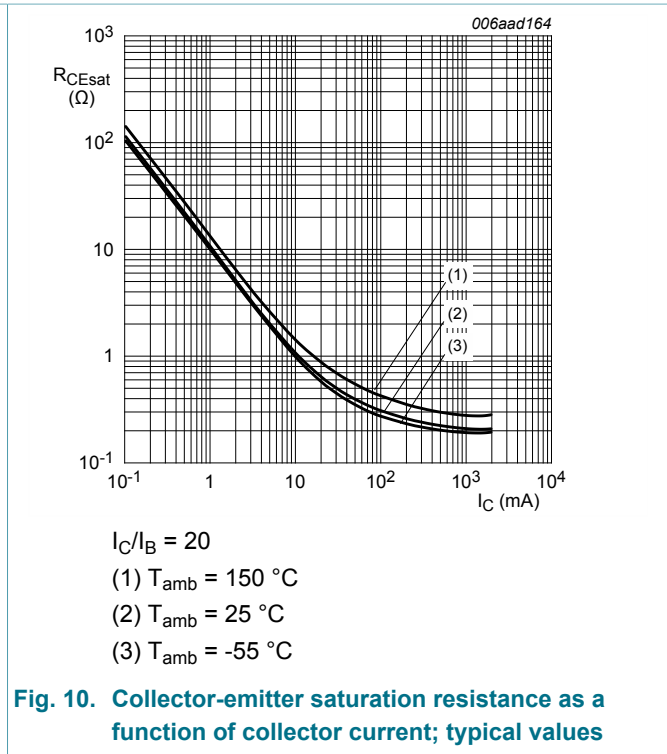
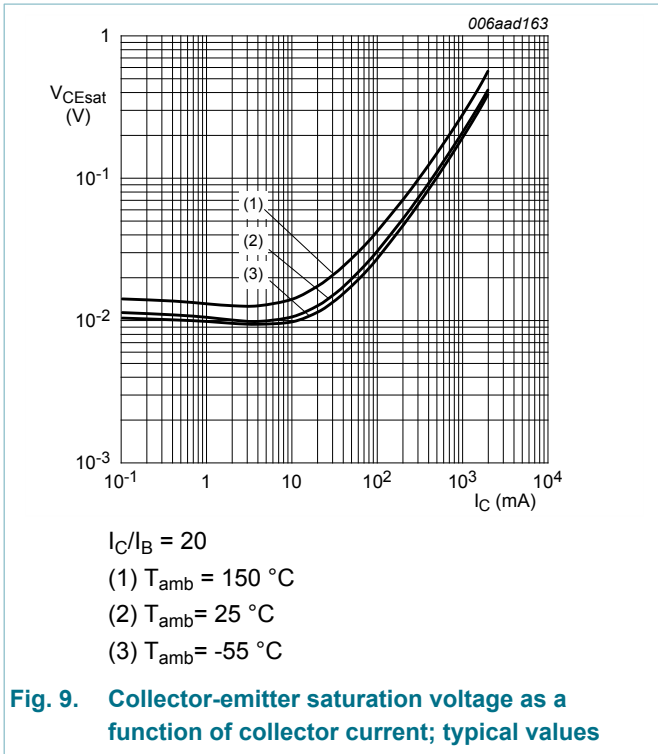
$V_{CE} = 5\text{ V}$   
 (1)  $T_{amb} = -55\text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = 150\text{ }^{\circ}\text{C}$

**Fig. 7. Base-emitter voltage as a function of collector current; typical values**



$I_C/I_B = 20$   
 (1)  $T_{amb} = -55\text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = 150\text{ }^{\circ}\text{C}$

**Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values**



## 11. Test information

### 11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.



## 12. Package outline

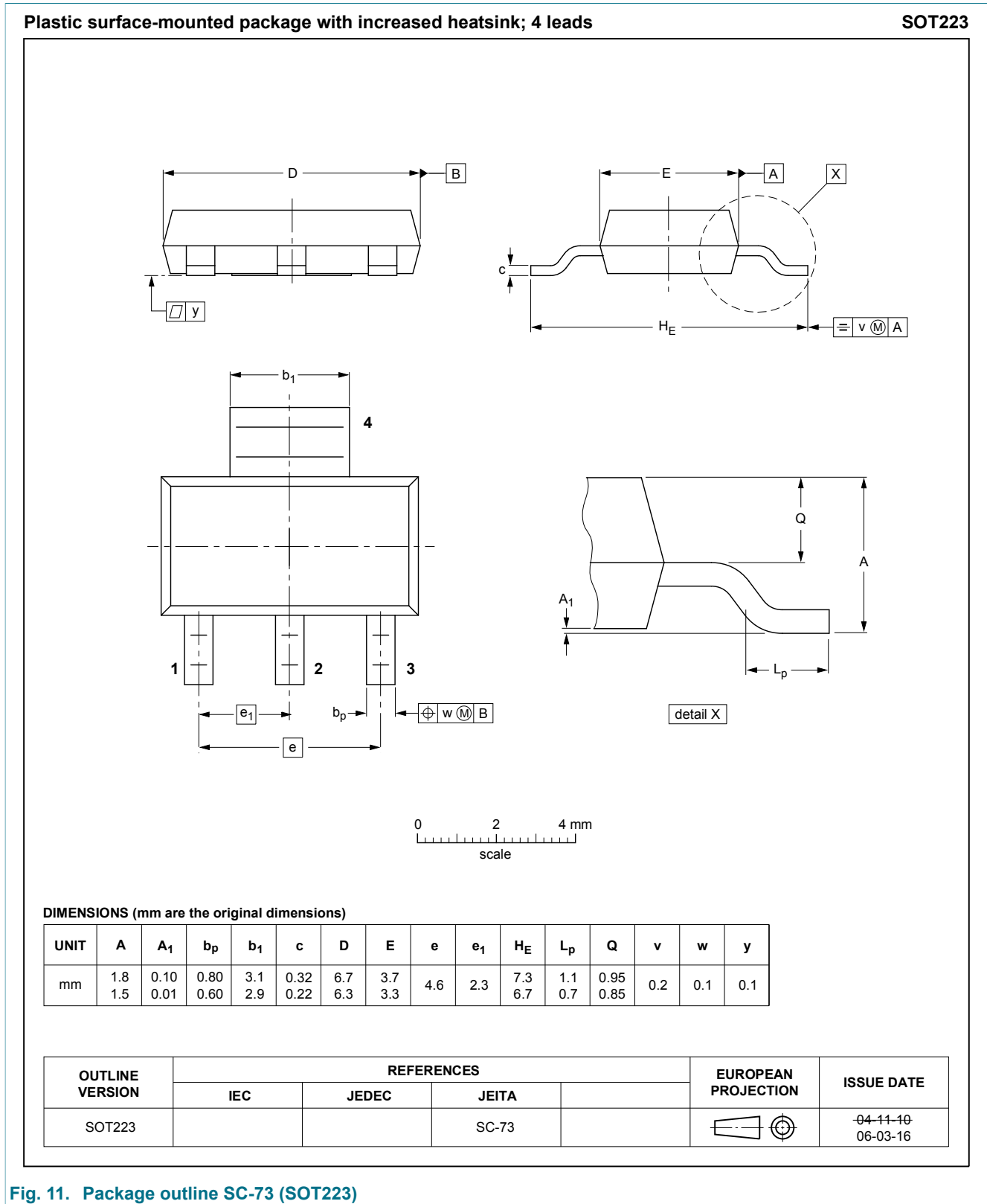


Fig. 11. Package outline SC-73 (SOT223)

### 13. Soldering

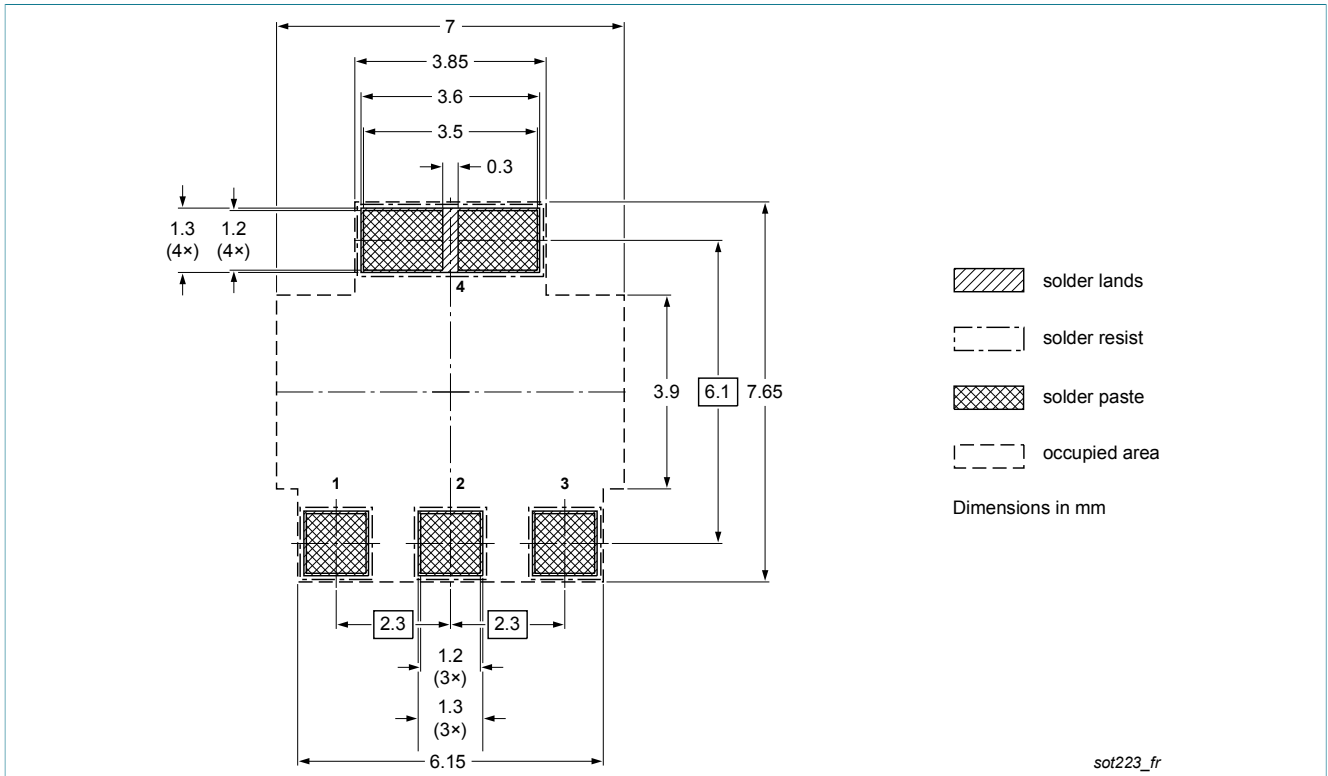


Fig. 12. Reflow soldering footprint for SC-73 (SOT223)

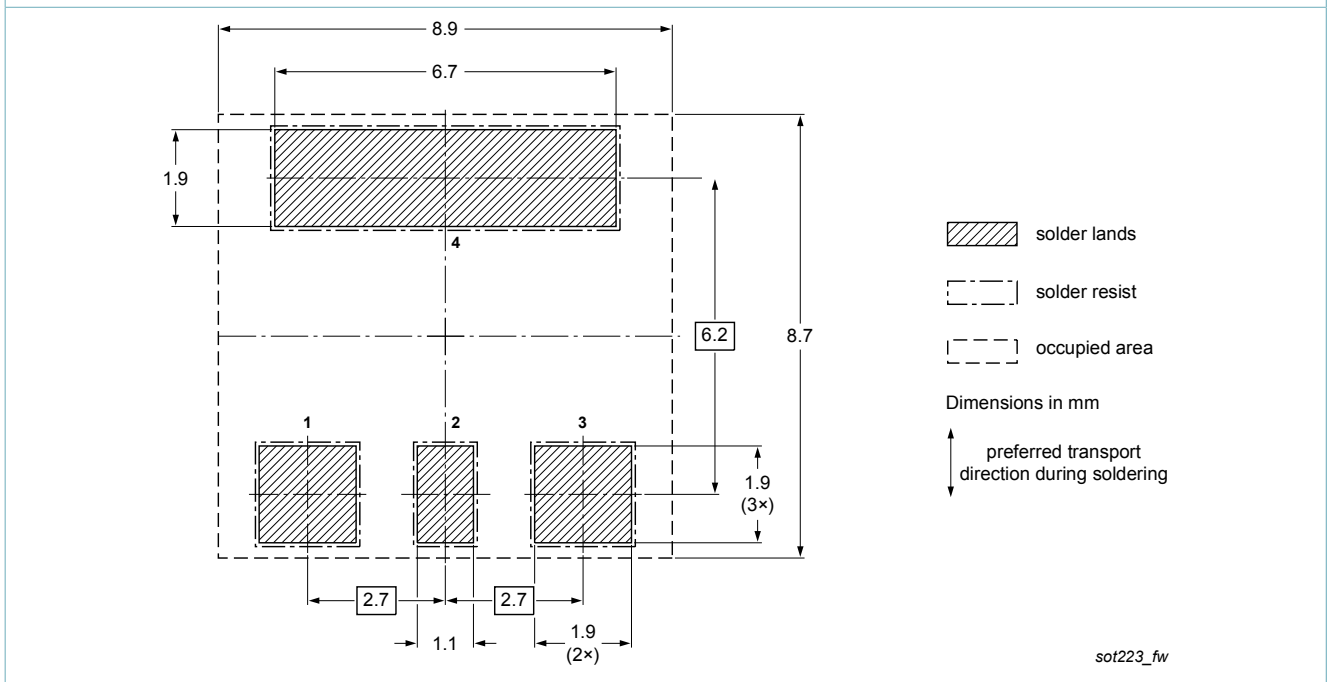


Fig. 13. Wave soldering footprint for SC-73 (SOT223)

## 14. Revision history

Table 8. Revision history

| Data sheet ID  | Release date | Data sheet status  | Change notice | Supersedes |
|----------------|--------------|--------------------|---------------|------------|
| PBSS4240Z v. 1 | 20141016     | Product data sheet | -             | -          |

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|--------------------------------|--------------------|---|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
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Date of release: 16 October 2014

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