DC Brushless Fan Motor Driver

5V Single-phase Full wave Fan motor driver

BH6766FVM

●Description

This is the summary of application for BH6766FVM that suit for notebook PC cooling fan motor. They employ Bi-CMOS process and realize low ON resistance, low power consumption and quiet drive.

●Features

- ■ Compact package (MSOP8)
- ■ BTL soft switching drive
- ■ Constant voltage output for hall element
- ■ Rotating speed pulse signal (FG) output

●Package(s)

	W(Typ.) x D(Typ.) x H(Max.)
MSOP8	2.90mm x 4.00mm x 0.90mm

MSOP8

●Application

- ■ Compact 5V fan such as notebook PC cooling fan

●Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Supply voltage	Vcc	7	V
Power dissipation	Pd	585*	mW
Operating temperature	Topr	-40 to +105	℃
Storage temperature	Tstg	-55 to +150	℃
Output current	Iomax	630**	mA
FG signal output voltage	Vfg	7	V
FG signal output current	Ifg	5	mA
Junction temperature	Tjmax	150	℃

* Reduce by 4.68mW/℃ over Ta=25℃(70.0mm×70.0mm×1.6mm glass epoxy board)

** This value is not to exceed Pd

●Operating conditions

Parameter	Symbol	Limit	Unit
Operating supply voltage range	Vcc	2.0 to 6.0	V
Hall input voltage range	Vh	0.4 to Vcc-1.1	V

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●**Electrical characteristics**(Unless otherwise specified Ta=25℃,Vcc=5V)

Parameter	Symbol	Limits			Unit	Conditions	Characteristics
		Min.	Typ.	Max.			
Circuit current	Icc	-	5	8	mA		Fig.1
Input offset voltage	Vhofs	-	-	±6	mV		-
Output voltage	Vo	-	0.32	0.49	V	Io＝250mA Upper and Lower total	Fig.2,3
Input-output Gain	Gio	45	48	51	dB		-
FG low voltage	Vfgl	-	-	0.3	V	Ifg=3mA	Fig.4
FG leak current	Ifgl	-	-	5	μA	Vfg=7V	-
Input hysteresis voltage	Vhys	±5	±10	±15	mV		Fig.5
Hall bias voltage	Vhb	1.1	1.3	1.5	V	Ihb=-5mA	Fig.6

●**Reference data**

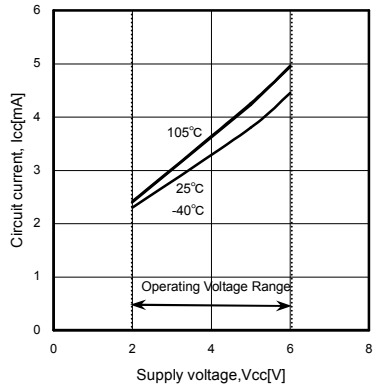

Fig.1 Circuit current

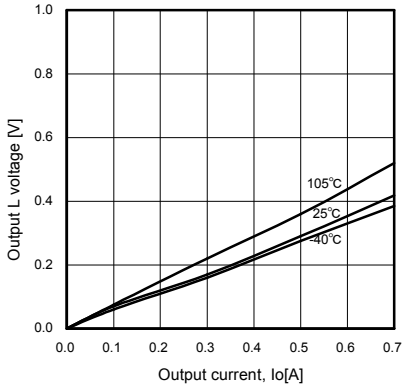

Fig.2 Output L voltage

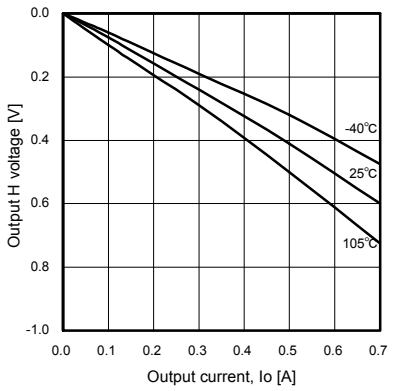

Fig.3 Output H voltage

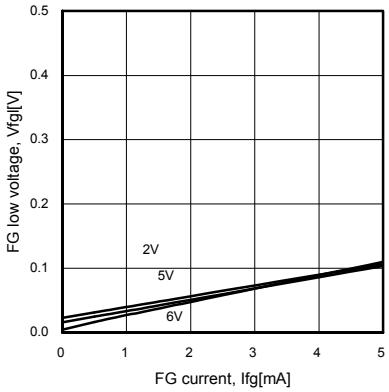

Fig.4 FG low voltage

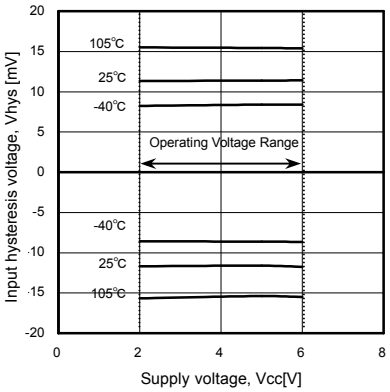

Fig.5 Input hysteresis voltage

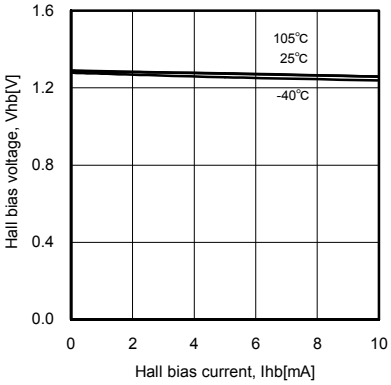

Fig.6 Hall bias voltage

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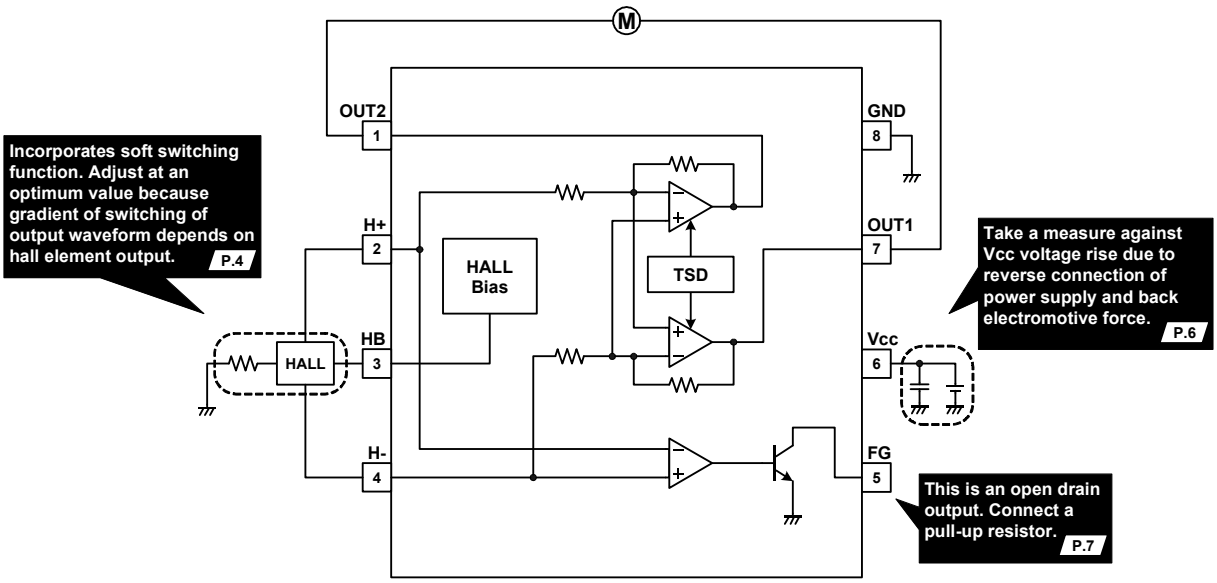
●**Block diagram, application circuit, and pin assignment**



OSC : Internal reference oscillation circuit
TSD : Thermal shut down(heat rejection circuit)

PIN No.	Terminal name	Function
1	OUT2	Motor output terminal 2
2	H+	Hall input terminal+
3	HB	Hall bias terminal
4	H-	Hall input terminal-
5	FG	FG signal output terminal
6	Vcc	Power supply terminal
7	OUT1	Motor output terminal 1
8	GND	GND terminal

●**Truth table**

H+	H-	OUT1	OUT2	FG
H	L	H	L	H(Output Tr：OFF)
L	H	L	H	L(Output Tr：ON)

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●Description of operations

1) Soft switching function (silent drive setting)

Input signal to hall amplifier is amplified to produce an output signal.
When the hall element output signal is small, the gradient of switching of output waveform is gentle; When it is large, the gradient of switching of output waveform is steep. Enter an appropriate hall element output to IC where output waveform swings sufficiently.

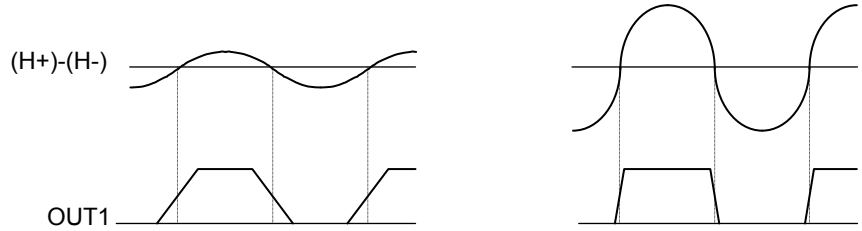

Fig.7 Relation between hall element output amplitude and output waveform

2) Hall input setting

Hall input voltage range is shown in operating conditions.

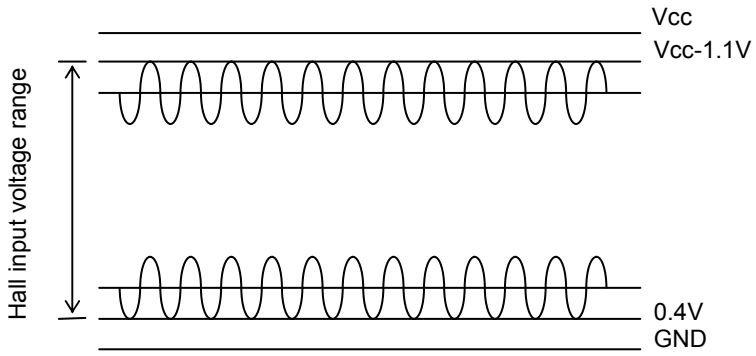

Fig.8 Hall input voltage range

Adjust the value of hall element bias resistor R1 in Fig.9 so that the input voltage of a hall amplifier is input in "hall input voltage range" including signal amplitude.

○Reducing the noise of hall signal

Hall element may be affected by Vcc noise depending on the wiring pattern of board. In this case, place a capacitor like C1 in Fig.9. In addition, when wiring from the hall element output to IC hall input is long, noise may be loaded on wiring. In this case, place a capacitor like C2 in Fig.9.

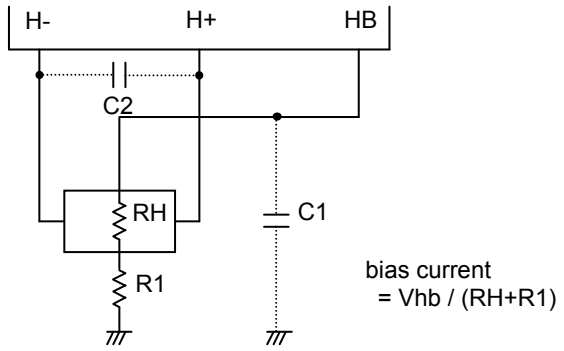

Fig.9 Application near of hall signal

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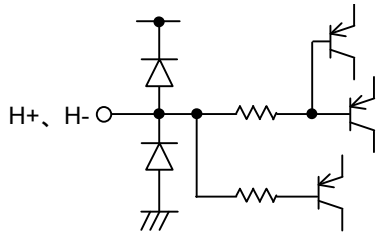
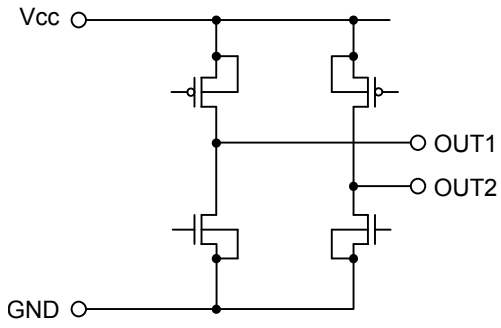
●**Equivalent circuit**

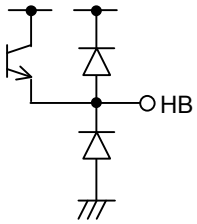
1) Hall input terminal



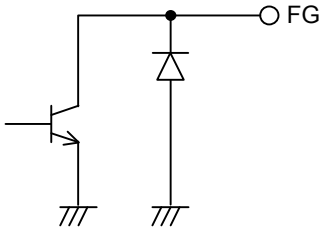
2) Motor output terminal



3) Hall bias terminal



4) FG output terminal



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●**Safety measure**

1) Reverse connection protection diode

Reverse connection of power results in IC destruction as shown in Fig.10. When reverse connection is possible, reverse connection destruction preventive diode must be added between power supply and Vcc.

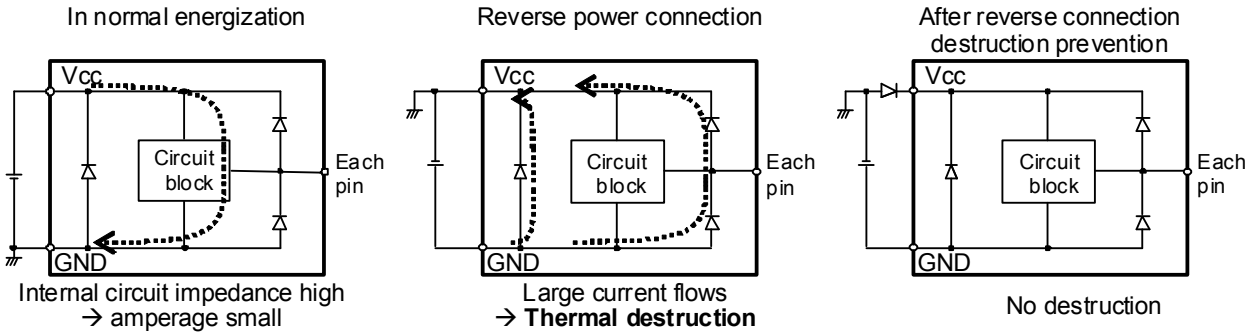

Fig.10 Flow of current when power is connected reversely

2) Measure against Vcc voltage rise by back electromotive force

Back electromotive force (Back EMF) generates regenerative current to power supply. However, when reverse connection protection diode is connected, Vcc voltage rises because the diode prevents current flow to power supply.

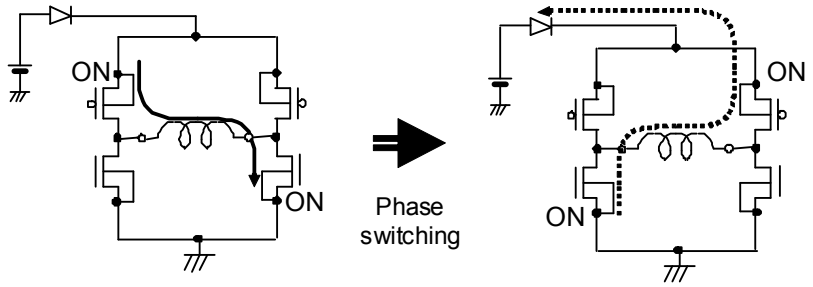

Fig.11 Vcc voltage rise by back electromotive force

When the absolute maximum rated voltage may be exceeded due to voltage rise by back electromotive force, place (A) Capacitor or (B) Zener diode between Vcc and GND. If necessary, add both (C)..

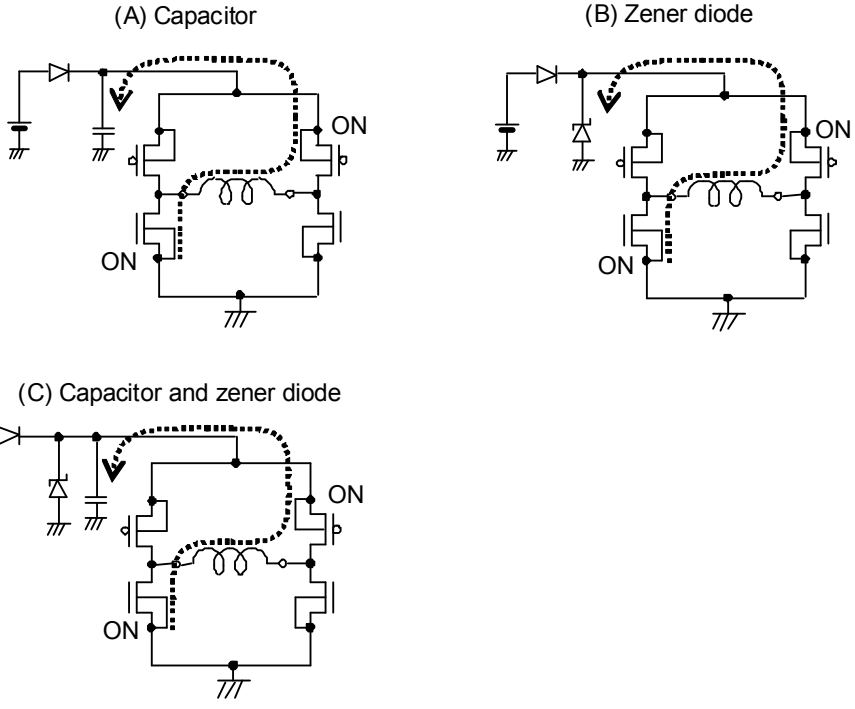

Fig.12 Measure against Vcc voltage rise

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3) Problem of GND line PWM switching
Do not perform PWM switching of GND line because the potential of GND terminal cannot be kept at the minimum.

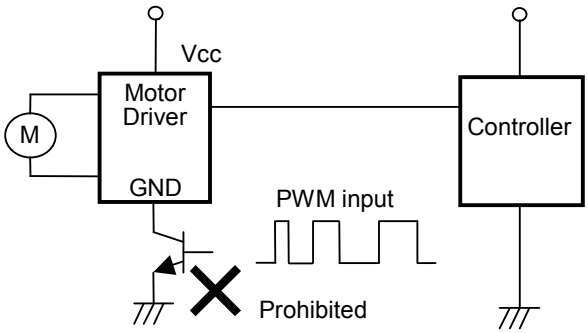


Fig.13 GND Line PWM switching prohibited

4) FG output
FG output is an open drain and requires pull-up resistor.
The IC can be protected by adding resistor R1. An excess of absolute maximum rating, when FG output terminal is directly connected to power supply, could damage the IC.

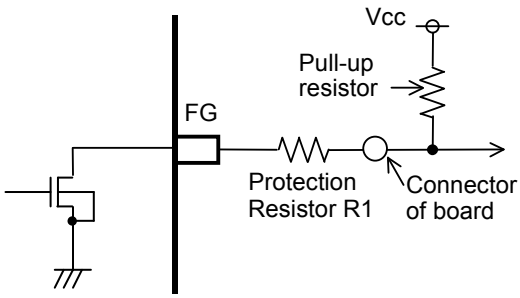
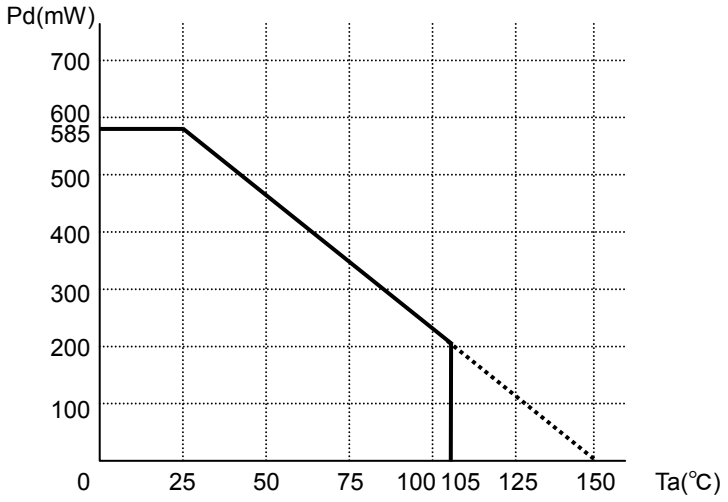


Fig.14 Protection of FG terminal

●Thermal derating curve

Thermal derating curve indicates power that can be consumed by IC with reference to ambient temperature. Power that can be consumed by IC begins to attenuate at certain ambient temperature. This gradient is determined by thermal resistance θja.
Thermal resistance θja depends on chip size, power consumption, package ambient temperature, packaging condition, wind velocity, etc., even when the same package is used. Thermal derating curve indicates a reference value measured at a specified condition. Fig.15 shows a thermal derating curve (Value when mounting FR4 glass epoxy board 70 [mm] x 70 [mm] x 1.6 [mm] (copper foil area below 3 [%]))



∗ Reduce by 4.68 mW/°C over 25°C.
(70.0mm×70.0mm×1.6mm glass epoxy board)

Fig.15 Thermal derating curve

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●**Notes for use**

1) Absolute maximum ratings
Devices may be destroyed when supply voltage or operating temperature exceeds the absolute maximum ratings. Because the cause of this damage cannot be identified as a short circuit or an open circuit, if any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.
2) Connecting the power supply connector backward
Connecting of the power supply in reverse polarity can damage IC. Take precautions when connecting the power supply lines. An external direction diode can be added.
3) Power supply line
Back electromotive force causes regenerated current to power supply line, therefore take a measure such as placing a capacitor between power supply and GND for routing regenerated current. And fully ensure that the capacitor characteristics have no problem before determine a capacitor value. (when applying electrolytic capacitors, capacitance characteristic values are reduced at low temperatures)
4) GND potential
It is possible that the motor output terminal may deflect below GND terminal because of influence by back electromotive force of motor. The potential of GND terminal must be minimum potential in all operating conditions, except that the levels of the motor outputs terminals are under GND level by the back electromotive force of the motor coil. Also ensure that all terminals except GND and motor output terminals do not fall below GND voltage including transient characteristics. Malfunction may possibly occur depending on use condition, environment, and property of individual motor. Please make fully confirmation that no problem is found on operation of IC.
5) Thermal design
Use a thermal design that allows for a sufficient margin in light of the power dissipation(Pd) in actual operating conditions.
6) Inter-pin shorts and mounting errors
Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.
7) Actions in strong electromagnetic field
Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.
8) ASO
When using the IC, set the output transistor so that it does not exceed absolute maximum rations or ASO.
9) Thermal shut down circuit
The IC incorporates a built-in thermal shutdown circuit (TSD circuit). Operation temperature is 175ºC (typ.) and has a hysteresis width of 25ºC (typ.). When IC chip temperature rises and TSD circuit works, the output terminal becomes an open state. TSD circuit is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operation this circuit or use the IC in an environment where the operation of this circuit is assumed.
10) Testing on application boards
When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.
11) GND wiring pattern
When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.
12) Capacitor between output and GND
When a large capacitor is connected between output and GND, if Vcc is shorted with 0V or GND for some cause, it is possible that the current charged in the capacitor may flow into the output resulting in destruction. Keep the capacitor between output and GND below 100uF.
13) IC terminal input
When Vcc voltage is not applied to IC, do not apply voltage to each input terminal. When voltage above Vcc or below GND is applied to the input terminal, parasitic element is actuated due to the structure of IC. Operation of parasitic element causes mutual interference between circuits, resulting in malfunction as well as destruction in the last. Do not use in a manner where parasitic element is actuated.
14) In use
We are sure that the example of application circuit is preferable, but please check the character further more in application to a part which requires high precision. In using the unit with external circuit constant changed, consider the variation of externally equipped parts and our IC including not only static character but also transient character and allow sufficient margin in determining.

Status of this document
The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.
If there are any differences in translation version of this document formal version takes priority

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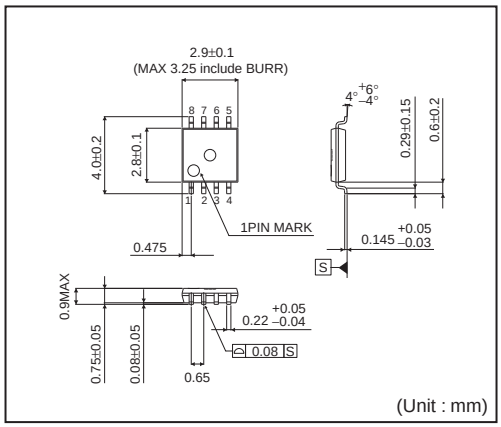
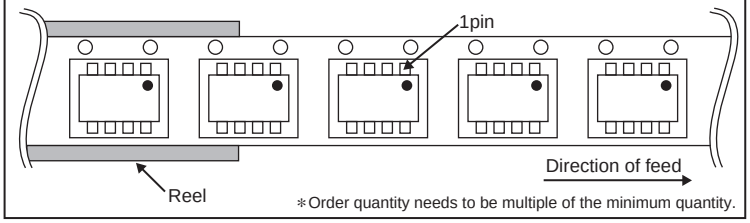
●Physical Dimension

MSOP8



<Tape and Reel information>

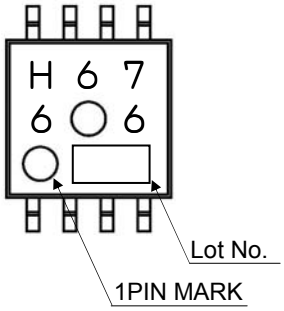
Tape	Embossed carrier tape
Quantity	3000pcs
Direction of feed	TR (The direction is the 1pin of product is at the upper right when you hold reel on the left hand and you pull out the tape on the right hand)



∗Order quantity needs to be multiple of the minimum quantity.

●Marking Diagram

MSOP8
(TOP VIEW)



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

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   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
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   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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LifeElectronics
Живое партнерство

ООО «ЛайфЭлектроникс» **“LifeElectronics” LLC**

ИНН 7805602321 КПП 780501001 Р/С 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 30101810900000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

Мы предлагаем:

- *Конкурентоспособные цены и скидки постоянным клиентам.*
- *Специальные условия для постоянных клиентов.*
- *Подбор аналогов.*
- *Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.*

- *Приемлемые сроки поставки, возможна ускоренная поставка.*
- *Доставку товара в любую точку России и стран СНГ.*
- *Комплексную поставку.*
- *Работу по проектам и поставку образцов.*
- *Формирование склада под заказчика.*

- *Сертификаты соответствия на поставляемую продукцию (по желанию клиента).*
- *Тестирование поставляемой продукции.*
- *Поставку компонентов, требующих военную и космическую приемку.*
- *Входной контроль качества.*
- *Наличие сертификата ISO.*

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- *Регистрацию проекта у производителя компонентов.*
- *Техническую поддержку проекта.*
- *Защиту от снятия компонента с производства.*
- *Оценку стоимости проекта по компонентам.*
- *Изготовление тестовой платы монтаж и пусконаладочные работы.*

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