

Technical Note

Sound Processors for BOOM BOX / Mini-component Stereo

Sound Processors with Built-in 3-band Equalizers

BD3403FV, BD3861FS, BD3883FS

No.10086EAT01

Description

The Sound Processor has a built-in 3 Band Equalizer and can be controlled with a 2-wire serial. It is suited for a sound quality design which incorporates various functions, ranging from source selectors, such as BOOM BOX, Mini-audio systems and Micro-audio systems to preamplifiers at the front stage of the power amp.

Features

- 1) High S/N, achieved by implementing 2-stage configuration of Front Volume and Rear Volume.
- 2) Provides surround and rear volume with Soft-switch to reduce a shock sound during switching functions(BD3883FS).
- 3) Volume and tone implemented with the resistance ladder circuit
- (to achieve high performance with low noise and low distortion).Uses the BiCMOS process that achieves low-consumption current, which contributes to an energy-saving design.
- Using the BiCMOS process, has the advantage in quality over the scaling down of the internal regulators and heat controls.
 SSOP-A32 and SSOP-B40 are used for the packages. Input pins and output pins are organized and separately laid out so as to keep the signal flows in one direction which consequently, simplify pattern layout of the set board and decrease the board dimensions.

Applications

BOOM BOX, mini-audio systems, and micro-audio systems.

Product lineup

Parameter	BD3403FV	BD3861FS	BD3883FS	
Operating Voltage Range	6.5 to 9.5V	6.5 to 9.5V	6.5 to 9.5V	
Equalizer	3 band (BASS, MIDDLE, TREBLE)	3 band (BASS, MIDDLE, TREBLE)	3 band (BASS, MIDDLE, TREBLE)	
Front Volume	0 to -30dB/2dB step	0 to -50dB/2dB step -50 to -70dB/4dB step, -∞dB	0 to -87dB/1dB step, -∞dB	
Rear Volume	0 to -59dB/1dB step, -∞dB	0 to -59dB/1dB step, -∞dB	0, -10dB	
Input Gain	0 to 26dB/2dB step	0 to 26dB/2dB step	0, 6, 12, 16, 20, 23, 26, 29dB	
Microphone Input	0	0	_	
Surround	0	-	0	
Package	SSOP-B40	SSOP-A32	SSOP-A32	

Absolute maximum ratings (Ta=25°C)

Prameter	Sumbol	Ratings				
Prameter	Symbol	BD3403FV	BD3861FS,BD3883FS	– Unit		
Power Supply Voltage	Vcc	10	10	V		
Power Dissipation	Pd	900 ^{*1}	950 ^{*2}	mW		
Input Voltage Range	Vin	GND-0.3 to VCC+0.3	GND-0.3 to VCC+0.3	V		
Operating Temperature Range	Topr	-25 to +75	-25 to +75	°C		
Storage Temperature Range	Tstg	-55 to +125	-55 to +125	°C		

¹ Reduced by 9.0 mW/°C over 25°C, when installed on the standard board (size: 70×70×1.6mm) for (BD3403FV).

² Reduced by 9.5 mW/°C over 25°C, when installed on the standard board (size:70×70×1.6mm) for (BD3861FS,BD3883FS).

Operating voltage range

· <u> </u>			
Prameter	Symbol	Ratings	Unit
BD3403FV			
BD3861FS	Vcc	6.5 to 9.5	V
BD3883FS			

•Electrical characteristics

OBD3403FV

 $V_{CC}=9V$, f=1KHz, VIN=1Vrms, Rg=600 Ω , RL=10k Ω , Ta=25°C, Input Gain=0dB, VOL=0dB, Bass, Middle, Treble=0dB, Surround=OFF, unless otherwise noted.

Duot	Bass, Middle, Treble=0dB, Surround=OFF, unless otherwise noted.									
	Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition			
	Circuit Current	IQ	-	16.0	30.0	mA	At no signal			
	Output Voltage Gain	GV	-1.5	0.0	1.5	dB	GV=20log(VOUT/VIN)			
-AL	Total Harmonic Distortion ratio	THD	-	0.02	0.08	%	400 to 30kHz BPF			
TOTAL	Maximum Output Voltage	VOM	2.0	2.5	-	Vrms	THD=1%			
	Output Noise Voltage	VNO	-	1.8	6.0	µVrms	Rg=0kΩ, IHF-A			
	Cross-talk between Channels	СТ	-	3.0	9.0	µVrms	Rg=0kΩ, IHF-A			
	6dBSW Gain	GV6	5	6	7	dB	VIN=200mVrms GV6=20log(VOUT/VIN)			
	Input Voltage Gain 1	Gvmaxl1	-1	*2	+1	dB	VIN=200mVrms, From 0 to 10dB GvmaxI1=20log(VOUT/VIN)			
	Input Voltage Gain 2	Gvmaxl2	-1.5	*2	+1.5	dB	VIN=200mVrms From 12 to 26dB GvmaxI2=20log(VOUT/VIN)			
UT	Input Gain Switching Step	Gvmaxlst	-	2	-	dB	From 0 to 26dB			
INPUT	Input Total Harmonic Distortion ratio	THDI	-	0.02	0.08	%	400 to 30kHz BPF			
	Input Maximum Output Voltage	VOMI	2.0	2.5	-	dB	THD=1%			
	Cross-talk between Selectors	CS	-	-80.0	-70.0	dB	Rg=0kΩ, IHF-A CS=20log(VOUT/VIN)			
	Input Impedance	RI	35.0	50.0	65.0	kΩ	RI=51k×VOUT/ (VIN-VOUT)			
	E Input SW Attenuation	GRE	-	-20.0	-15.0	dB	GRE=20log(VOUT/VIN)			
	Input Volume 1	GIV1	-2	*3	+2	dB	From 0 to -30dB GIV1=20log(VOUT/VIN)			
	Volume Switching Step 1	GIVst1	-	2	-	dB	From 0 to -30dB			
OUTPUT VOLUME	Output Volume	GOV	-1	*1	+1	dB	From 0 to -59dB Gov=20log(VOUT/VIN)			
OLL	Output Switching Step	GOVst	-	1	-	dB	From 0 to –59dB			
ΟŠ	Maximum attenuation	GminO	-	-	-90.0	dB	IHF-A, GminO=20log(VOUT/VIN)			
SURROUND	Surround Gain CH1→CH2	Gsur1	5	7	9	dB	V _{IN} =200mVrms, f=1kHz			
SURR	Surround Gain CH2→CH1	Gsur2	5	7	9	dB	V _{IN} =200Vrms, f=1kHz			
0	Bass Boost Gain	GBB	-2	*1	+2	dB	V _{IN} =200mVrms, f=90Hz, From 0 to 14dB GBB=20log(VOUT/VIN)			
BASS	Bass Cut Gain	GBC	-2	*1	+2	dB	V _{IN} =200mVrms, f =90Hz, From −14 to 0dB GBC=20log(VOUT/VIN)			
	Bass Switching Step	GBST	-	2	-	dB	V _{IN} =200mVrms, f=90Hz			
Ш	Middle Boost Gain	GMB	-2	*1	+2	dB	V _{IN} =200mVrms, From 0 to 12dB GMB=20log(VOUT/VIN)			
MIDDLE	Middle Cut Gain	GMC	-2	*1	+2	dB	V _{IN} =200mVrms, From -12 to 0dB GMC=20log(VOUT/VIN)			
	Middle Switching Step	GMST	-	2	-	dB	V _{IN} =200mVrms			
ΓE	Treble Boost Gain	GTB	-2	*1	+2	dB	V _{IN} =200mVrms, f=10kHz From 0 to 12dB GTB=20log(VOUT/VIN)			
TREBLE	Treble Cut Gain	GTC	-2	*1	+2	dB	V _{IN} =200mVrms, f=10kHz From -12 to 0dB GTC=20log(VOUT/VIN)			
	Treble Switching Step	GTST	-	2	-	dB	V _{IN} =200mVrms, f=10kHz			
MIC	Microphone Voltage Gain	GMIC	4.5	6.0	7.5	dB	V _{IN} =200mVrms GMIC=20log(VOUT/VIN)			

*1 *2 Typ. is set to the value descrived in condition.

Min. and Max. mean the error.

⊙BD3861FS

VCC=9V, f=1KHz, VIN=1Vrms, Rg=600Ω, RL=10kΩ, Ta=25°C, Input Gain=0dB, VOL=0dB, Bass, Middle, Treble=0dB, unless otherwise noted.

	Bass, Middle, Treble=0dB, unless otherwise noted.									
	Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition			
	Circuit Current	IQ	-	13.0	26.0	mA	At no signal			
	Output Voltage Gain	GV	-1.5	0.0	1.5	dB	GV=20log(VOUT/VIN)			
LAL	Total Harmonic Distortion	THD	-	0.02	0.08	%	400 to 30kHz BPF			
TOTAL	Maximum Output Voltage	VOM	2.0	2.5	-	Vrms	THD=1%			
	Output Noise Voltage	VNO	-	8.0	15.0	μVrms	Rg=0kΩ, IHF-A			
	Cross-talk between Channels	СТ	-	-80	-70	dB	Rg=0kΩ, IHF-A			
	6dB SW Gain	GV6	5	6	7	dB	VIN=200mVrms GV6=20log(VOUT/VIN)			
	Input Voltage Gain 1	Gvmaxl1	-1	*1	+1	dB	VIN=200mVrms From 0 to 10dB Gvmaxl1=20log(VOUT/VIN)			
F	Input Voltage Gain 2	Gvmaxl2	-1.5	*1	+1.5	dB	VIN=200mVrms, From 12 to 26dB GvmaxI2=20log(VOUT/VIN)			
INPUT	Input Gain Switching Step	Gvmaxlst	-	2	-	dB	From 0 to 26dB			
Z	Input Total Harmonic Distortion	THDI	-	0.02	0.08	%	400 to 30kHz BPF			
	Input Maximum Output Voltage	VOMI	2.0	2.5	-	dB	THD=1%			
	Cross-talk between Selectors	CS	-	-80.0	-70.0	dB	Rg=0kΩ, IHF-A CS=20log(VOUT/VIN)			
	Input Impedance	RI	35.0	50.0	65.0	kΩ	RI=51k×VOUT/ (VIN-VOUT)			
	E Input SW Attenuation	GRE	-	-20.0	-15.0	dB	GRE=20log(VOUT/VIN)			
	Input Volume 1	GIV1	-2	*1	+2	dB	From 0 to -50dB GIV1=20log(VOUT/VIN)			
INPUT VOLUME	Input Volume 2	GIV2	-3	*1	+3	dB	From -54 to -70dB GIV2=20log(VOUT/VIN)			
ЙЦ	Volume Switching Step 1	GIVst1	-	2	-	dB	From 0 to -50dB			
	Volume Switching Step 2	GIVst2	-	4	-	dB	From -54 to –70dB			
	Maximum attenuation	Gminl	-	-	-90.0	dB	IHF-A, GminI=20log(VOUT/VIN)			
OUTPUT VOLUME	Output Volume	GOV	-1	*1	+1	dB	From 0 to -59dB Gov=20log(VOUT/VIN)			
JTP DLU	Output Switching Step	GOVst	-	1	-	dB	From 0 to -59dB			
20 VO	Maximum attenuation	GminO	-	-	-90.0	dB	IHF-A GminO=20log(VOUT/VIN)			
S	Bass Boost Gain	GBB	-2	*1	+2	dB	VIN=200mVrms, f=90Hz, From 0 to 14dB GBB=20log(VOUT/VIN)			
BASS	Bass Cut Gain	GBC	-2	*1	+2	dB	VIN=200mVrms, f =90Hz, From -14 to 0dB GBC=20log(VOUT/VIN)			
	Bass Switching Step	GBST	-	2	-	dB	VIN=200mVrms, f=90Hz			
щ	Middle Boost Gain	GMB	-2	*1	+2	dB	VIN=200mVrms, From 0 to 12dB GMB=20log(VOUT/VIN)			
MIDDLE	Middle Cut Gain	GMC	-2	*1	+2	dB	VIN=200mVrms, From -12 to 0dB GMC=20log(VOUT/VIN)			
~	Middle Switching Step	GMST	-	2	-	dB	VIN=200mVrms			
3LE	Treble Boost Gain	GTB	-2	*1	+2	dB	VIN=200mVrms, f=10kHz From 0 to 12dB GTB=20log(VOUT/VIN)			
TREBLE	Treble Cut Gain	GTC	-2	*1	+2	dB	VIN=200mVrms, f=10kHz From -12 to 0dB GTC=20log(VOUT/VIN)			
	Treble Switching Step	GTST	-	2	-	dB	VIN=200mVrms, f=10kHz			
MIC	Microphone Voltage Gain	GMIC	4.5	6.0	7.5	dB	VIN=200mVrms GMIC=20log(VOUT/VIN)			

*1 Typ. is set to the value descrived in condition.

Min. and Max. mean the error.

⊙BD3883FS

Ta=25°C, VCC=8V, f=1kHz, Vi=200mVrms, RL=10k Ω , Rg=600 Ω , Input Selector=Ach, Input Gain=0dB, Volume=0dB, Bass=0dB, Middle=0dB, Treble=0dB, Surround=OFF, RECOUT=OFF, unless otherwise noted.

Surround=OFF, RECOUT=OFF, unl		Symbol				Condition	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition	
Circuit Current	IQ	-	8	21	mA	At no signal	
Total Output Voltage Gain	Gv	-2	0	2	dB		
Total Harmonic Distortion	THDO	-	0.01	0.1	%	BW=400 to 30kHz	
Maximum Output Voltage	Vomaxo	1.6	2.1	-	Vrms	THD=1% BW=400 to 30kHz	
Total Residual Noise Voltage	Vno	-	2	10	µVrms	Rg=0Ω, Vol=-∞dB BW=IHF-A, REAR ATT=-10dB	
Total Output Noise Voltage	Vmno	-	4	15	µVrms	Rg=0Ω, Vol=0dB BW=IHF-A	
Cross-talk between Channels	CTC12	-	-80	-70	dB	Rg=0Ω, BW=IHF-A VOUT=1Vrms	
Input Impedance	Rin	70	100	130	kΩ		
Output Impedance	Rout	-	-	50	Ω		
Cross-talk between Selectors	CTS1	-	-80	-70	dB	VOUT=1Vrms Rg=0Ω, BW=IHF-A	
Volume Control Range	VRI	-90	-87	-84	dB	BW=IHF-A ,Vout=1Vrms	
Volume Setting Error 1	VEI1	-2	0	2	dB	0 to -53dB,BW=IHF-A VOUT=1Vrms	
Volume Setting Error 2	VEI2	-3	0	3	dB	-54 to -87dB,BW=IHF-A VOUT=1Vrms	
Maximum Attenuation	Vmin	-	-	-90	dB	BW=IHF-A VOUT=1Vrms	
Volume Input Impedance	Rvin	39	56	73	kΩ		
Bass Gain	Gb	-17	7.5 to +1	7.5	dB		
Bass Gain Setting Error	BE	-2.5	0	-2.5	dB		
Middle Gain	Gm	-	14 to +1	4	dB		
Middle Gain Setting Error	ME	-2	0	-2	dB		
Treble Gain	Gt	-	14 to +1	4	dB		
Treble Gain Setting Error	TE	-2	0	2	dB		
Surround In-phase Gain	Vsur1	-2	0	2	dB		
Surround Single-phase Gain	Vsur2	4.3	6.3	8.3	dB	AC-grounding	
Opposite-phase Gain	Vsur3	8	10	12	dB		
	Total Output Voltage Gain Total Harmonic Distortion Maximum Output Voltage Total Residual Noise Voltage Total Output Noise Voltage Cross-talk between Channels Input Impedance Output Impedance Cross-talk between Selectors Volume Control Range Volume Setting Error 1 Volume Setting Error 2 Maximum Attenuation Volume Input Impedance Bass Gain Bass Gain Setting Error Middle Gain Setting Error Treble Gain Setting Error Surround In-phase Gain Surround Single-phase Gain	Total Output Voltage GainGvTotal Harmonic DistortionTHDOMaximum Output VoltageVomaxoTotal Residual Noise VoltageVnoTotal Output Noise VoltageVmnoCross-talk between ChannelsCTC12Input ImpedanceRoutCross-talk between SelectorsCTS1Volume Control RangeVRIVolume Setting Error 1VEI1Volume Setting Error 2VEI2Maximum AttenuationVminVolume Input ImpedanceRvinBass GainGbBass Gain Setting ErrorBEMiddle Gain Setting ErrorMETreble Gain Setting ErrorTESurround In-phase GainVsur1Surround Single-phase GainVsur2	Total Output Voltage GainGv-2Total Harmonic DistortionTHDO-Maximum Output VoltageVomaxo1.6Total Residual Noise VoltageVno-Total Output Noise VoltageVmno-Total Output Noise VoltageVmno-Cross-talk between ChannelsCTC12-Input ImpedanceRout-Cross-talk between SelectorsCTS1-Volume Control RangeVRI-90Volume Setting Error 1VEI1-2Volume Setting Error 2VEI2-3Maximum AttenuationVmin-Volume Input ImpedanceRvin39Bass GainGb-17Bass Gain Setting ErrorBE-2.5Middle Gain Setting ErrorME-2Treble Gain Setting ErrorTE-2Surround In-phase GainVsur1-2Surround Single-phase GainVsur24.3	Total Output Voltage Gain Gv -2 0 Total Harmonic DistortionTHDO $ 0.01$ Maximum Output VoltageVomaxo 1.6 2.1 Total Residual Noise VoltageVno $ 2$ Total Output Noise VoltageVmno $ 4$ Cross-talk between ChannelsCTC12 $ -80$ Input ImpedanceRin70 100 Output ImpedanceRout $ -$ Cross-talk between SelectorsCTS1 $ -80$ Volume Control RangeVRI -90 -87 Volume Setting Error 1VEI1 -2 0 Volume Setting Error 2VEI2 -3 0 Maximum AttenuationVmin $ -$ Volume Input ImpedanceRvin 39 56 Bass GainGb -17.5 to $+11$ Bass Gain Setting ErrorBE -2.5 0 Middle Gain Setting ErrorME -2 0 Treble Gain Setting ErrorTE -2 0 Surround In-phase GainVsur1 -2 0 Surround Single-phase GainVsur2 4.3 6.3	Total Output Voltage GainGv-202Total Harmonic DistortionTHDO-0.010.1Maximum Output VoltageVomaxo1.62.1-Total Residual Noise VoltageVno-210Total Output Noise VoltageVmno-415Cross-talk between ChannelsCTC1280-70Input ImpedanceRin70100130Output ImpedanceRout50Cross-talk between SelectorsCTS180Volume Control RangeVRI-90-87-84Volume Setting Error 1VEI1-202Volume Setting Error 2VEI2-303Maximum AttenuationVmin90Volume Input ImpedanceRvin395673Bass Gain Setting ErrorBE-2.50-2.5Middle GainGm-1+ to +1+Middle Gain Setting ErrorME-202Treble Gain Setting ErrorTE-202Surround In-phase GainVsur1-202Surround Single-phase GainVsur24.36.38.3	Total Output Voltage GainGv Gv-202dBTotal Harmonic DistortionTHDO0.010.1%Maximum Output VoltageVomaxo1.62.1VrmsTotal Residual Noise VoltageVno210 μ VrmsTotal Output Noise VoltageVmno415 μ VrmsTotal Output Noise VoltageVmno415 μ VrmsCross-talk between ChannelsCTC1280-70dBInput ImpedanceRin70100130kQOutput ImpedanceRout80-70dBVolume Control RangeVRI-90-87-84dBVolume Setting Error 1VEI1-202dBVolume Setting Error 2VEI2-303dBVolume Input ImpedanceRvin395673kQBass GainGb-1dBMiddle GainGrm-2.50-2.5dBMiddle Gain Setting ErrorME-2.202dBTreble Gain Setting ErrorTE-202dBSurround In-phase GainVsur1-202dBSurround Single-phase GainVsur24.36.38.3dB	

Control signal specifications

- 1. Signal Timing Conditions
 - Data is read on the rising edge of the clock.
 - $\boldsymbol{\cdot}$ Latch is read on the falling edge of the clock.
 - Latch signal must terminate with the LOW state.
 - $\boldsymbol{\cdot}$ To avoid malfunctions, clock and data signals must terminate with the LOW state.
 - 1byte=8bit



Doromotor	Symbol		Linit		
Parameter	Symbol	Min.	Тур.	Max.	– Unit
Minimum Clock Width	twc	2.0	-	-	μs
Minimum Data Width	twd	2.0	-	-	μs
Minimum Latch Width	tw1	2.0	-	-	μs
Data Set-up Time (DATA→CLK)	Tsd	1.0	-	-	μs
Data Hold Time (CLK→DATA)	Thd	1.0	-	-	μs
Latch Set-up Time (CLK→LATCH)	ts1	1.0	-	-	μs
Latch Hold Time (DATA→LATCH)	th1	1.0	-	-	μs
Latch Low Set-up Time	ts	1.0	-	-	μs
Latch Low Hold Time	th	1.0	-	-	μs

2. Voltage Conditions for Control Signals (BD3403FV, BD3861FS)

Baramatar	Condition		Unit		
Parameter	Condition	Min.	Тур.	Max.	Unit
"H" Input Voltage	Vcc=6.5 to 9.5V	2.6	-	5.5	V
"L" Input Voltage	Vcc=6.5 to 9.5V	0	-	1.1	V

3. Voltage Conditions for Control Signals (BD3883FS)

Parameter	Condition		Unit		
Parameter	Condition	Min.	Тур.	Max.	Unit
"H" Input Voltage	Vcc=6.5 to 9.5V	2.2	-	5.5	V
"L" Input Voltage	Vcc=6.5 to 9.5V	0	-	1.0	V

Control data format list

(BD3403FV)	
Addroop 1	

3403FV) ddress 1							
D10	D11	D12	D13	D14	D15	D16	D17
OUTPUT Volume 1			OUTPUT Volume 2			Function Select	
ddress 2							
D20	D21	D22	D23	D24	D25	D26	D27
	INPUT	Volume		Surround 0:OFF 1:ON	0	Function Select	ct 0
ddress 3							
D30	D31	D32	D33	D34	D35	D36	D37
	Input Selector		6dB SW 0: 0dB 1:+6dB	IN E MUTE 0:OFF 1:ON	0	Function Selec	ct 1
ddress 4		1	1			I	
D40	D41	D42	D43	D44	D45	D46	D47
	Input Ga	ain/Bass		0: Input Gain 1: Bass	1	Function Selec	ct 1
ddress 5							
D50	D51	D52	D53	D54	D55	D56	D57
	Middle	/Treble		0: Middle 1: Treble	Function Select 0 1 1		
3861FS) ddress 1							
D10	D11	D12	D13	D14	D15	D16	D17
OUTPUT Volume			OUTPUT Volume 2			Functic 0	n Select 0
ddress 2							
D20	D21	D22	D23	D24	D25	D26	D27
INPUT Volume					Function Select 0 1		ct 0
ddress 3							
D30	D31	D32	D33	D34	D35	D36	D37
Input Selector 0: 0d			6dBSW 0: 0dB 1:+6dB	IN E MUTE 0:OFF 1:ON	0	Function Selec	ct 1
ddress 4							
D40	D41	D42	D43	D44	D45	D46	D47

D40	D41	D42	D43	D44	D45	D46	D47
	Input Ga	in/Bass		0: Input Gain 1: Bass	1	Function Selec	et 1
Address 5							

Address 5							
D50	D51	D52	D53	D54	D55	D56	D57
	Middle/	Treble		0: Middle 1: Treble	1 0	Function Selec	et 1

Γ

(BD3883FS)

Basic Configuration of Control Data Format

<u>←</u> Data inpι	ut direction									
	MSB									LSB
	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data				D	ata				Select	Address

Control Data Formats

← Data input direction									Select Address	
-	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data(1)	Input Gain I			nput Selector		Treble fc		0	0	
	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data(2)		Fr	ont Volume	e A		Front Volume B *			0	1
	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data(3)		Bass	Gain			Treble Gain				0
	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data(4)		Middle	e Gain		Time Constan t Select	REC OUT	Surroun d	Rear Volume	1	1

O* indicates 0 or 1.

• By changing the setting of Select Address, four different control formats are selectable. (BD3883FS)

• At power-on sequence, initialize all data.

Example:

← Data input direction										
MSB LS	B N	ISB	LS	B M	SB	LSI	3 N	1SB	LS	SB
Data(1)	L	Data(2)		L	Data(3)		L	Data(4)		L
"L" means a "latch."										

· After power-on, for the second and subsequent times, only the necessary data can be selected for setting.

Example: When changing the volume:

\leftarrow Data input direction									
MSB	MSB LSB								
Data(2	Data(2) L								
"L" means a "latch."									

• RECOUT, Surround and Rear Volume in Data(4) are Soft-switched using time constants. (BD3883FS)

•Block diagram, application circuit, pin assignment

(BD3403FV)



UNIT RESISTANCE : Ω CAPACITANCE : F

Fig.2

(BD3861FS)



UNIT RESISTANCE : Ω CAPACITANCE : F

Fig.3

⁽BD3883FS) When using RECOUT:



UNIT RESISTANCE: Ω CAPACITOR: F

Fig.4

When using 2ndHPF:



RESISTANCE: Ω CAPACITOR:F

Fig.5

⁽BD3883FS)

Reference data



Fig.6 Circuit Current – Supply Voltage (BD3403FV)



Fig.9 Output Voltage - Input Voltage





Fig.15 Variable Treble Cut-off Frequency (BD3883FS)



Fig.7Circuit Current – Supply Voltage (BD3883FS)



Fig.10 Total Harmonic Distortion ratio -Output Voltage (BD3403FV, BD3861FS)



(BD3403FV, BD3861FS)







Fig.8 Voltage Gain - Frequency



Fig.11Total Harmonic Distortion ratio -Output Voltage (BD3883FS)



Fig.14 Treble Gain - Frequency (BD3403FV, BD3861FS)

Notes for use

- 1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- 2) Although ROHM is confident that the example application circuit reflects the best possible recommendations, be sure to verify circuit characteristics for your particular application. Modification of constants for other externally connected circuits may cause variations in both static and transient characteristics for external components as well as this Rohm IC. Allow for sufficient margins when determining circuit constants.
- 3) Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings, such as the applied voltage or operating temperature range (Topr), may result in IC damage. Assumptions should not be made regarding the state of the IC (short mode or open mode) when such damage is suffered. A physical safety measure, such as a fuse, should be implemented when using the IC at times where the absolute maximum ratings may be exceeded.

4) GND potential

Ensure a minimum GND pin potential in all operating conditions. Make sure that no pins are at a voltage below the GND at any time, regardless of whether it is a transient signal or not.

5) Thermal design

Perform thermal design, in which there are adequate margins, by taking into account the permissible dissipation (Pd) in actual states of use.

- 6) Short circuit between terminals and erroneous mounting Pay attention to the assembly direction of the ICs. Wrong mounting direction or shorts between terminals, GND, or other components on the circuits, can damage the IC.
- Operation in strong electromagnetic field Using the ICs in a strong electromagnetic field can cause operation malfunction.
- 8) 2-wire serial control

Because SC and SI terminals are designed for inputting high-frequency digital signals, wiring and layout patterns should be routed as not to cause interference with the analog-signal-related lines.

- 9) E Input external resistance (BD3883FS) To avoid a sudden noise into E Input, external resistance (4.7kΩ) should be connected as close as possible to the IC terminal.
- 10) Function switching

Action to absorb shock sounds is taken when switching between the Volume, Treble, Middle and Bass functions.

11) Power-ON Reset (BD3883FS)

A built-in circuit for performing initialization inside the IC at Power-ON is provided. Specifically, the initial states are set as described in the table below. In the case of the setting design, however, to be on the safe side, it is recommended that data shall be sent to all the addresses as initial data at power-ON and, until this sending operation is completed, MUTE shall be applied. To avoid malfunctions, serial data signals must be set to the Low state at power-ON/OFF.

Function	Initial State
Input Selector	MUTE
Input Gain	0 dB
RECOUT	OFF
Volume	–∞dB
Surround	OFF
Treble	0 dB
Middle	0 dB
Bass	0 dB
Rear Volume	0dB

12) Step switching noise (BD3883FS)

For Surround and Rear Volume, an external capacitor C is attached to the CAP pin to control the switching step noise. In the application circuit, a constant value, as an example, is shown by the CAP pin.

The time constant for charge/discharge of the capacitor C (varying between VBE to 5VBE (2.65V)) controls the slow switching operation.

The switching time constant T is calculated as the follows:

 $T=2.55 \times 10^5 \times C$

VBE has temperature characteristics and may affect the value of the time constant T.

13) Input Selector and Input Gain When changing Input Selector or Input Gain, the Soft-switching is not applied. Therefore, it is recommended to implement the MUTE function.



14) Constraints of serial control (BD3883FS)

On Soft-switching of the RECOUT, Surround, and Rear Volume functions, data must not be sent serially to the functions before the switching operation is completed.

If the function for Soft-switch should serially send the data (Data(4)) on the same Select Address, the time interval between the send operations must be set to 500 -600msec.



15) Function setting while muting Volume (BD3883FS)

While muting Volume, to avoid increasing residual noise, set Bass, Middle and Treble to 0dB, Surround to OFF, and Rear Volume to -10dB.

Ordering part number



SSOP-B40



SSOP-A32



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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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