



Power Supply Selector Switch IC for SD Cards



VIN1=2.7 to 4.5V

VIN2=1.2 to 2.4V 120mΩ(Typ.)

> 25µA(Typ.) 0.01µA(Typ.)

-40 to +85°C

Datasheet

BD2204GUL

Description

BD2204GUL is high side switch IC that has built-in 2 circuits of MOSFET. Switch has achieved 120m Ω (Typ.) on-resistance. 3.3V power supply and 1.8V power supply for memory card can be selected by SEL terminal. Moreover, it has built-in simultaneous-on prevention function at power switching, reverse-current protection function to prevent reverse-current from output terminal to input terminal at power-off, and discharge circuit to discharge electricity in output terminal.

Features

- Dual channel of low on resistance (Typ. = 120mΩ)
 N-channel MOSFET built in
- 3.3V and 1.8V are chosen and an output is possible.
- 0.5A Continuous Current load
- Reverse-current protection when power switch off
- Prevent VIN1 and VIN2 from simultaneous-on.
- Output Discharge Circuit
- Thermal Shutdown
- Active-High Control Logic
- VCSP50L1 package

Applications

Digital cameras Digital video camera SD cards slot

Typical Application Circuit

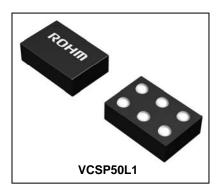
Key Specifications

Input voltage range:

■ON resistance:
Operating current:
Standby current:

■Operating temperature range:

Package VCSP50L1 W(Typ.) D(Typ.) H (Max.) 1.50mm x 1.00mm x 0.55mm



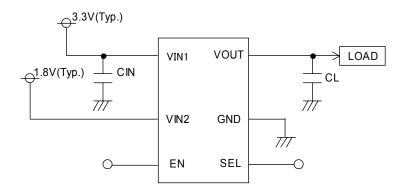


Figure 1. Typical application circuit

OProduct structure : Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays

Block Diagram

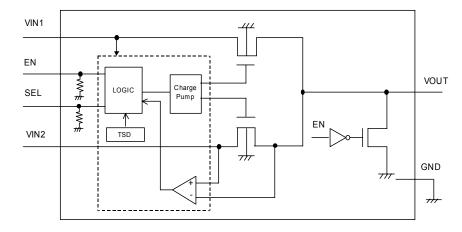


Figure 2. Block Diagram

●Pin Configuration

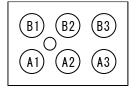


Figure 3. Pin Configuration (BOTTOM VIEW)

Pin Descriptions

Pin No.	Symbol	1/0	Pin function
A1	VIN1	I	Switch1 input and supply voltage for IC
A2	VIN2	I	Switch2 input
A3	EN	I	Active-high enable input with pull-down resistance (Typ.700Ω)
B1	VOUT	0	Switch output
B2	GND	-	Ground
В3	SEL	I	Output selector input with pull-down resistance (Typ.700Ω) As SEL=L, Vout=3.3V output, as SEL=H, Vout=1.8V output

• Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Switch1 input voltage	VIN1	-0.3 to 6.0	V
Switch2 input voltage	VIN2	-0.3 to 6.0	V
EN voltage	VEN	-0.3 to 6.0	V
SEL voltage	VSEL	-0.3 to 6.0	V
VOUT voltage	Vout	-0.3 to 6.0	V
Output current	Ιουτ	1.0	А
Storage temperature	Tstg	-55 to 150	°C
Power dissipation	Pd	575 ^{*1}	mW

*1 In the case of exceeding Ta = 25° C, 4.6mW should be reduced per 1° C.

Recommended Operating Ratings

Parameter	Symbol		Unit			
Falameter	Symbol	Min.	Тур.	Max.	Unit	
Switch1 input voltage	VIN1	2.7	3.3	4.5	V	
Switch2 input voltage	Vin2	1.2	1.8	2.4	V	
Operating temperature	TOPR	-40	25	85	°C	
Output current	Ιουτ	-	-	0.5	А	

• Electrical Characteristics

(VIN1= 3.3V, VIN2= 1.8V, Ta= 25°C, unless otherwise specified.)

Parameter	Symbol	Limits		Unit	Condition		
Parameter	Symbol	Min.	Тур.	Max.	Unit		
Operating current1	IDD1	-	30	45	μA	VEN = 1.2V, VSEL = 0V VOUT = OPEN	
Operating current2	IDD2	-	35	52.5	μA	VEN = VSEL = 1.2V VOUT = OPEN	
Standby current	ISTB	-	0.01	1	μA	VEN = 0V, VOUT = OPEN	
EN, SEL input voltage	Venh Vselh	1.2	-	-	V	High input	
EN, SEE Input voltage	Venl Vsell	-	-	0.4	V	Low input	
EN, SEL input H current	Ienh Iselh	2.3	4.7	11.0	μA	VEN = VSEL = 3.3V with pull-down resistance	
EN, SEL input L current	IENL ISELL	-1.0	-	1.0	μA	VEN = VSEL = 0V	
Pull-down resistance	Rpd	0.3	0.7	1.4	MΩ	Input PIN pull-down resistance	
On-resistance1	Ron1	-	120	200 *2	mΩ	IOUT = 500mA	
On-resistance2	Ron2	-	120	200 *2	mΩ	IOUT = 500mA	
Switch leakage current	ILEAK	-	0.01	1	μA	VEN = 0V, VOUT = 0V	
Output rise time1	Ton1	-	60	300	μs	SEL = L, RL = 10Ω Vout : 10% → 90%	
Output fall time1	TOFF1	-	0.1	1	μs	SEL = L, RL = 10Ω VOUT : 90% → 10%	
Output fall time1DISC	TOFF1D	-	300	1000	μs	EN = SEL = L, CL = 1 μ F Vout : 90% \rightarrow 10% SEL = H, RL = 10 Ω	
Output rise time2	Ton2	-	30	150	μs	Vout : 10% → 90%	
Output fall time2	TOFF2	-	0.1	1	μs	SEL = H, RL = 10Ω Vout : $90\% \rightarrow 10\%$	
Output fall time2DISC	Toff2D	-	220	1000	μs	EN = L, SEL = H, CL = 1μ F Vout : $90\% \rightarrow 10\%$	
Discharge on-resistance	RDISC	-	80	150	Ω	IOUT = -1mA, VEN = 0V	
Discharge current	IDISC	-	10	15	mA	Vout = 3.3V, Ven = 0V	
VOUT drop voltage ^{*3}	Voutdrop1	-	-	0.4	V	$CL = 15\mu$ F, IOUT = 500mA VOUT = VIN1 \rightarrow VIN2	
	Voutdrop2	-	-	0.4	V	CL = 15µF, Iout = 500mA Vout = VIN2→VIN1	

^{*2} Not 100% tested at the time of shipment.

^{*3} When the switch changes from VIN1 to VIN2 or from VIN2 to VIN1, it is possible that VOUT voltage drops. Dropped voltage of VOUT is specified as Voutdrop1 and Voutdrop2.

That voltage drop is caused by the function which prevents VIN1 and VIN2 from turning on simultaneously. This function generates the period which both VIN1 and VIN2 are turned off, and prevents the penetration current between VIN1 and VIN2.

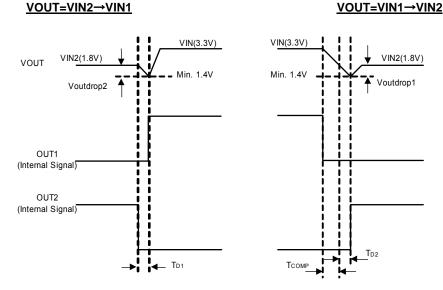


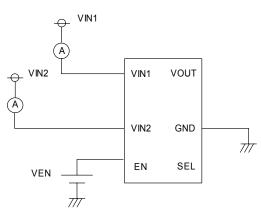
Figure 4. Vout drop voltage

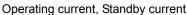
*TD1 and TD2 + TCOMP are period of Simultaneous-Off.

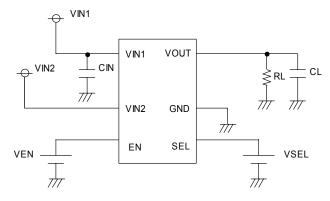
*TCOMP is period of VOUT becoming same voltage as VIN2.

*The value of Min. is in condition of Iout=500mA and CL=15uF.

Measurement Circuit







EN, SEL input voltage, Output rise, fall time

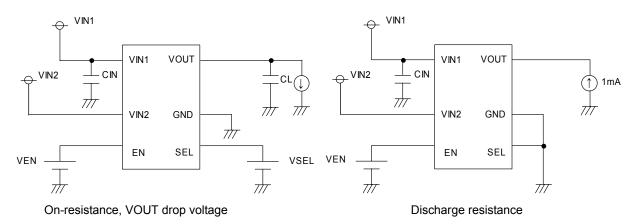
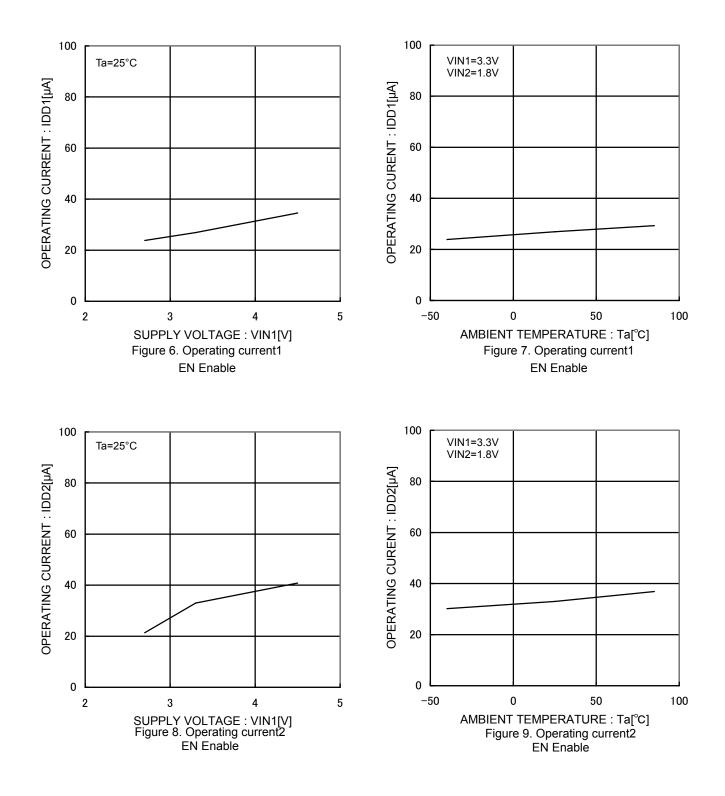
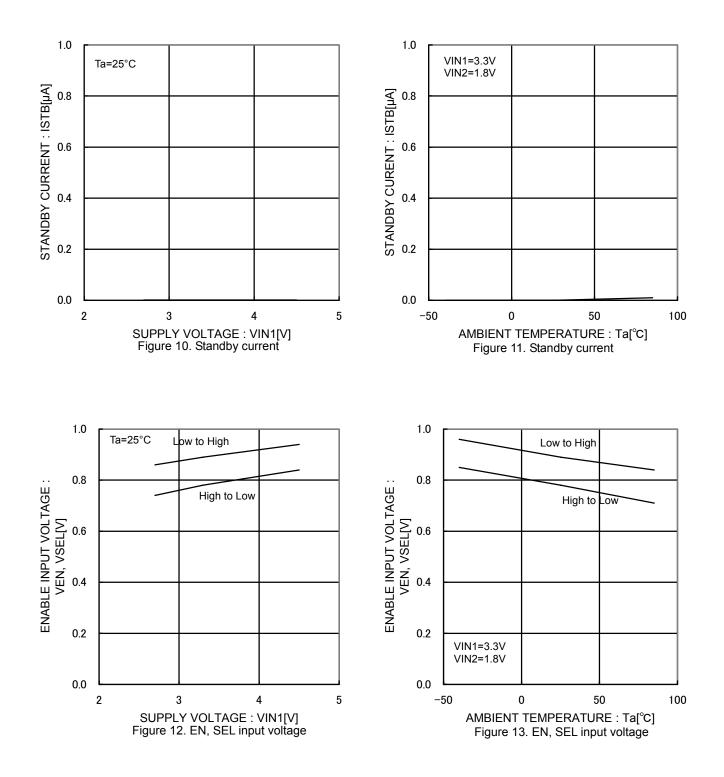


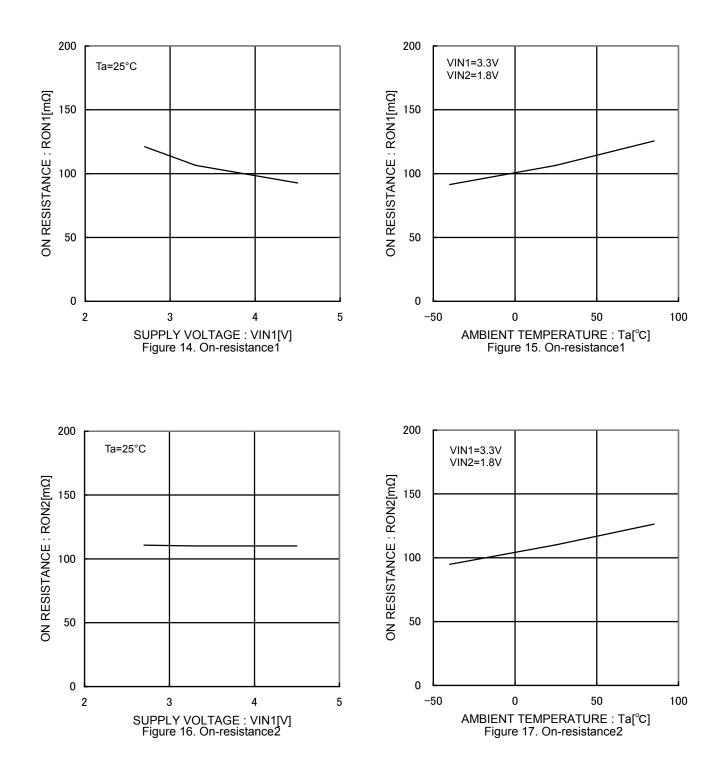
Figure 5. Measurement circuit

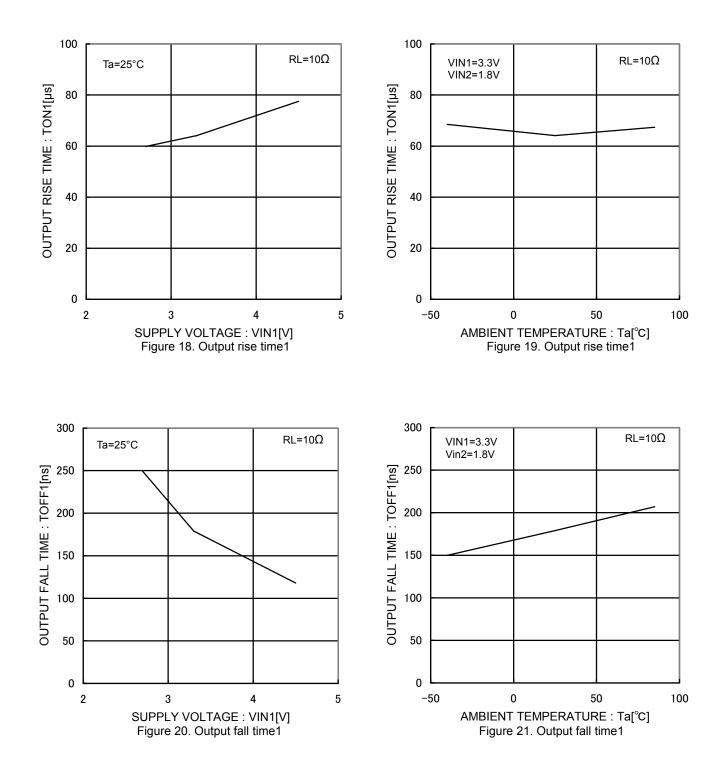
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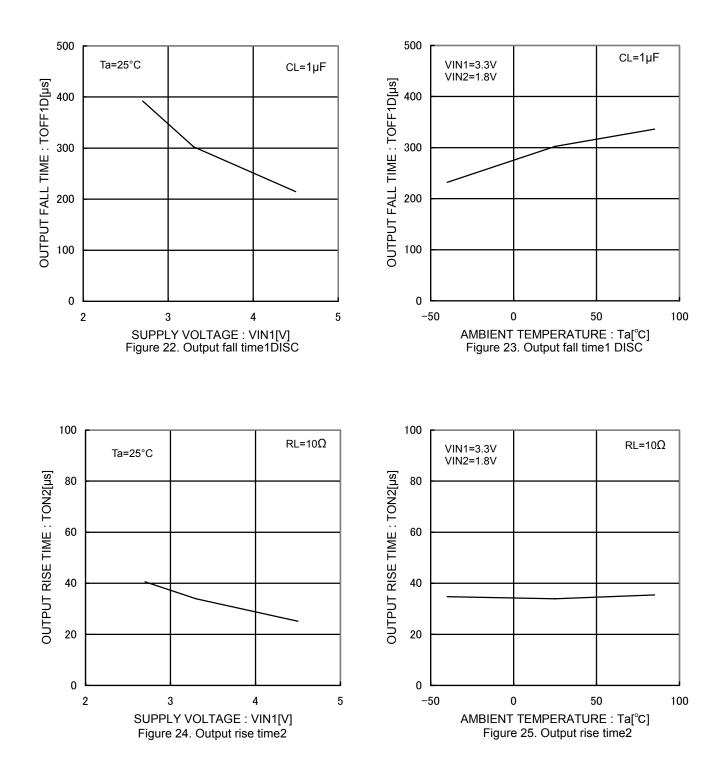
Typical Performance Curves

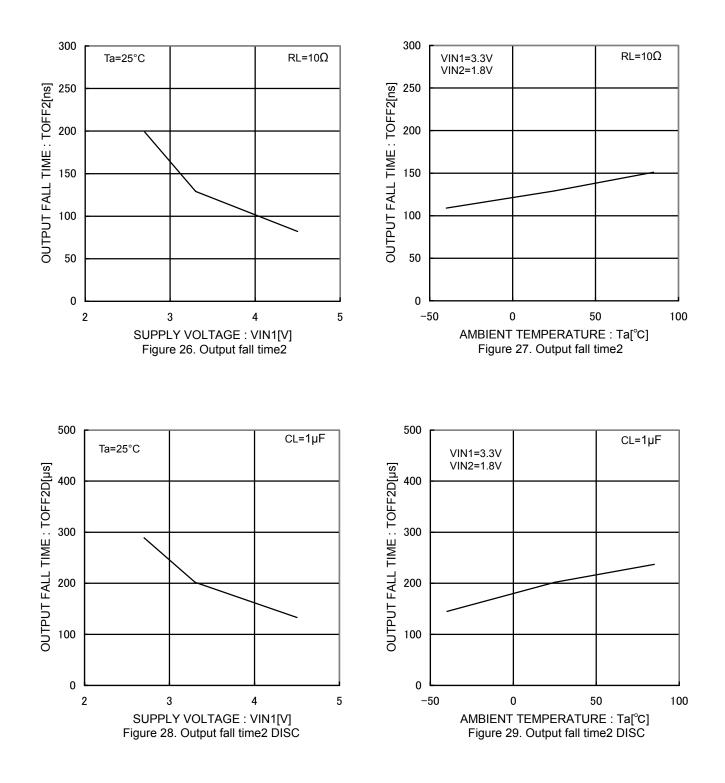


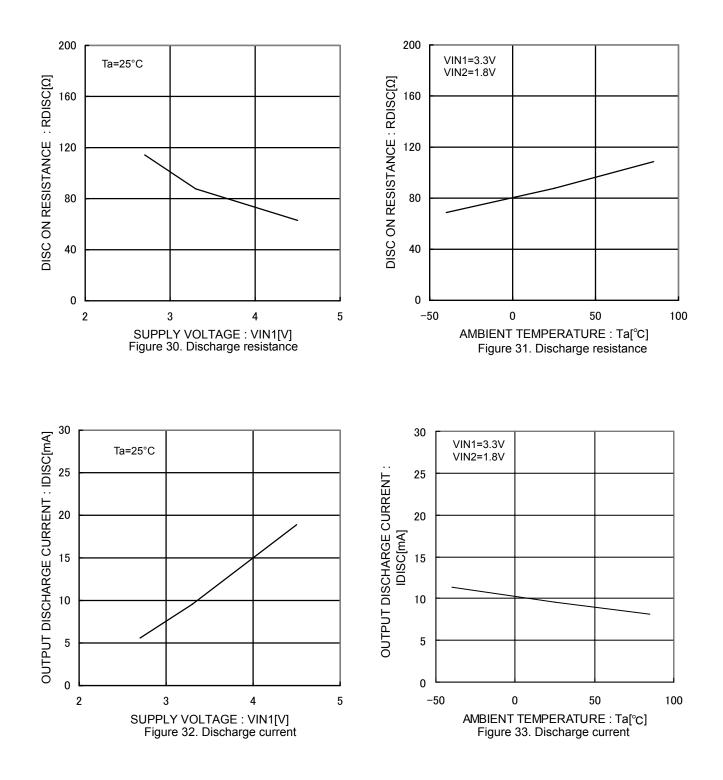


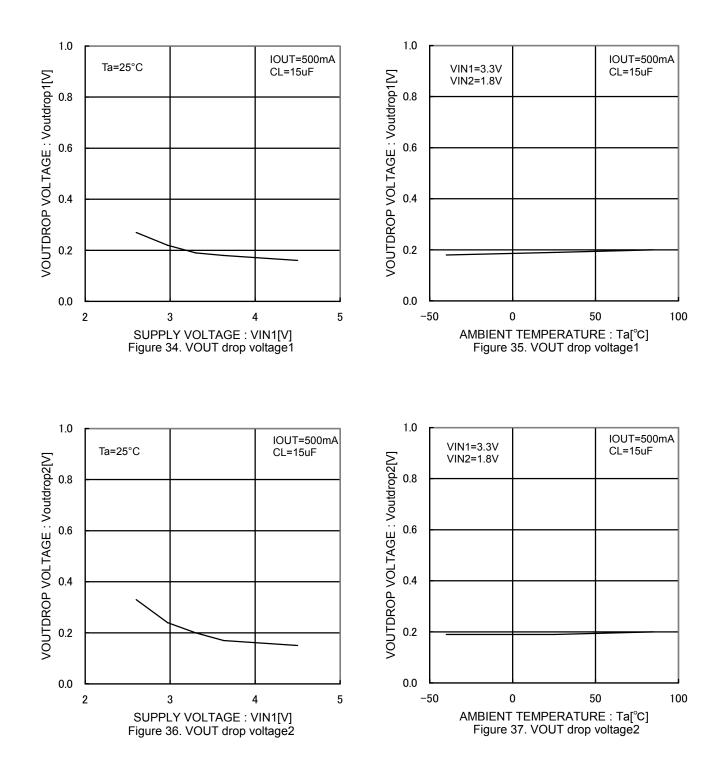


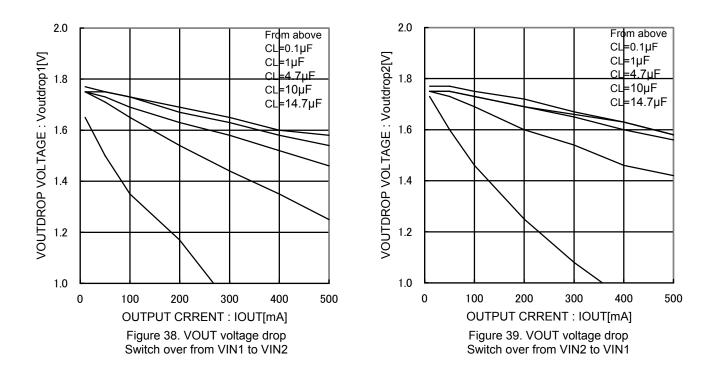




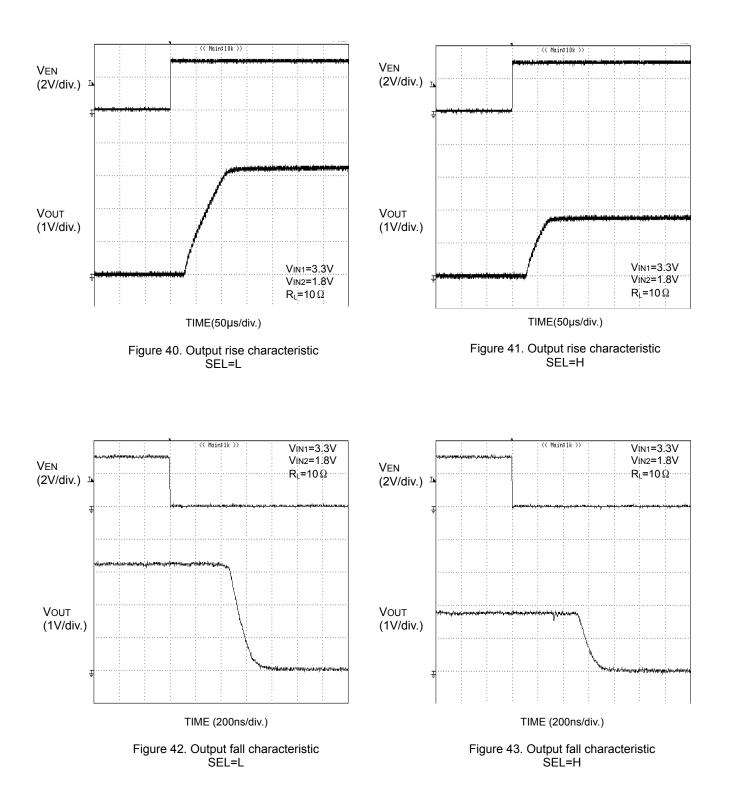








Typical Wave Forms



Typical Wave Forms - continued

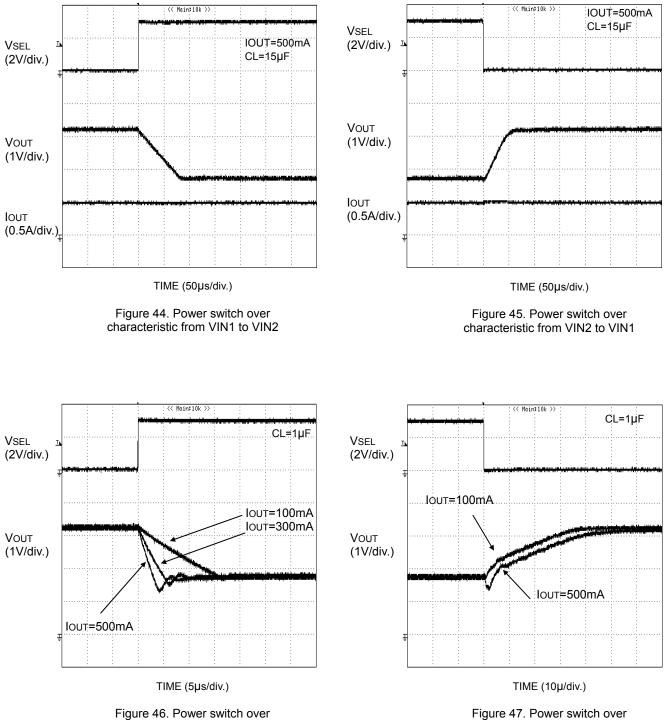
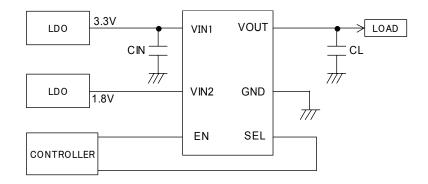


Figure 47. Power switch over characteristic from VIN2 to VIN1

characteristic from VIN1 to VIN2

Application Example





Application Information

When ringing occurs power source line to IC, and may cause bad influences upon IC actions. In order to avoid this case, connect a bypath capacitor by VIN1 terminal and GND terminal of IC, 0.1µF or higher is recommended.

The switch over time for VOUT drop voltage and power at power switch over varies depending on the load current (IOUT) and the load capacity (CL) of output. Please decide load capacity (CL) suited to load current (IOUT).

This system connection diagram doesn't guarantee operating as the application.

The external circuit constant and so on is changed and it uses, in which there are adequate margins by taking into account external parts or dispersion of IC including not only static characteristics but also transient characteristics.

Functional Description

1. Switch operation

VIN1 terminal, VIN2 terminal and VOUT terminal are connected to the drain and the source of switch MOSFET respectively. And the VIN1 terminal is used also as power source input to internal control circuit.

When the switch is turned on from EN control input at SEL=L (SEL=H) input, VIN1 (VIN2) terminal and VOUT terminal are connected by a 120m Ω switch. In on status, the switch is bi-directional. Therefore, when the potential of VOUT terminal is higher than that of VIN1 (VIN2) terminal, current flows from VOUT terminal to VIN1 (VIN2) terminal.

Since a parasitic diode between the drain and the source of switch MOSFET is canceled, in the off status, it is possible to prevent current from flowing reversely from VOUT to VIN1 (VIN2).

2. Switch over operation

When H is input to SEL terminal while VIN1 voltage has been output to VOUT terminal, VIN2 voltage is output to VOUT terminal to prevent current from flowing reversely after detecting that VOUT terminal gets lower than VIN2 voltage. When L is input to SEL terminal while VIN2 voltage has been output to VOUT terminal, VIN voltage is output to VOUT terminal immediately.

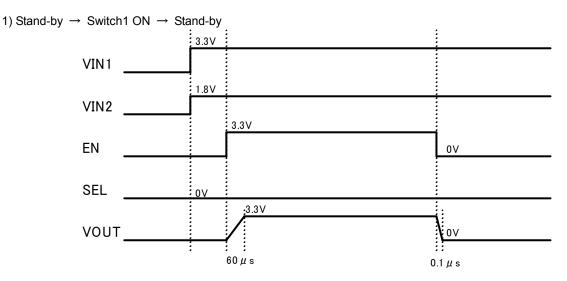
3. Thermal shutdown circuit (TSD)

If over current would continue, the temperature of the IC would increase drastically. If the junction temperature were beyond 135°C (Typ.), thermal shutdown circuit operates and makes power switch turn off. Then, when the junction temperature decreases lower than 115°C (Typ.), power switch is turned on. Unless the fact of the increasing chips temperature is removed or the output of power switch is turned off, this operation repeats. The thermal shutdown circuit operates when the switch is on (EN signal is active).

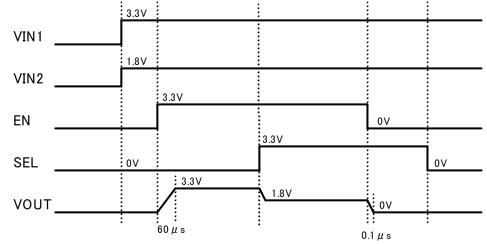
4. Discharge Circuit

Discharge circuit operates when switch off. When discharge circuit operates, $80\Omega(Typ.)$ resistor is connected between VOUT pin and GND pin. This discharges the electrical charge quickly.

Timing Chart



2) Stand-by \rightarrow VIN1 Output (Switch1 ON) \rightarrow VIN2 Output (Switch2 ON) \rightarrow Stand-by



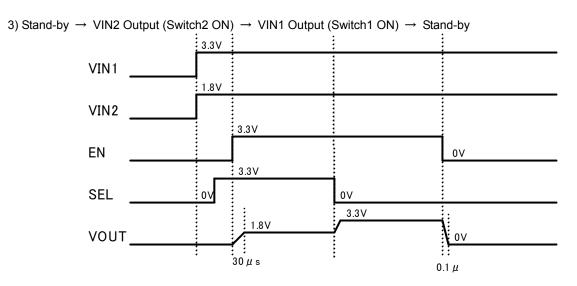
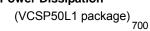
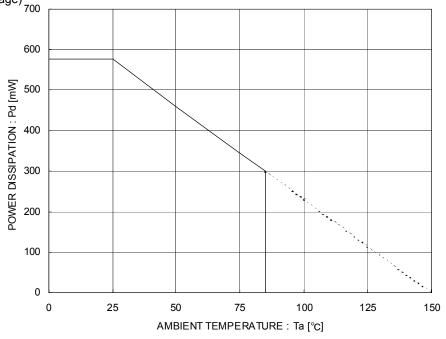
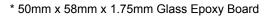


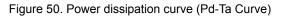
Figure 49. Timing Chart

Power Dissipation









●I/O Equivalence Circuit

Symbol	Pin No	Equivalent circuit
VIN1	A1	to VOUT
VIN2	A2	to VOUT
EN, SEL	A3, B3	C C C C C C C C C C C C C C C C C C C
VOUT	B1	

Operational Notes

(1) Absolute Maximum Ratings

An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.

(2) Operating conditions

These conditions represent a range within which characteristics can be provided approximately as expected. The electrical characteristics are guaranteed under the conditions of each parameter.

(3) Reverse connection of power supply connector

The reverse connection of power supply connector can break down ICs. Take protective measures against the breakdown due to the reverse connection, such as mounting an external diode between the power supply and the IC's power supply terminal.

(4) Power supply line

Design PCB pattern to provide low impedance for the wiring between the power supply and the GND lines. In this regard, for the digital block power supply and the analog block power supply, even though these power supplies has the same level of potential, separate the power supply pattern for the digital block from that for the analog block, thus suppressing the diffraction of digital noises to the analog block power supply resulting from impedance common to the wiring patterns. For the GND line, give consideration to design the patterns in a similar manner.

Furthermore, for all power supply terminals to ICs, mount a capacitor between the power supply and the GND terminal. At the same time, in order to use an electrolytic capacitor, thoroughly check to be sure the characteristics of the capacitor to be used present no problem including the occurrence of capacity dropout at a low temperature, thus determining the constant.

(5) GND voltage

Make setting of the potential of the GND terminal so that it will be maintained at the minimum in any operating state. Furthermore, check to be sure no terminals are at a potential lower than the GND voltage including an actual electric transient.

(6) Short circuit between terminals and erroneous mounting

In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between terminals or between the terminal and the power supply or the GND terminal, the ICs can break down.

(7) Operation in strong electromagnetic field

Be noted that using ICs in the strong electromagnetic field can malfunction them.

(8) Inspection with set PCB

On the inspection with the set PCB, if a capacitor is connected to a low-impedance IC terminal, the IC can suffer stress. Therefore, be sure to discharge from the set PCB by each process. Furthermore, in order to mount or dismount the set PCB to/from the jig for the inspection process, be sure to turn OFF the power supply and then mount the set PCB to the jig. After the completion of the inspection, be sure to turn OFF the power supply and then dismount it from the jig. In addition, for protection against static electricity, establish a ground for the assembly process and pay thorough attention to the transportation and the storage of the set PCB.

(9) Input terminals

In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input terminal. Therefore, pay thorough attention not to handle the input terminals, such as to apply to the input terminals a voltage lower than the GND respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input terminals a voltage lower than the power supply voltage or within the guaranteed value of electrical characteristics.

(10) Ground wiring pattern

If small-signal GND and large-current GND are provided, It will be recommended to separate the large-current GND pattern from the small-signal GND pattern and establish a single ground at the reference point of the set PCB so that resistance to the wiring pattern and voltage fluctuations due to a large current will cause no fluctuations in voltages of the small-signal GND. Pay attention not to cause fluctuations in the GND wiring pattern of external parts as well.

(11) External capacitor

In order to use a ceramic capacitor as the external capacitor, determine the constant with consideration given to a degradation in the nominal capacitance due to DC bias and changes in the capacitance due to temperature, etc.

(12) Thermal shutdown circuit (TSD)

When junction temperatures become detected temperatures or higher, the thermal shutdown circuit operates and turns a switch OFF. The thermal shutdown circuit is aimed at isolating the LSI from thermal runaway as much as possible. Therefore, do not continuously use the LSI with this circuit operating or use the LSI assuming its operation.

(13) Thermal design

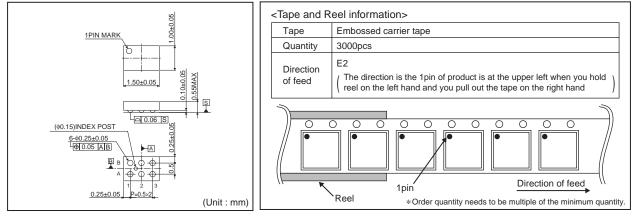
Perform thermal design in which there are adequate margins by taking into account the power dissipation (Pd) in actual states of use.

BD2204GUL

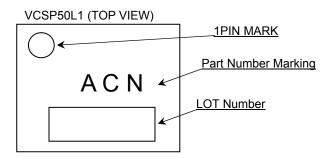


Physical Dimension Tape and Reel Information

VCSP50L1 (BD2204GUL)



Marking Diagram



Status of this document

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority.

Revision History

Date	Revision	Changes
07.Aug.2012	001	New Release

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4) The Products are not subject to radiation-proof design.
- 5) Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6) In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse) is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7) De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8) Confirm that operation temperature is within the specified range described in the product specification.
- 9) ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1) When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2) In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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ООО "ЛайфЭлектроникс"

ИНН 7805602321 КПП 780501001 Р/С 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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