

# **Digital Temperature Sensor IC**

## **BH1900NUX**

#### **General Description**

The BH1900NUX is a digital temperature sensor IC equipped with a two-wire serial interface. It's small package, so it can be placed near the component which temperature is measured.

This device has 3 address pins and it allows to connect 8 devices on one bus.

#### **Features**

- Digital Output: Two-wire Serial Interface.
- 8 Addresses.
- Thermostat Mode.
- Small package.

#### **Applications**

Smart phone, Tablet, LCD TV, notebook PC, portable game machine, and digital camera.

#### **Key Specifications**

■ Supply Voltage Range: 2.7V to 3.6V
■ Temperature Accuracy(-20°C to +85°C): ±3.0°C
■ Operating Current: 75µA (Typ)
■ Shutdown Current: 1µA (Typ)
■ Operating Temperature Range: -30°C to +95°C

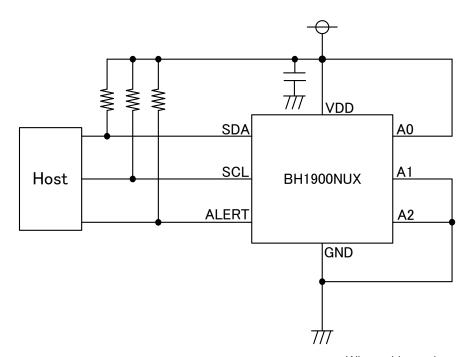
#### Package

VSON008X2030

**W(Typ) x D(Typ) x H(Max)** 2.00mm x 3.00mm x 0.60mm



#### **Typical Application Circuit**

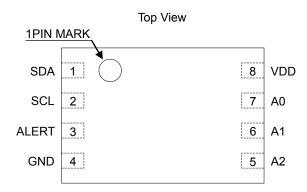


When address pins are A2=L, A1=L, A0=H.

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## **Pin Configuration**

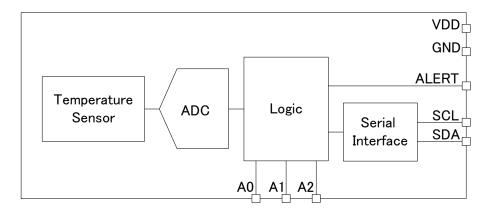


**Pin Description** 

Description	•	
Pin No.	Pin Name	Function
1	SDA	Serial bus data
2	SCL	Serial bus clock
3	ALERT	Alert output
4	GND	Ground
5	A2	Address2
6	A1	Address1
7	A0	Address0
8	VDD	Power supply <sup>(Note 1)</sup>

(Note 1)Dispose a bypass capacitor as close as possible to the IC.

## **Block Diagram**



Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Limits	Units
Supply Voltage	$V_{DD\_MR}$	4.5	V
Input Voltage	V <sub>INMR</sub>	-0.3 to (VDD+0.3) or +4.5 Whichever is less	V
Operating Temperature Range	Topr	-30 to +95	Ô
Storage Temperature	Tstg	-40 to +125	Ô
Maximum Junction Temperature	Tjmax	125	°C

Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

## Thermal Resistance (Note 1)

Parameter	Symbol	Thermal Res	istance (Typ) 2s2p <sup>(Note 4)</sup>	Unit
VSON008X2030		18'	2s2p` *** /	
Junction to Ambient	$\theta_{JA}$	308.3	69.9	°C/W
Junction to Top Characterization Parameter <sup>(Note 2)</sup>	$\Psi_{JT}$	43	10	°C/W

(Note 1)Based on JESD51-2A(Still-Air),
(Note 2)The thermal characterization parameter to report the difference between junction temperature and the temperature at the top center of the outside surface of the component package.
(Note 3)Using a PCB board based on JESD51-3.

(,		
Layer Number of Measurement Board	Material	Board Size
Single	FR-4	114.3mm x 76.2mm x 1.57mmt
Тор		
Copper Pattern	Thickness	

(Note 4)Using a PCB board based on JESD51-7

**Footprints and Traces** 

Layer Number of Measurement Board	Material	Board Size
4 Layers	FR-4	114.3mm x 76.2mm x 1.6mmt

70µm

Тор		2 Internal Laye	ers	Bottom		
Copper Pattern	Thickness Copper Pattern		Thickness	Copper Pattern	Thickness	
Footprints and Traces	70µm	74.2mm x 74.2mm	35µm	74.2mm x 74.2mm	70µm	

Recommended Operating Conditions (Ta= -30°C to +95°C)

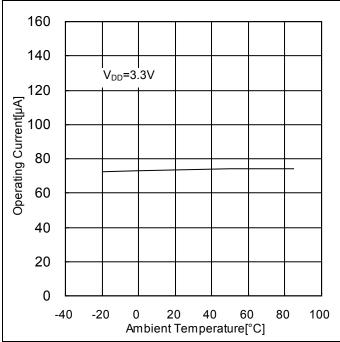
Parameter	Symbol	Min	Тур	Max	Units
Power Supply Voltage	$V_{DD}$	2.7	3.3	3.6	٧

## Electrical Characteristics (Unless otherwise specified V<sub>DD</sub>=3.3V, Ta=25°C)

Parameter	Symbol	Min	Тур	Max	Units	Conditions
Temperature Accuracy	Et	-3.0	-	+3.0	°C	Ta=-20°C to +85°C
Resolution	R <sub>ES</sub>	-	0.0625	-	°C/LSB	
Oscillator Frequency	Fosc	470	750	-	kHz	
Conversion time	T <sub>AD</sub>	-	22	35	ms	
Operating Current	I <sub>DD</sub>	-	75	150	μA	In Non-communication
Shutdown Current	I <sub>SS</sub>	-	1	5	μA	
L Input Voltage (SDA, SCL)	V <sub>IL</sub>	-	-	0.54	V	
H Input Voltage (SDA, SCL)	V <sub>IH</sub>	1.26	-	-	V	
L Input Voltage2 (A0, A1, A2)	V <sub>IL2</sub>	-	-	0.3*V <sub>DD</sub>	V	
H Input Voltage2 (A0, A1, A2)	V <sub>IH2</sub>	0.7*V <sub>DD</sub>	-	-	V	
Input Leakage Current	I <sub>IL</sub>	-10	-	+10	μA	
Digital Output Voltage (SDA, ALERT)	V <sub>OL</sub>	0	-	0.4	V	I <sub>OL</sub> =3mA

Caution: A characteristic of the IC might change heating during reflow soldering. When high temperature accuracy is required, correction should be done after assembly.

## **Typical Performance Curves**



Ambient Temperature[°C]
Figure 1. Operating Current vs Ambient Temperature

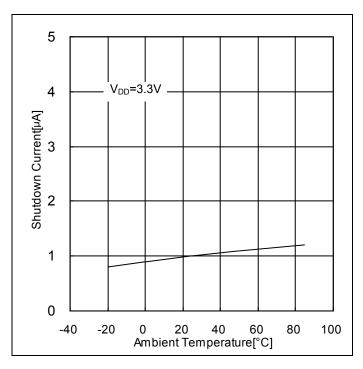


Figure 2. Shutdown Current vs Ambient Temperature

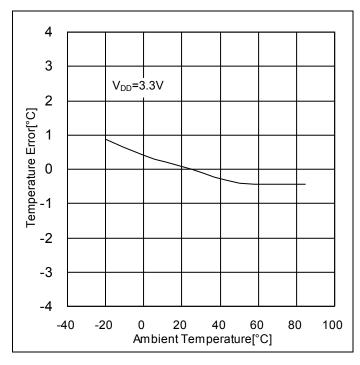
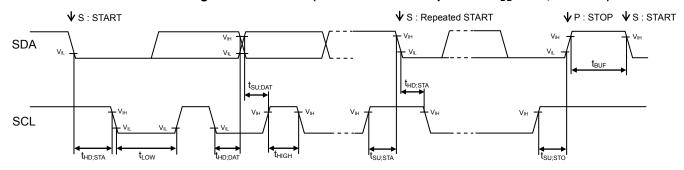


Figure 3. Temperature Error vs Ambient Temperature

## Two-wire Serial Interface Timing Characteristics (Unless otherwise specified V<sub>DD</sub>=3.3V, Ta=25°C)



Parameter	Symbol	Min	Тур	Max	Unit	Conditions
SCL Clock frequency	f <sub>SCL</sub>	0	-	400	kHz	
'L' Period of the SCL Clock	t <sub>LOW</sub>	1.3	-	-	μs	
'H' Period of the SCL Clock	t <sub>HIGH</sub>	0.6	-	-	μs	
Setup Time for Repeated START	t <sub>SU;STA</sub>	0.6	-	-	μs	
Hold Time for START	t <sub>HD;STA</sub>	0.6	-	-	μs	
Data Setup Time	t <sub>SU;DAT</sub>	100	-	-	ns	
Data Hold Time	t <sub>HD;DAT</sub>	0	-	-	μs	
Setup Time for STOP	t <sub>su;sto</sub>	0.6	-	-	μs	
Bus Free Time between STOP and START	t <sub>BUF</sub>	1.3	-	-	μs	

## **Two-wire Serial Communication**

1. Write format

(1) Writing 1Byte data

S	Slave Address	W 0	ACK	Register Address	ACK
	Byte 1 Data	ACK	Р		

(2) Writing 2Byte data

S	Slave Address	W 0	ACK	Register Address		AC	K
	Byte 1 Data	ACK	Byte 2 Data		ACK	Р	

## 2. Read format

(1) Reading 2Byte data after setting register address

S	Slave Address	W 0	ACK	Register Address	ACK
S	Slave Address	R 1	ACK	Byte 1 Data	ACK
	Byte 2 Data	NACK	Р		

(2) Reading data of the addressed register

S	Slave Address	R 1	ACK	Byte 1 Data	ACK
Byte 2 Data		NACK	Р		
	from master to sla	ive		from slave to master	

## 4. Slave Address

The slave address is selectable from 8 addresses by A0, A1, A2 pins.

A2	A1	A0	Slave Address
L	L	L	1001000
L	L	Н	1001001
L	Н	L	1001010
L	Н	Н	1001011
Н	L	L	1001100
Н	L	Н	1001101
Н	Н	L	1001110
Н	Н	Н	1001111

Register Map<sup>(Note 1)</sup>

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Register Address	Register Name	R/W	Byte	D7	D6	D5	D4	D3	D2	D1	D0
0x00	Temperature		1	Temperature Data[11:4]							
UXUU	Register	R	2	Т	emperatur	e Data[3:0	0]	0	0	0	0
0x01	Configuration	RW	1	os	ALERT	0		JLT JE[1:0]	POL	0	SD
0,01	Register	IXVV	2	0	0	0	0	0	0	WT	[1:0]
0.03	T Degister	RW	1	1 T <sub>LOW</sub> Limit[11:4]							
0x02	T <sub>LOW</sub> Register	KVV	2		T <sub>LOW</sub> Li	mit[3:0]		0	0	0	0
0.03	1					T <sub>HIGH</sub> Limit[11:4]					
0x03	0x03 T <sub>HIGH</sub> Register RW		2	T <sub>HIGH</sub> Limit[3:0]			0	0	0	0	
0x04	Software Reset	RW	1	0	0	0	0	0	0	0	SW_ RST

(Note 1) Do not write any commands to other addresses except above. Do not write '1' to the fields in which value is '0' in above table.

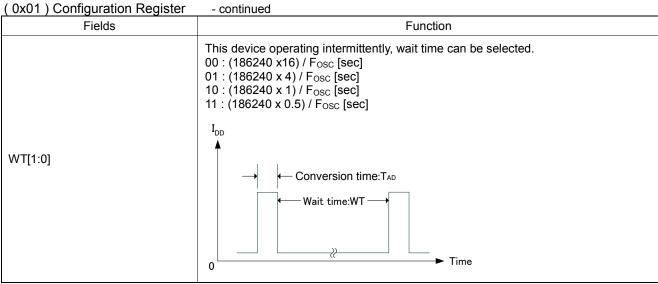
(0x00) Temperature Register

( 0x00 ) Temperature register	
Fields	Function
Temperature Data[11:0]	Measurement Temperature Data Output Register. Negative numbers are represented in binary twos complement format. The Temperature Register is 0x0000 until the first conversion complete after a software reset or power-on. Conversion to temperature value is like below.  Measurement Temperature Value [°C] = Temperature Data [11:0] x 0.0625

default value 0x0000

(0x01) Configuration Register

Fields	Function	
OS	When the device is in shutdown mode, writing '1' to the OS bit starts a single temperature measurement. The device returns to the shutdown state at the completion of the single measurement.  0: Continuous Measurement  1: Single Measurement When reading '0' is read.	
ALERT	The ALERT bit is a read-only register, it provides information of interrupt state. The ALERT bit becomes '0', when Temperature Data or ALERT is read.  0: Interrupt is not active 1: Interrupt is active	
FAULT QUEUE[1:0]	The FAULT QUEUE is number of consecutive times. When consecutive measurement values are out of range, the ALERT pin state changes.  00 : Single 01 : 2times 10 : 4times 11 : 6times	
POL	The POL bit selects the polarity of the ALERT pin output. 0 : Active Low 1 : Active High	
SD	When '1' is written in SD field, it becomes shut down mode after the measurement temperature.  0 : Active 1 : Shut Down	



default value 0x0002

(0x02) T<sub>LOW</sub> Register

Fields	Function				
1 10103	Tunction				
T <sub>LOW</sub> Limit [11:0]	Lower Temperature Limit Setting Register. Negative numbers are represented in binary twos complement format. It has to be set as $T_{LOW}$ Limit $< T_{HIGH}$ Limit. Conversion to $T_{LOW}$ temperature value is like below. Lower Temperature Limit [°C] = $T_{LOW}$ Limit [11:0] x 0.0625				

default value 0x4B00

(0x03) T<sub>HIGH</sub> Register

Fields	Function
T <sub>HIGH</sub> Limit [11:0]	Upper Temperature Limit Setting Register. Negative numbers are represented in binary twos complement format. It has to be set as $T_{HIGH}$ Limit > $T_{LOW}$ Limit. Conversion to $T_{HIGH}$ temperature value is like below. Upper Temperature Limit [°C] = $T_{HIGH}$ Limit [11:0] x 0.0625

default value 0x5000

(0x04) Software Reset

_	exe i j contrare reser						
	Fields	Function					
	SW_RST	Reset process is performed when writing SW_RST='1'. '1' is not written in register SW_RST.					

default value 0xFF

Caution: Read value of Software Reset Register is always 0xFF.

#### Thermostat mode

When the consecutive measurement temperature value exceeds the value in T<sub>HIGH</sub> Limit, the device becomes interrupt and the ALERT pin becomes active. The number of consecutive times is set in FAULT QUEUE. Interrupt is released when any of below action is taken.

- The device becomes shut down mode.
- · Temperature Data or ALERT is read.
- $\bullet \ \text{Measurement temperature value falls below the value in } \ T_{LOW} \ Limit \ consecutively \ over \ same \ times \ as \ FAULT \ QUEUE.$

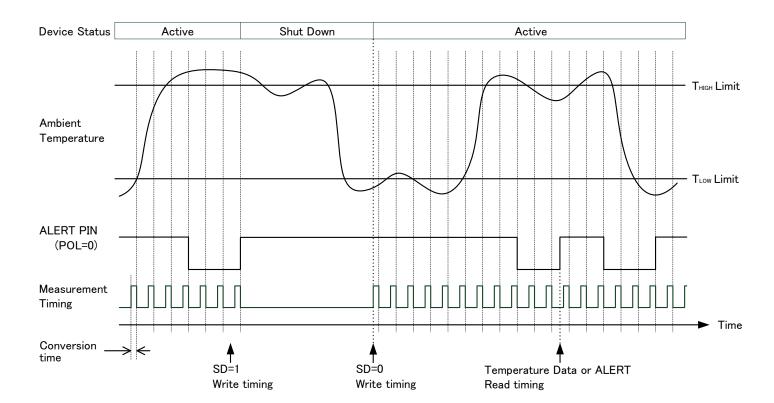
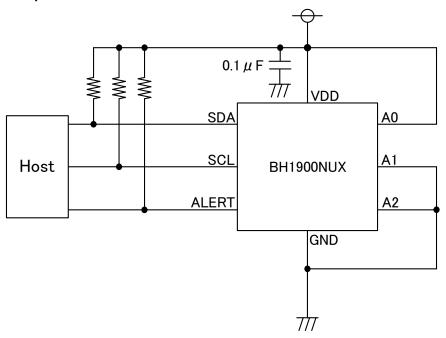


Figure 4. Comparator Mode Action Sequence (Consecutive times = 2times)

## **Application Example**



When address pins are A2=L, A1=L, A0=H.

Caution: Adjust the bypass capacitor value as necessary, according to voltage noise conditions, etc. It is recommended to place bypass capacitance as near the IC as possible.

I/O equivalence circuit

equivalence circuit	T		
Pin Name	Equivalent Circuit	Pin Name	Equivalent Circuit
SDA		ALERT	
SCL		A0 A1 A2	VDD VDD VDD

#### **Operational Notes**

#### 1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

#### 2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

#### 3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

#### 4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

#### 5. Thermal Consideration

Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the maximum junction temperature rating.

#### 6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

#### 7. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

#### 8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

#### 9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

#### 10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

#### 11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

#### **Operational Notes - continued**

#### 12. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode. When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

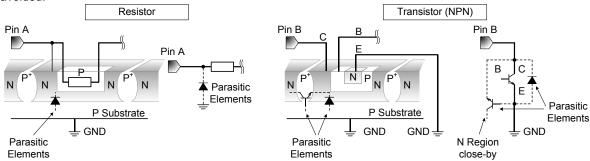


Figure 5. Example of monolithic IC structure

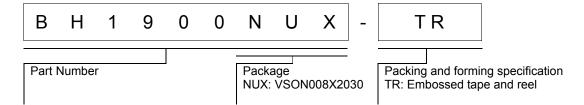
#### 13. Ceramic Capacitor

When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

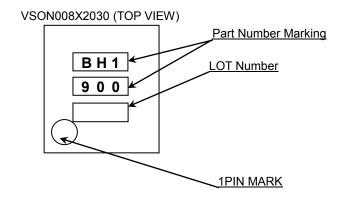
#### 14. Area of Safe Operation (ASO)

Operate the IC such that the output voltage, output current, and the maximum junction temperature rating are all within the Area of Safe Operation (ASO).

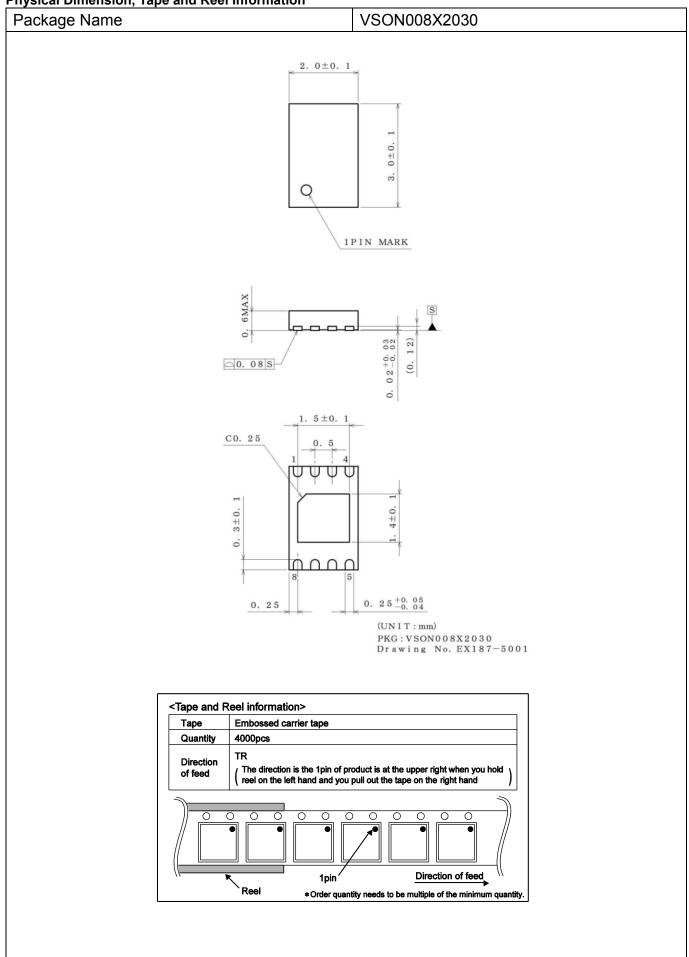
## **Ordering Information**



## **Marking Diagrams**



**Physical Dimension, Tape and Reel Information** 



## **Revision History**

Date	Revision	Changes
26.Apr.2016	001	New Release

## **Notice**

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JÁPAN	USA	EU	CHINA
CLASSⅢ	CLASSIII	CLASS II b	CL ACCIII
CLASSIV		CLASSⅢ	CLASSⅢ

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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OOO «ЛайфЭлектроникс" "LifeElectronics" LLC

ИНН 7805602321 КПП 780501001 P/C 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

#### Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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