

Selection Guide

Part No.	Dice	Lens Type	Iv (mcd) [2] @ 10mA		Viewing Angle [1]
			Min.	Typ.	2θ1/2
WP710A10NT	Pure Orange (GaAsP/GaP)	Orange Transparent	15	40	34°
			*12	*30	

Notes:

1. θ1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.

2. Luminous intensity/ luminous Flux: +/-15%.

*Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Typ.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Pure Orange	607		nm	IF=20mA
λD [1]	Dominant Wavelength	Pure Orange	602		nm	IF=20mA
Δλ1/2	Spectral Line Half-width	Pure Orange	35		nm	IF=20mA
C	Capacitance	Pure Orange	15		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	Pure Orange	2.05	2.5	V	IF=20mA
IR	Reverse Current	Pure Orange		10	uA	VR = 5V

Notes:

1.Wavelength: +/-1nm.

2.Forward Voltage: +/-0.1V.

3.Wavelength value is traceable to the CIE127-2007 compliant national standards.

Absolute Maximum Ratings at TA=25°C

Parameter	Pure Orange	Units
Power dissipation	62.5	mW
DC Forward Current	25	mA
Peak Forward Current [1]	145	mA
Reverse Voltage	5	V
Operating/Storage Temperature	-40°C To +85°C	
Lead Solder Temperature [2]	260°C For 3 Seconds	
Lead Solder Temperature [3]	260°C For 5 Seconds	

Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

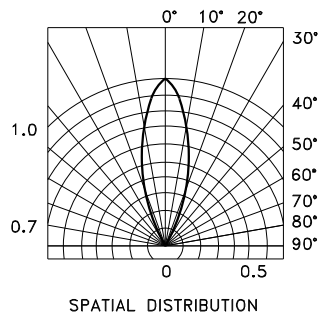
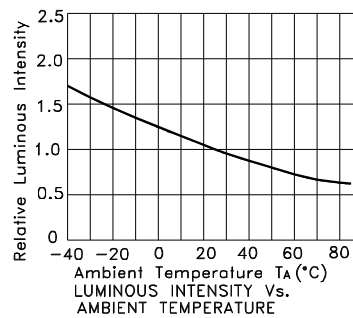
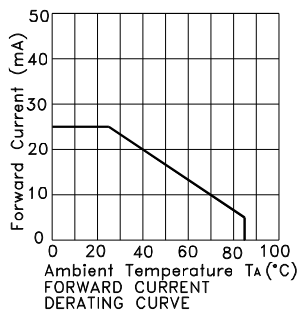
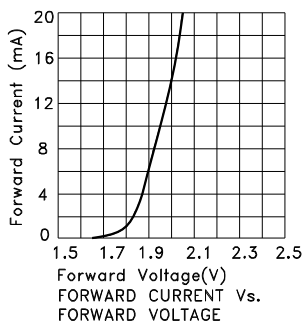
2. 2mm below package base.

3. 5mm below package base.



Pure Orange

WP710A10NT



Kingbright

PACKING & LABEL SPECIFICATIONS

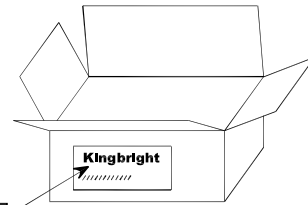
WP710A10NT



56K / 9# BOX

OUTSIDE LABEL

OUTSIDE LABEL



28K / 5# BOX

<h1>Kingbright</h1>	
P/NO: WP710A10xxx	
QTY: 1000 pcs	Q.C. Q C xx xx xxxx PASSED
S/N: XXXX	
CODE: XXX	
LOT NO:	
 <small>XXXXXXXXXXXXXXXXXXXXXXXXXXXX</small>	
RoHS Compliant	

PRECAUTIONS

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)



Fig.1

”O” Correct mounting method ”X” Incorrect mounting method

2. When soldering wire to the LED, use individual heat-shrink tubing to insulate the exposed leads to prevent accidental contact short-circuit. (Fig.2)
3. Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.

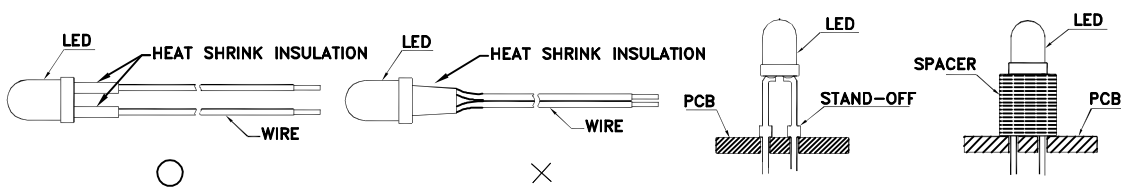


Fig. 2

Fig. 3

Fig. 4

4. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

6. Do not bend the leads more than twice. (Fig. 8)



7. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.



8. The tip of the soldering iron should never touch the lens epoxy.

9. Through-hole LEDs are incompatible with reflow soldering.

10. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.

11. Recommended Wave Soldering Profiles:



Notes:

1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
4. Fixtures should not incur stress on the component when mounting and during soldering process.
5. SAC 305 solder alloy is recommended.
6. No more than one wave soldering pass.

All design applications should refer to Kingbright application notes available at <http://www.KingbrightUSA.com/ApplicationNotes>

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- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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