

## DATA SHEET

# SKY66114-11: 2.4 GHz Front-End Module for Bluetooth® Low Energy /802.15.4/Thread/ZigBee®

## Applications

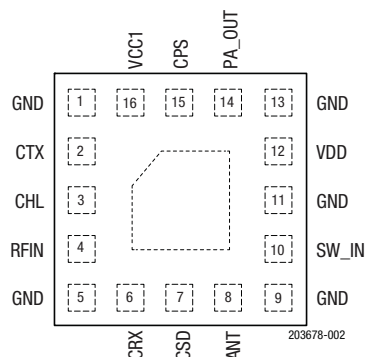
- In-home appliances
- Smart thermostats
- IoT gateways
- Smart lighting
- Sensors
- Range extender

## Features

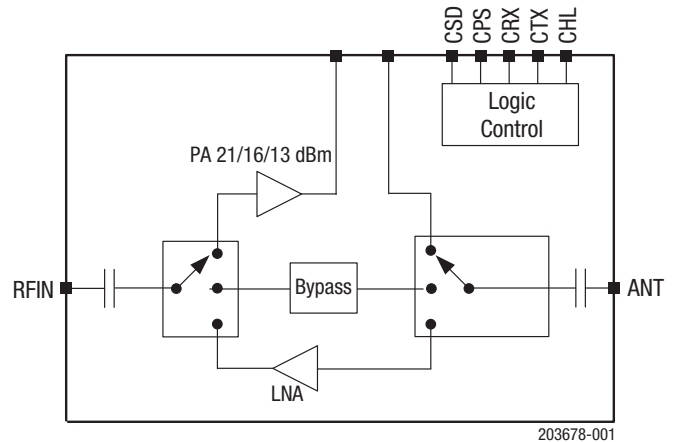
- Integrated PA with up to +23 dBm output power
- Integrated LNA with 12 dB gain
- Integrated switch selects between PA, LNA, and bypass functions
- Low noise figure: 2 dB typical
- Single-ended transmit/receive interface
- Fast switch on/off time: < 800 ns
- Supply range: 1.8 V to 3.6 V
- Sleep mode current: < 1  $\mu$ A typical
- No external bias resistor is required
- Small MCM (16-pin, 2.4 mm  $\times$  2.4 mm  $\times$  0.7 mm) package, NiPdAu-plated (MSL3, 260  $^{\circ}$ C per JEDEC-J-STD-020)



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.



**Figure 2. SKY66114-11 Pinout (Top View)**



**Figure 1. SKY66114-11 Block Diagram**

## Description

The SKY66114-11 is a high-performance, fully integrated RF front-end module (FEM) designed for Bluetooth Low Energy, 802.15.4, Thread, and ZigBee applications.

The SKY66114-11 is designed for ease of use and maximum flexibility. The device integrates a high-efficiency saturated PA, high-gain LNA, T/R and bypass switches, and digital controls compatible with 1.6 V to 3.6 V CMOS levels.

The RF blocks operate over a wide supply voltage range from 1.8 V to 3.6 V that allows the SKY66114-11 to be used in battery powered applications over a wide spectrum of the battery discharge curve.

A functional block diagram is shown in Figure 1. The SKY66114-11 is provided in a small, 16-pin, 2.4  $\times$  2.4 mm Multi-Chip Module (MCM) package. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

**Table 1. SKY66114-11 Signal Descriptions<sup>1</sup>**

| Pin | Name | Description   | Pin | Name   | Description   |
|-----|------|---|-----|--------|---|
| 1   | GND  | Ground  | 9   | GND    | Ground  |
| 2   | CTX  | Connect to GPIO signal for mode control (see Table 6) | 10  | SW_IN  | Transmit arm of T/R switch, nominal impedance 50 $\Omega$                           |
| 3   | CHL  | Connect to GPIO signal for mode control (see Table 6) | 11  | GND    | Ground  |
| 4   | RFIN | RF input power, connect to 50 $\Omega$ RF source      | 12  | VDD    | Connect to positive supply  |
| 5   | GND  | Ground  | 13  | GND    | Ground  |
| 6   | CRX  | Connect to GPIO signal for mode control (see Table 6) | 14  | PA_OUT | Output of final PA stage; connect to output matching network (OMN) and VCC2 supply. |
| 7   | CSD  | Connect to GPIO signal for mode control (see Table 6) | 15  | CPS    | Connect to GPIO signal for mode control (see Table 6)                               |
| 8   | ANT  | Connect to 50 $\Omega$ antenna                        | 16  | VCC1   | Connect to positive supply  |

<sup>1</sup> The paddle should be connected to ground.

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY66114-11 are provided in Table 2. The recommended operating conditions are specified in Table 3, and electrical specifications are provided in Tables 4 and 5.

The state of the SKY66114-11 is determined by the logic provided in Table 6.

**Table 2. SKY66114-11 Absolute Maximum Ratings<sup>1</sup>**

| Parameter   | Symbol      | Minimum | Maximum | Units |
|---|-------------|---------|---------|-------|
| Supply voltage  | VCC1        | -0.3    | +3.6    | V     |
|   | VCC2        | -0.3    | +3.6    | V     |
|   | VDD         | -0.3    | +3.6    | V     |
| Control pin voltages  | VCTL        | -0.3    | +3.6    | V     |
| Transmit output power at ANT port into 50 $\Omega$ load     | POUT_TX_MAX |         | +22.5   | dBm   |
| Input power at RFIN port                                    | PIN_TR_MAX  |         | +5      | dBm   |
| Receive input power at ANT port <sup>2</sup>                | PIN_RX_MAX  |         | +15     | dBm   |
| Bypass input power at ANT port <sup>2</sup>                 | PIN_BYP_MAX |         | +20     | dBm   |
| Operating temperature                                       | TA          | -40     | +85     | °C    |
| Storage temperature   | TSTG        | -40     | +85     | °C    |
| Electrostatic discharge (Human Body Model (HBM), Class 1C): | ESD         |         | 500     | V     |
|   |             |         | 2000    | V     |

<sup>1</sup> Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

<sup>2</sup> CW test signal.

**ESD HANDLING:** Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.

**Table 3. Recommended Operating Conditions**

| Parameter                    | Symbol | Min              | Typ | Max | Units |
|------------------------------|--------|------------------|-----|-----|-------|
| Supply voltage on VCC1 pin   | VCC1   | 1.7              | 3.0 | 3.6 | V     |
| Supply voltage on PA_OUT pin | VCC2   | 1.2              | 3.0 | 3.6 | V     |
| Supply voltage on VDD pin    | VDD    | 1.7 <sup>1</sup> | 3.0 | 3.6 | V     |
| Operating temperature        | TA     | -40              | +25 | +85 | °C    |

<sup>1</sup> Performance at VDD < 2.5 V will be slightly degraded compared to VDD = 2.5 V or more.

**Table 4. SKY66114-11 Electrical Specifications<sup>1</sup>**  
**(VCC1 = 1.8 V, VCC2 = 3.0 V, VDD = 3.0 V, TA = +25 °C, Unless Otherwise Noted)**

| Parameter                            | Symbol  | Test Condition   | Min | Typ                   | Max | Units                |
|--------------------------------------|---------|--|-----|-----------------------|-----|----------------------|
| <b>DC Characteristics</b>            |         |  |     |                       |     |                      |
| Total supply current (transmit mode) | ICC_TX  | POUT = +22 dBm <sup>2</sup><br>POUT = +20 dBm <sup>3</sup><br>POUT = +16 dBm <sup>4</sup><br>POUT = +13 dBm <sup>5</sup> |     | 125<br>90<br>60<br>45 |     | mA<br>mA<br>mA<br>mA |
| Total supply current (receive mode)  | ICC_RX  |  |     | 4                     | 6   | mA                   |
| Total supply current (bypass mode)   | ICC_BYP |  |     | 65                    |     | μA                   |
| Sleep supply current                 | ICC_OFF | No RF  |     |                       | 1   | μA                   |
| Quiescent current                    | ICCQ_TX | High-power mode <sup>2</sup><br>Low-power mode <sup>3</sup><br>Low-power mode <sup>4</sup>                               |     | 20<br>15<br>15        |     | mA<br>mA<br>mA       |
| <b>Logic Characteristics</b>         |         |  |     |                       |     |                      |
| Control voltage:                     |         |  |     |                       |     |                      |
| High                                 | VIH     |  | 1.6 |                       | VDD | V                    |
| Low                                  | VIL     |  | 0   |                       | 0.3 | V                    |
| Control current:                     |         |  |     |                       |     |                      |
| High                                 | IiH     |  |     |                       | 1.0 | μA                   |
| Low                                  | IiL     |  |     |                       | 1.0 | μA                   |

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

<sup>2</sup> VCC1 = 1.8 V, VCC2 = 3.0 V, PIN = -1 dBm.

<sup>3</sup> VCC1 = 1.8 V, VCC2 = 3.0 V, PIN = -4 dBm.

<sup>4</sup> VCC1 = 1.8 V, VCC2 = 1.8 V, PIN = -3 dBm.

<sup>5</sup> VCC1 = 1.8 V, VCC2 = 1.2 V, PIN = -4 dBm.

**Table 5. SKY66114-11 Electrical Specifications<sup>1</sup>****(V<sub>CC1</sub> = 1.8 V, V<sub>CC2</sub> = 3.0 V, V<sub>DD</sub> = 3.0 V, T<sub>A</sub> = +25 °C, All Unused Ports Terminated with 50 Ω, Unless Otherwise Noted)**

| Parameter                                     | Symbol            | Test Condition   | Min  | Typ                      | Max  | Units                    |
|---|-------------------|--|--|--------------------------|------|--------------------------|
| Transmit Characteristics                      |                   |  |  |                          |      |                          |
| Frequency range                               | f                 |  | 2400   |                          | 2483 | MHz                      |
| Output power at ANT port                      | P <sub>OUT</sub>  | V <sub>CC1</sub> = 1.8 V, V <sub>CC2</sub> = 3.0 V, P <sub>IN</sub> = -1 dBm<br>V <sub>CC1</sub> = 1.8 V, V <sub>CC2</sub> = 3.0 V, P <sub>IN</sub> = -4 dBm<br>V <sub>CC1</sub> = 1.8 V, V <sub>CC2</sub> = 1.8 V, P <sub>IN</sub> = -3 dBm<br>V <sub>CC1</sub> = 1.8 V, V <sub>CC2</sub> = 1.2 V, P <sub>IN</sub> = -4 dBm |  | +22<br>+20<br>+16<br>+13 |      | dBm<br>dBm<br>dBm<br>dBm |
| Saturated gain, high-power mode               | Gain_SAT          |  |  | 24                       |      | dB                       |
| Saturated output power variation              | ΔP <sub>OUT</sub> | Across all ZigBee channels   |  |                          | 1    | dBp-p                    |
| 2 <sup>nd</sup> and 3 <sup>rd</sup> harmonics | 2fo, 3fo          | P <sub>OUT</sub> = +20 dBm,<br>IEEE 802.15.4 source  |  |                          | -42  | dBm/MHz                  |
| Input return loss                             | S <sub>11</sub>   |  |  | -10                      |      | dB                       |
| Turn-on time                                  | t <sub>RISE</sub> | From 50% of CTX edge to 90% of final<br>RF output power  |  | 1                        |      | us                       |
| Turn-off time                                 | t <sub>FALL</sub> | From 50% of CTX edge to 10% of final<br>RF output power  |  | 100                      |      | ns                       |
| Stability                                     | STAB              | CW, P <sub>in</sub> = 0 dBm, 0.1 GHz to 20 GHz,<br>load VSWR = 6:1   | All non-harmonically related outputs < -42 dBm/MHz |                          |      |                          |
| Ruggedness                                    | RUG               | CW, P <sub>in</sub> = 0 dBm,<br>load VSWR = 10:1   | No permanent damage                                |                          |      |                          |
| Receive Characteristics                       |                   |  |  |                          |      |                          |
| Frequency range                               | f                 |  | 2400   |                          | 2483 | MHz                      |
| Receive gain                                  | Gain_RX           |  |  | 12                       |      | dB                       |
| Receive noise figure                          | NF                |  |  | 2                        |      | dB                       |
| Third order input intercept point             | IIP3              |  |  | 0                        |      | dBm                      |
| 1 dB input compression point                  | IP1dB             |  | -14  | -8                       |      | dBm                      |
| Input return loss                             | S <sub>11</sub>   | ANT port   |  | -10                      |      | dB                       |
| Output return loss                            | S <sub>22</sub>   |  |  | -10                      |      | dB                       |
| Turn-on time                                  | t <sub>RISE</sub> | From 50% of CTX edge to 90% of final<br>RF output power  |  | 2                        |      | μs                       |
| Turn-off time                                 | t <sub>FALL</sub> | From 50% of CTX edge to 10% of final<br>RF output power  |  | 100                      |      | ns                       |
| Bypass Characteristics                        |                   |  |  |                          |      |                          |
| Frequency range                               | f                 |  | 2400   |                          | 2483 | MHz                      |
| Bypass gain                                   | Gain_BYP          |  |  | -1.5                     |      | dB                       |
| Input return loss                             | S <sub>11</sub>   |  |  | -10                      |      | dB                       |
| Output return loss                            | S <sub>22</sub>   |  |  | -10                      |      | dB                       |

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

**Table 6. SKY66114-11 Mode Control Logic<sup>1</sup>****(Vcc1 = 1.8 V, Vcc2 = 3.0 V, VDD = 3.0 V, TA = +25 °C)**

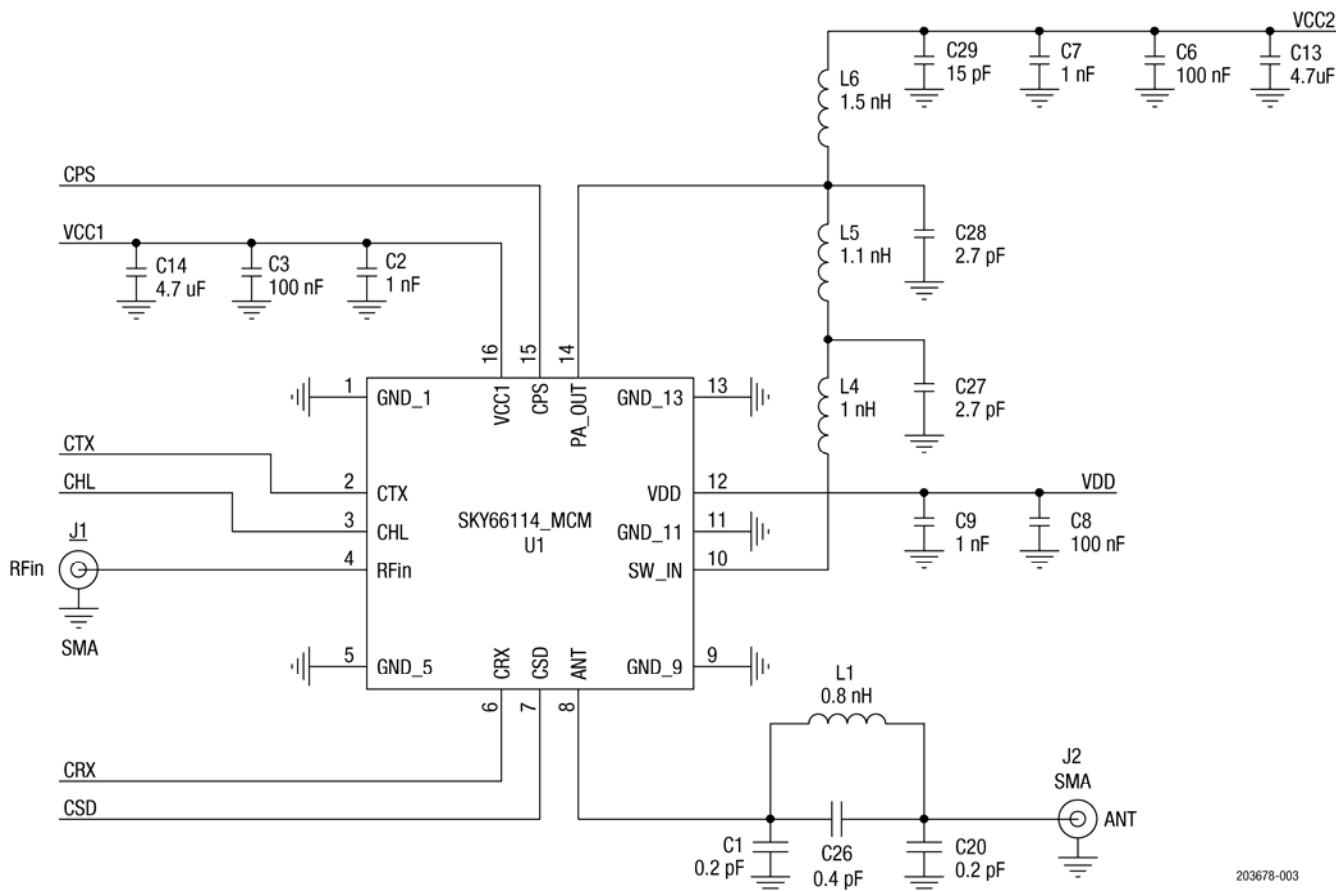
| Mode | Description                       | CSD (Pin 7) | CPS (Pin 15) | CRX (Pin 6) | CTX (Pin 2) | CHL (Pin 3) |
|------|-----------------------------------|-------------|--------------|-------------|-------------|-------------|
| 0    | All off (sleep mode) <sup>1</sup> | 0           | x            | x           | x           | x           |
| 1    | Receive LNA mode                  | 1           | 0            | 1           | 0           | x           |
| 2    | Transmit high-power mode          | 1           | 0            | x           | 1           | 1           |
| 3    | Transmit low-power mode           | 1           | 0            | x           | 1           | 0           |
| 4    | Receive bypass mode               | 1           | 1            | 1           | 0           | x           |
| 5    | Transmit bypass mode              | 1           | 1            | x           | 1           | x           |
| 6    | All off (sleep mode)              | 1           | x            | 0           | 0           | x           |

<sup>1</sup> All controls must be at logic "0" to achieve the specified sleep current.<sup>2</sup> X = Don't care.

## Application Schematic Description

A reference design schematic is provided in Figure 3. An Evaluation Board schematic diagram is shown in Figure 4.

The Evaluation Board Bill of Materials (BOM) is listed in Table 7. The reference layout layers are shown in Figure 5.



203678-003

**Figure 3. SKY66114-11 Reference Design Schematic**

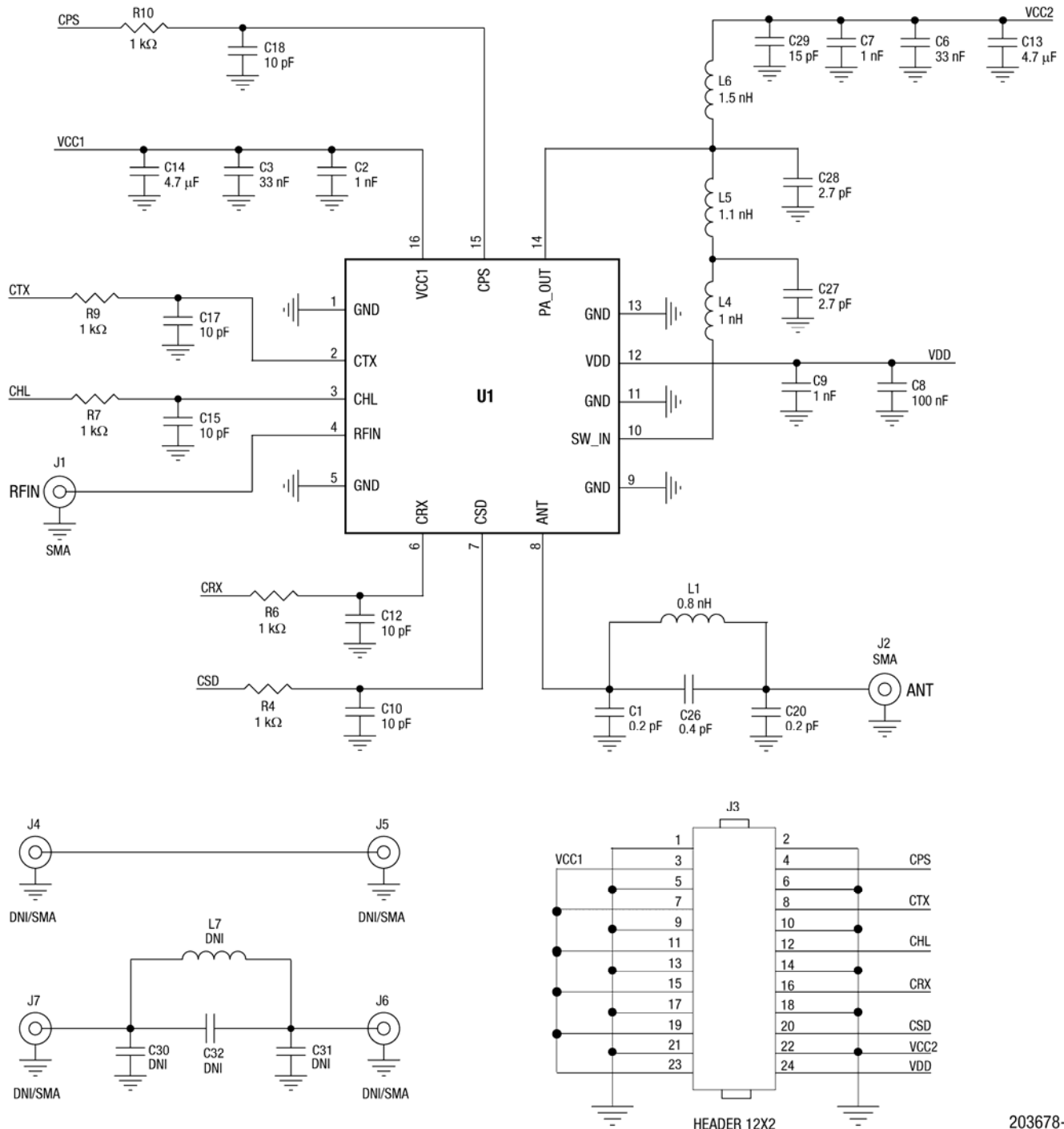


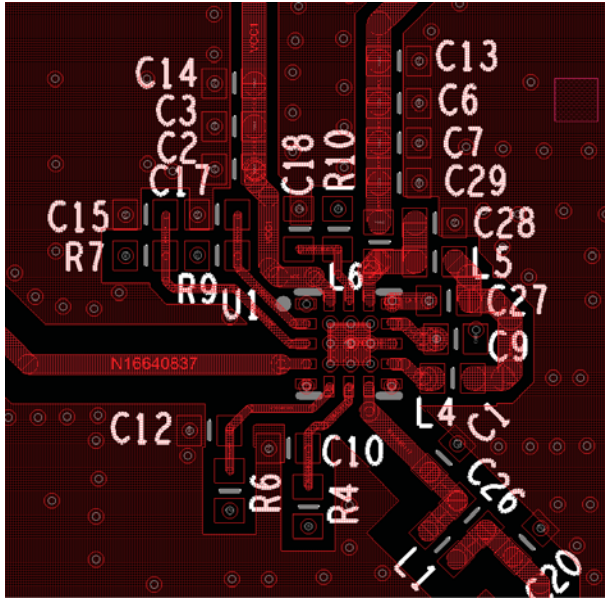
Figure 4. SKY66114-11 Evaluation Board Schematic Diagram

203678-004

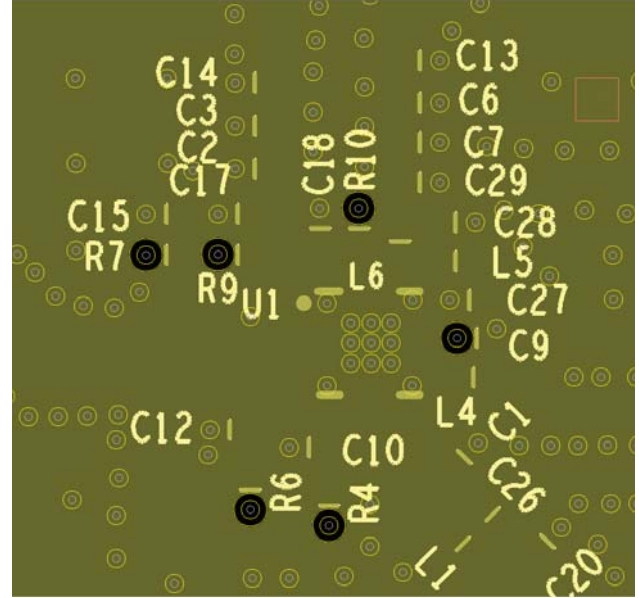
**Table 7. SKY66114-11 Evaluation Board Bill of Materials (BOM)**

| Component               | Value         | Size | Manufacturer | Mfr Part Number    | Description                                    |
|-------------------------|---------------|------|--------------|--------------------|--|
| C1, C20                 | 0.2 pF        | 0402 | Murata       | GJM1555C1HR20BB01D | Ceramic capacitor, 0.2 pF, High-Q, 50 V, COG   |
| C2, C7, C9              | 1000 pF       | 0402 | Murata       | GRM1555C1H102JA01D | Ceramic capacitor, 1000 pF, 50 V, 5%, COG      |
| C3, C6                  | 33000 pF      | 0402 | Murata       | GRM155R71C333KA01D | Ceramic capacitor, 33000 pF, 16 V, 10%, X7R    |
| C8                      | 0.1 $\mu$ F   | 0402 | Murata       | GRM155R71C104KA88J | Ceramic capacitor, 0.1 $\mu$ F, 16 V, 10%, X7R |
| C10, C12, C15, C17, C18 | 10 pF         | 0402 | Murata       | GRM1555C1H100JA01D | Ceramic capacitor, 10 pF, 50 V, 5%, COG        |
| C13, C14                | 4.7 $\mu$ F   | 0402 | Samsung      | CL05A475MP5NRNC    | Ceramic capacitor, 4.7 $\mu$ F, 10V, 20%, X5R  |
| C26                     | 0.4 pF        | 0402 | Murata       | GJM1555C1HR40BB01D | Ceramic capacitor, 0.4 pF, High-Q, 50 V, COG   |
| C27, C28                | 2.7 pF        | 0402 | Murata       | GJM1555C1H2R7BB01D | Ceramic capacitor, 2.7 pF, High-Q, 50 V, COG   |
| C29                     | 15 pF         | 0402 | Murata       | GRM1555C1H150JA01D | Ceramic capacitor, 15 pF, 50 V, 5%, COG        |
| L1                      | 0.8 nH        | 0402 | TDK          | MHQ1005P0N8BT000   | High-Q multilayer inductor                     |
| L4                      | 1 nH          | 0402 | TDK          | MHQ1005P1N0BT000   | High-Q multilayer inductor                     |
| L5                      | 1.1 nH        | 0402 | TDK          | MHQ1005P1N1BT000   | High-Q multilayer inductor                     |
| L6                      | 1.5 nH        | 0402 | TDK          | MHQ1005P1N5BT000   | High-Q multilayer inductor                     |
| R10, R9, R7, R6, R4     | 1000 $\Omega$ | 0402 | Samsung      | RC1005J102CS       | Resistor, 1000 $\Omega$ , 5%, 0402             |
| L7, C30, C31, C32       | DNI           |      |              |                    |  |

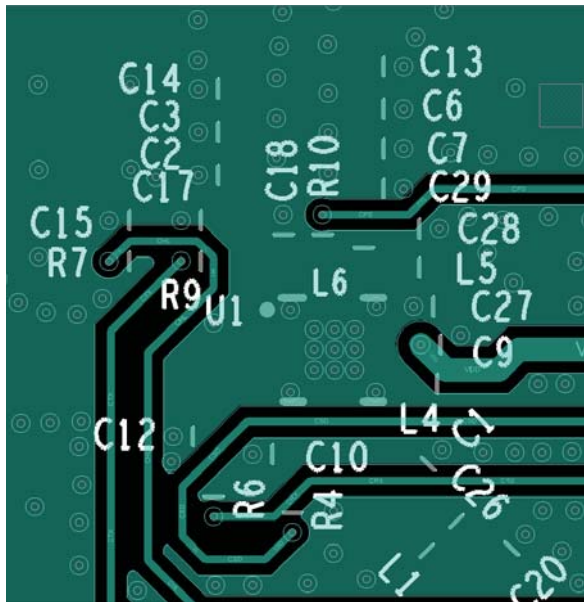




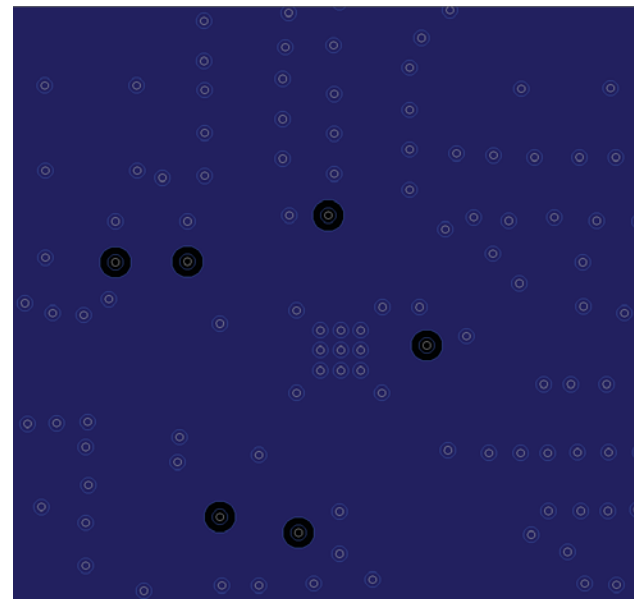
Reference Layout Layer 1



Reference Layout Layer 2



Reference Layout Layer 3



Reference Layout Layer 4

**Figure 5. Reference Layout Layers**

203678-005

## Package Dimensions

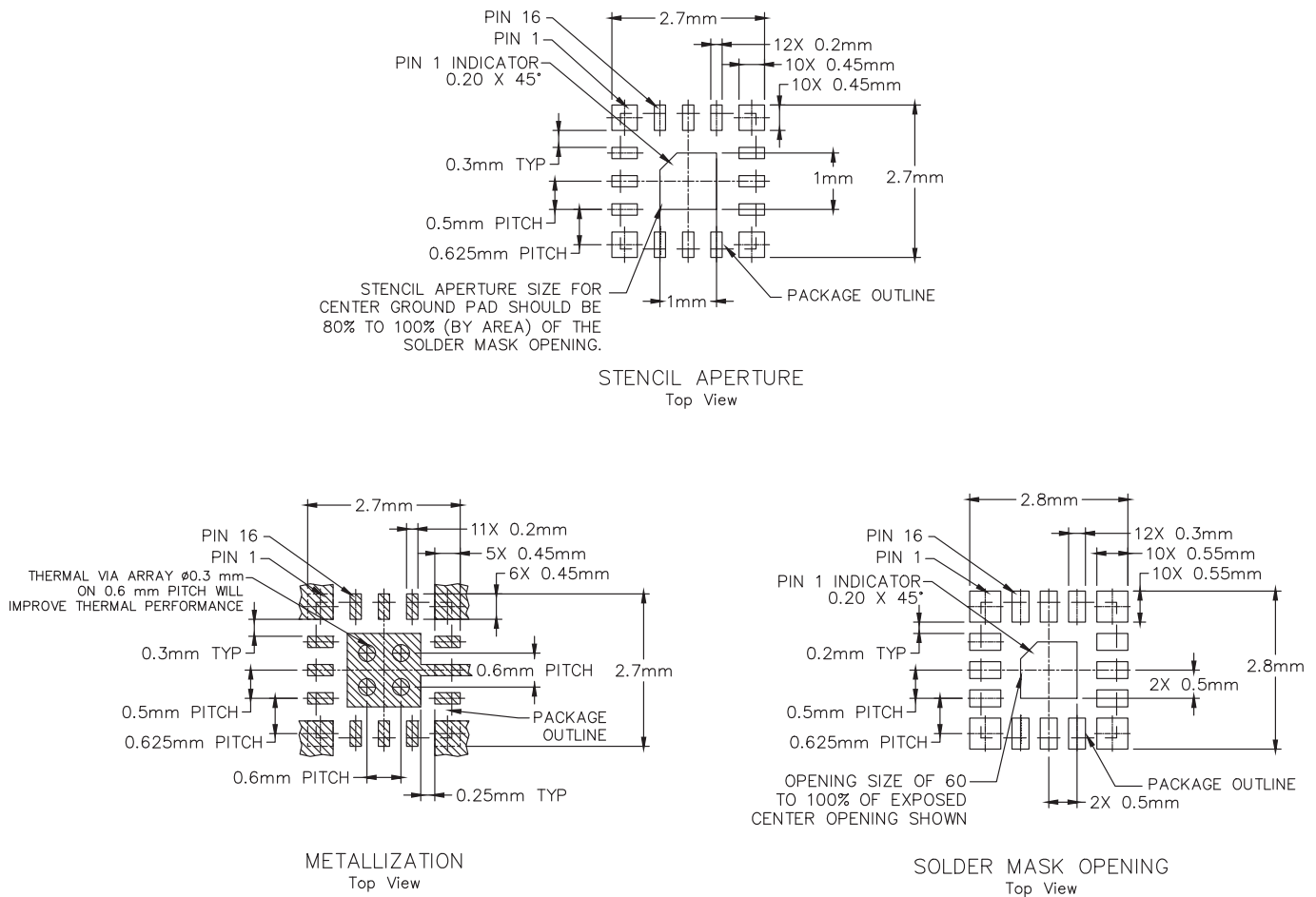
The PCB layout footprint for the SKY66114-11 is provided in Figure 6. The typical part marking is shown in Figure 7. Package dimensions are shown in Figure 8, and tape and reel dimensions are provided in Figure 9.

## Package and Handling Information

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY66114-11 is rated to Moisture Sensitivity Level 3 (MSL3) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *PCB Design and SMT Assembly/Rework Guidelines for MCM-L Packages*, document number 101752.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



NOTE: THERMAL VIAS SHOULD BE RESIN FILLED AND CAPPED IN ACCORDANCE WITH IPC-4761 TYPE VII VIAS. 30–35UM Cu THICKNESS IS RECOMMENDED.

203678-006

Figure 6. SKY66114-11 PCB Layout Footprint

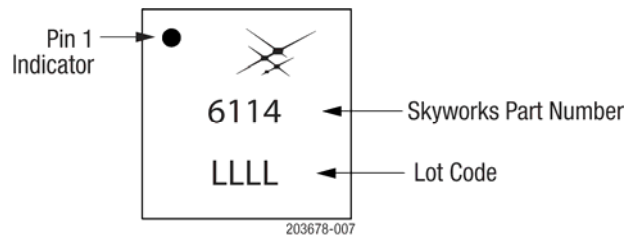
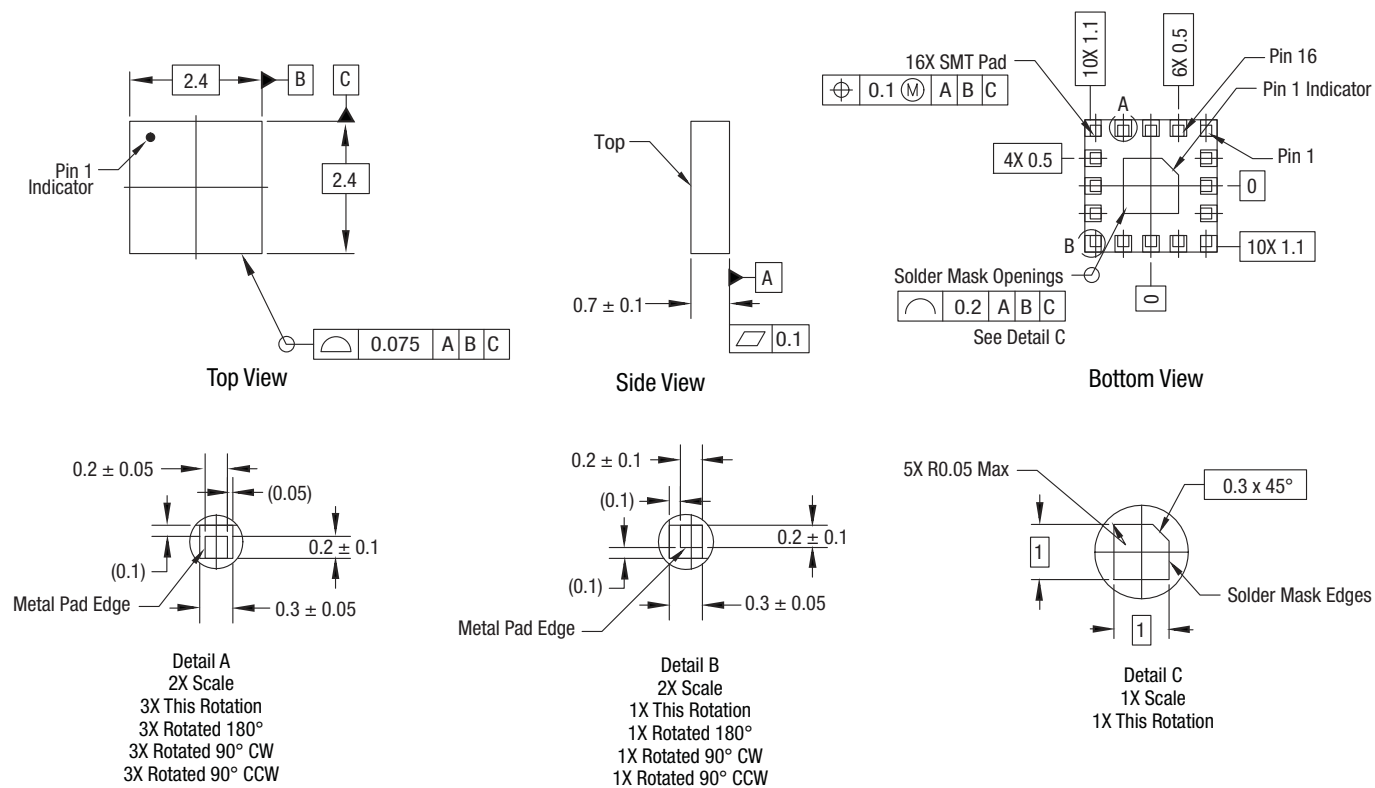


Figure 7. SKY66114-11 Typical Part Marking (Top View)

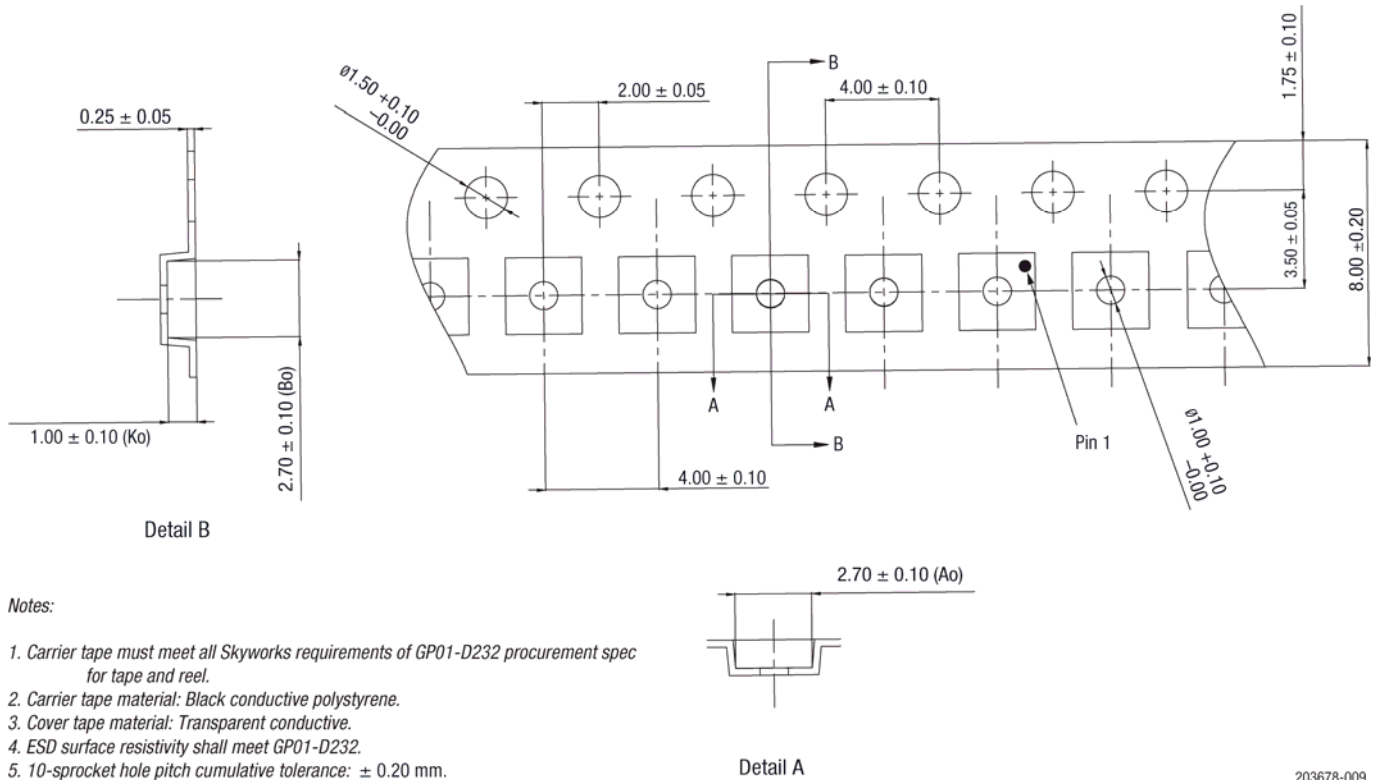


**Notes:**

1. All measurements are in millimeters.
2. Dimensions and tolerances according to ASME Y14.5M-1994.

203678-008

**Figure 8. SKY66114-11 Package Dimensions**



**Figure 9. SKY66114-11 Tape and Reel Dimensions**

## Ordering Information

| Part Number | Product Description   | Evaluation Board Part Number |
|-------------|---|------------------------------|
| SKY66114-11 | 2.4 GHz FEM for Bluetooth Low Energy/802.15.4/Thread/ZigBee | SKY66114-11EK1               |

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