

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

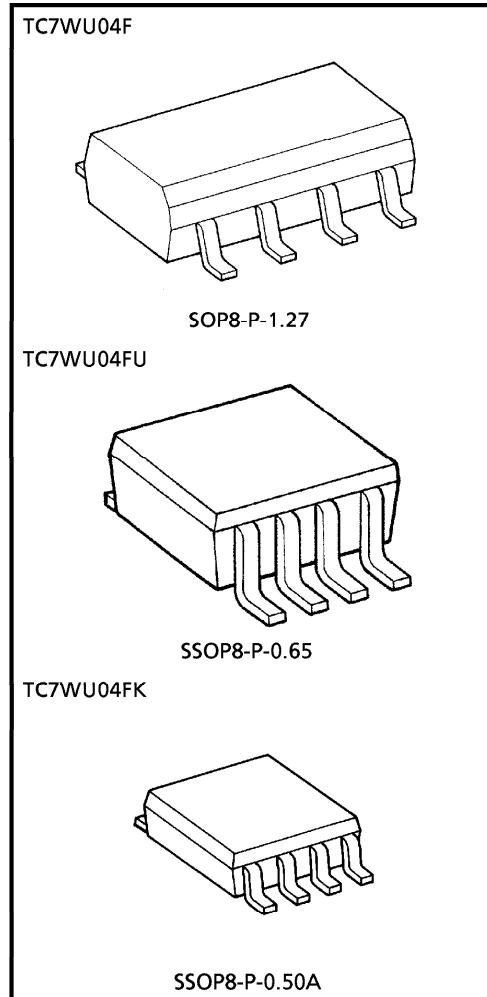
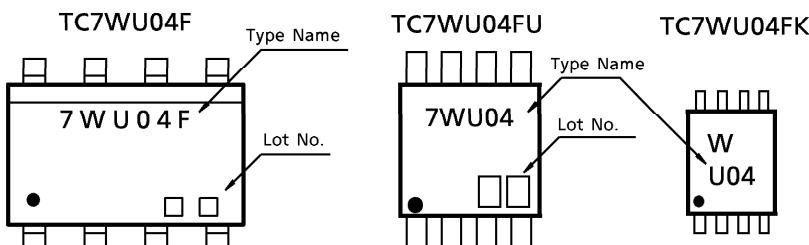
TC7WU04F, TC7WU04FU, TC7WU04FK**3 INVERTER**

The TC7WU04 is a high speed C²MOS INVERTER fabricated with silicon gate C²MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation.

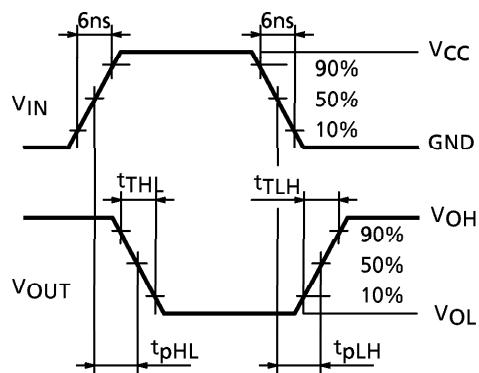
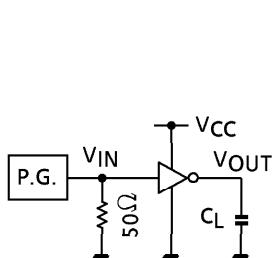
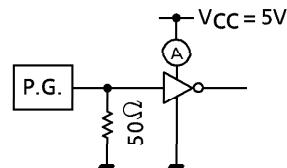
As the internal circuit is composed of single stage inverter, it can be applied for crystal oscillation. All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES

- High Speed $t_{PD} = 6\text{ns}$ (Typ.) at $V_{CC} = 5\text{V}$
- Low Power Dissipation $I_{CC} = 1\mu\text{A}$ (Max.) at $T_a = 25^\circ\text{C}$
- High Noise Immunity $V_{NIH} = V_{NIL} = 10\%$ V_{CC} (Min.)
- Output Drive Capability 10 LSTTL Loads
- Symmetrical Output Impedance ... $|I_{OH}| = I_{OL} = 4\text{mA}$ (Min.)
- Balanced Propagation Delays $t_{PLH} \approx t_{PHL}$
- Wide Operating Voltage Range ... $V_{CC(\text{opr})} = 2\sim 6\text{V}$

MARKING

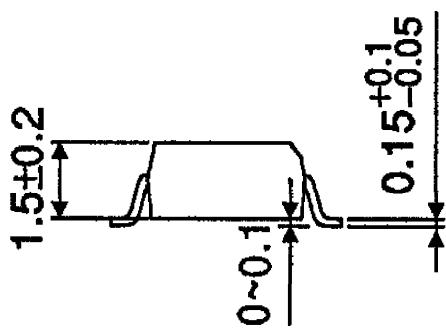
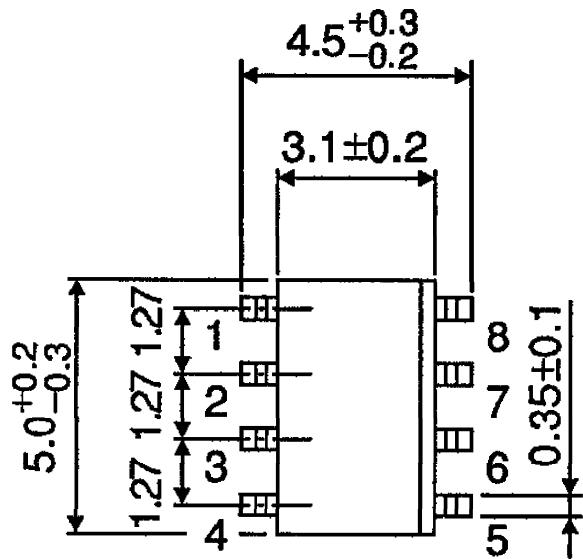
Weight
 SOP8-P-1.27 : 0.05g (Typ.)
 SSOP8-P-0.65 : 0.02g (Typ.)
 SSOP8-P-0.50A : 0.01g (Typ.)

**SWITCHING CHARACTERISTICS
TEST CIRCUIT****OPERATING CURRENT CONSUMPTION
TEST CIRCUIT**

This input waveform is equal to
SWITCHING CHARACTERISTICS TEST
CIRCUIT input waveform.

PACKAGE DIMENSIONS
SOP8-P-1.27

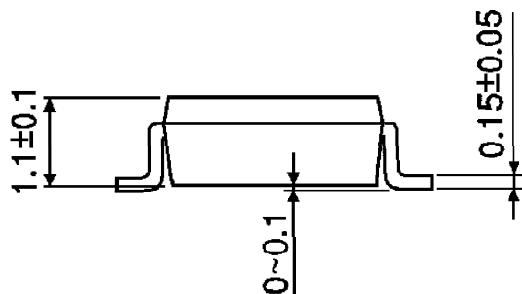
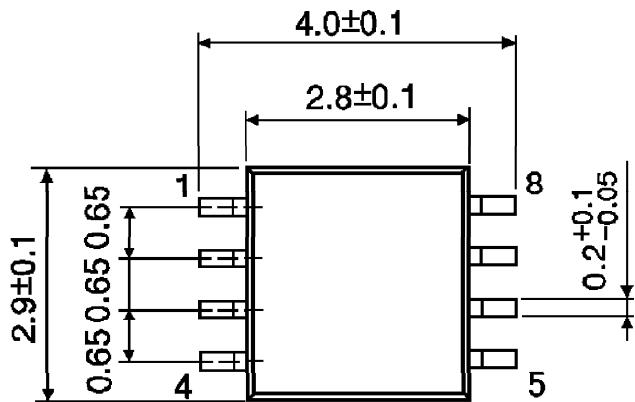
Unit : mm



Weight : 0.05g (Typ.)

PACKAGE DIMENSIONS
SSOP8-P-0.65

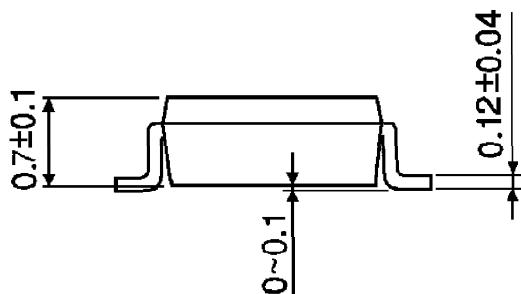
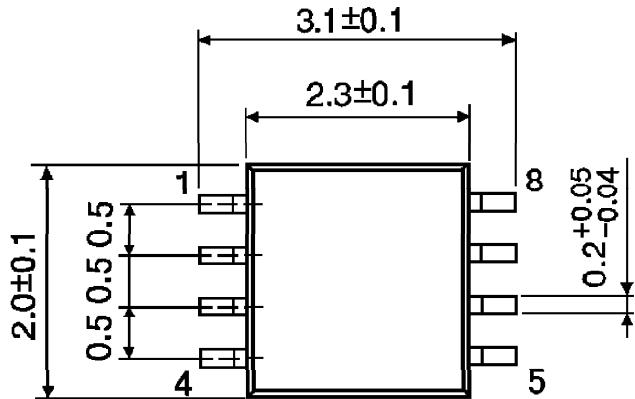
Unit : mm



Weight : 0.02g (Typ.)

PACKAGE DIMENSIONS
SSOP8-P-0.50A

Unit : mm



Weight : 0.01g (Typ.)

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000707EBA

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