Product data sheet

1. General description

Planar passivated four quadrant triac in a SOT186A (TO-220F) "full pack" plastic package intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

2. Features and benefits

- · High blocking voltage capability
- Isolated package
- Less sensitive gate for improved noise immunity
- · Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants

3. Applications

- General purpose phase control
- · General purpose switching

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	600	V
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 20 \text{ ms}$; Fig. 4; Fig. 5	-	-	95	Α
T _j	junction temperature		-	-	125	°C
I _{T(RMS)}	RMS on-state current	full sine wave; $T_h \le 56$ °C; Fig. 1; Fig. 2; Fig. 3	-	-	12	A
Static ch	aracteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G+;$ $T_j = 25 \text{ °C; } Fig. 7$	-	5	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + \text{ G-;} $ $T_j = 25 \text{ °C; } \underline{\text{Fig. 7}}$	-	8	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; T2- G-;}$ $T_j = 25 \text{ °C; } Fig. 7$	-	10	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2- \text{ G+;}$ $T_j = 25 \text{ °C; } \underline{\text{Fig. 7}}$	-	22	70	mA

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Dynamic	characteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit	50	250	-	V/µs

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	- mb	
2	T2	main terminal 2		T2—T1
3	G	gate		Sym051
mb	n.c.	mounting base; isolated		

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BT138X-600F	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A		

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_h \le 56$ °C; Fig. 1; Fig. 2; Fig. 3	-	12	A
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 ^{\circ}\text{C}$; $t_p = 20 \text{ms}$; $Fig. 4$; $Fig. 5$	-	95	A
		full sine wave; $T_{j(init)} = 25 ^{\circ}C$; $t_p = 16.7 \text{ms}$	-	105	A
l ² t	I ² t for fusing	t _p = 10 ms; sine-wave pulse	-	45	A ² s
dl _⊤ /dt	rate of rise of on-state	I _G = 50 mA; T2+ G+	-	50	A/µs
	current	I _G = 50 mA; T2+ G-	-	50	A/µs
		I _G = 50 mA; T2- G-	-	50	A/µs
		I _G = 140 mA; T2- G+	-	10	A/µs
I _{GM}	peak gate current		-	2	А
P_GM	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

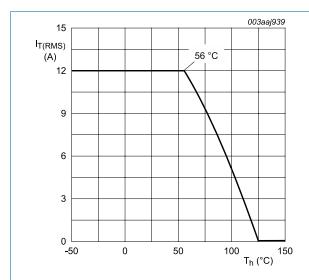


Fig. 1. RMS on-state current as a function of heatsink temperature; maximum values

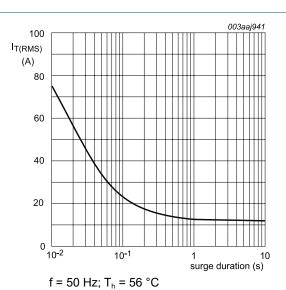
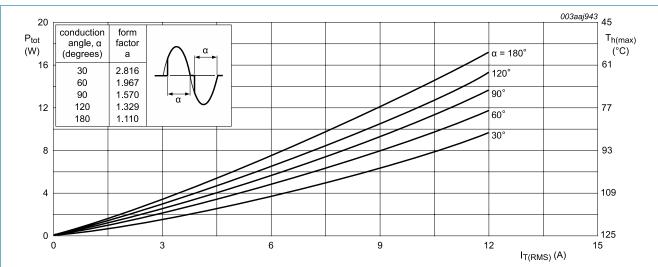


Fig. 2. RMS on-state current as a function of surge duration; maximum values



 α = conduction angle

a = form factor = $I_{T(RMS)} / I_{T(AV)}$ Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

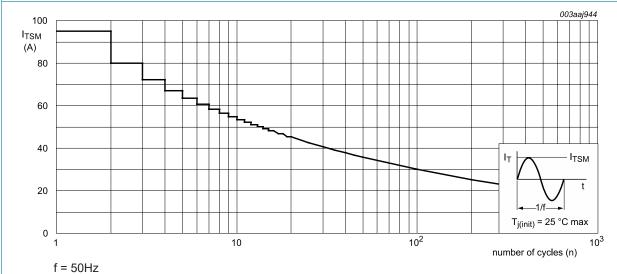
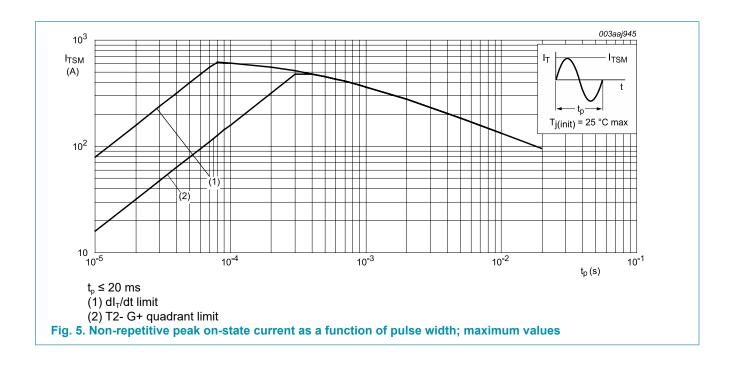


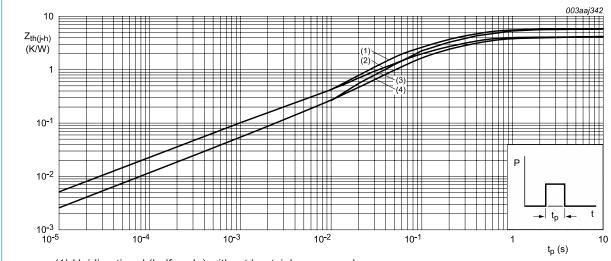
Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-h)}$	thermal resistance from junction to	full or half cycle; with heatsink compound; Fig. 6	-	-	4	K/W
hea	heatsink	full or half cycle; without heatsink compound; Fig. 6	-	-	5.5	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	55	-	K/W



- (1) Unidirectional (half cycle) without heatsink compound
- (2) Unidirectional (half cycle) with heatsink compound
- (3) Bidirectional (full cycle) without heatsink compound
- (4) Bidirectional (full cycle) with heatsink compound

Fig. 6. Transient thermal impedance from junction to heatsink as a function of pulse duration

9. Isolation characteristics

Table 6. Isolation characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{isol(RMS)}	RMS isolation voltage	from all terminals to external heatsink; sinusoidal waveform; clean and dust free; 50 Hz \leq f \leq 60 Hz; RH \leq 65 %; T _h = 25 °C	-	-	2500	V
C _{isol}	isolation capacitance	from main terminal 2 to external heatsink; f = 1 MHz; T _h = 25 °C	-	10	-	pF

10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static ch	naracteristics		l			
I _{GT}	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G+;$ $T_j = 25 ^{\circ}\text{C; } Fig. 7$	-	5	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G-;$ $T_j = 25 \text{ °C; } Fig. 7$	-	8	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; T2- G-;}$ $T_j = 25 \text{ °C; } Fig. 7$	-	10	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; T2- G+;}$ $T_j = 25 \text{ °C; } Fig. 7$	-	22	70	mA
L	latching current	$V_D = 12 \text{ V; } I_G = 0.1 \text{ A; } T2+ G+;$ $T_j = 25 \text{ °C; } Fig. 8$	-	7	40	μA
		$V_D = 12 \text{ V; } I_G = 0.1 \text{ A; } T2 + G-;$ $T_j = 25 \text{ °C; } Fig. 8$	-	20	60	mA
		$V_D = 12 \text{ V; } I_G = 0.1 \text{ A; T2- G-;}$ $T_j = 25 \text{ °C; } Fig. 8$	-	8	40	mA
		$V_D = 12 \text{ V; } I_G = 0.1 \text{ A; } T2- \text{ G+;}$ $T_j = 25 \text{ °C; } Fig. 8$	-	10	60	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	6	30	mA
V _T	on-state voltage	I _T = 15 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.4	1.65	V
V_{GT}	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 ^{\circ}\text{C};$ Fig. 11	0.25	0.4	-	V
I_D	off-state current	V _D = 600 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic	characteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit	50	250	-	V/µs
t _{gt}	gate-controlled turn-on time	I_{TM} = 16 A; V_D = 600 V; I_G = 0.2 A; $d_{IG}/$ dt = 0.1 A/µs	-	2	-	μs

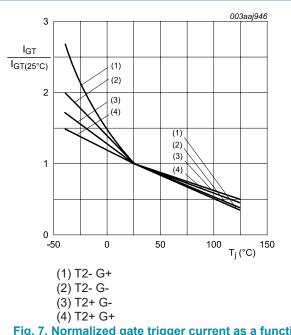


Fig. 7. Normalized gate trigger current as a function of junction temperature

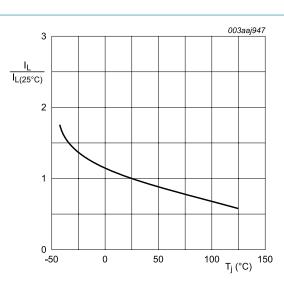


Fig. 8. Normalized latching current as a function of junction temperature

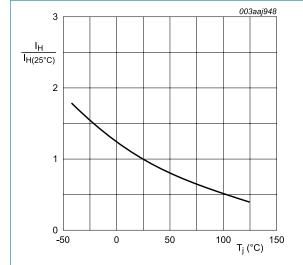
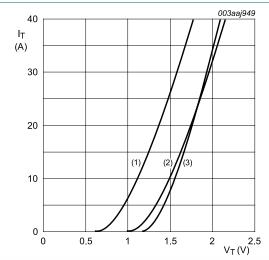


Fig. 9. Normalized holding current as a function of junction temperature



 V_o = 1.145 V; R_s = 0.031 Ω (1) T_j = 125 °C; typical values (2) T_j = 125 °C; maximum values (3) T_j = 25 °C; maximum values

Fig. 10. On-state current as a function of on-state voltage

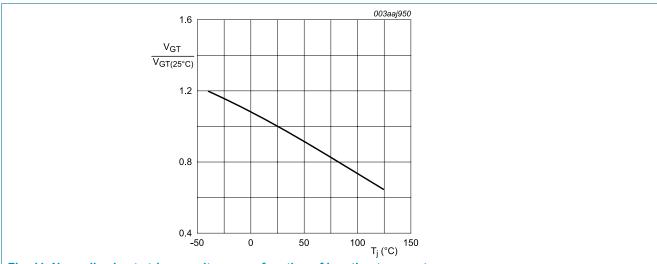
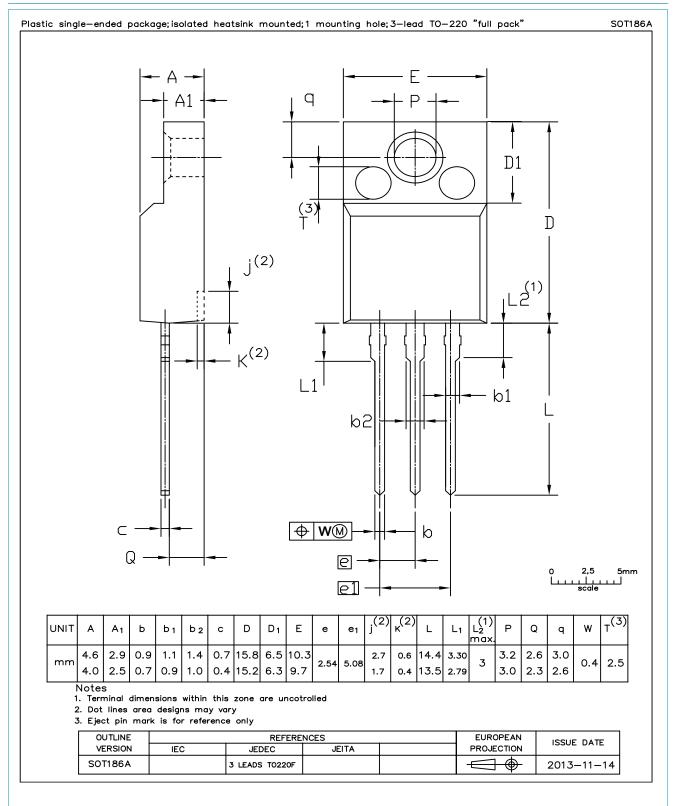


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

11. Package outline



12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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4Q Triad

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Тел: +7 (812) 336 43 04 (многоканальный) Email: org@lifeelectronics.ru