

DATA SHEET

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| Part No. | AN41400A |
| Package Code No. | UBGA019-W-2025AEB |

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AN41400A

1-ch Motor drive IC

■ Overview

The AN41400A is a 1-ch motor drive IC. This IC features a low ON Resistance and a wide Operating Supply Voltage Range of power supply for motor drive. Adopting an Wafer Level Chip Size Package makes it possible to shrink the mounting area.

■ Features

- 1-ch Motor drive IC
- Forward reverse drive is possible
- It is possible to drive not only a motor but also an actuator
- Low ON Resistance : 0.19 Ω (Upper and Lower)
- Operating Supply Voltage Range : Supply voltage range for control 2.7 V to 5.5 V,
Supply voltage range for drive 2.0 V to 13.8 V
- Downsizing by adopting an Wafer Level Chip Size Package
- Additional features : Built-in Stand-by function
Thermal shutdown circuit
Low voltage detection circuit

■ Applications

- For shutter, mirror, and lens of camera

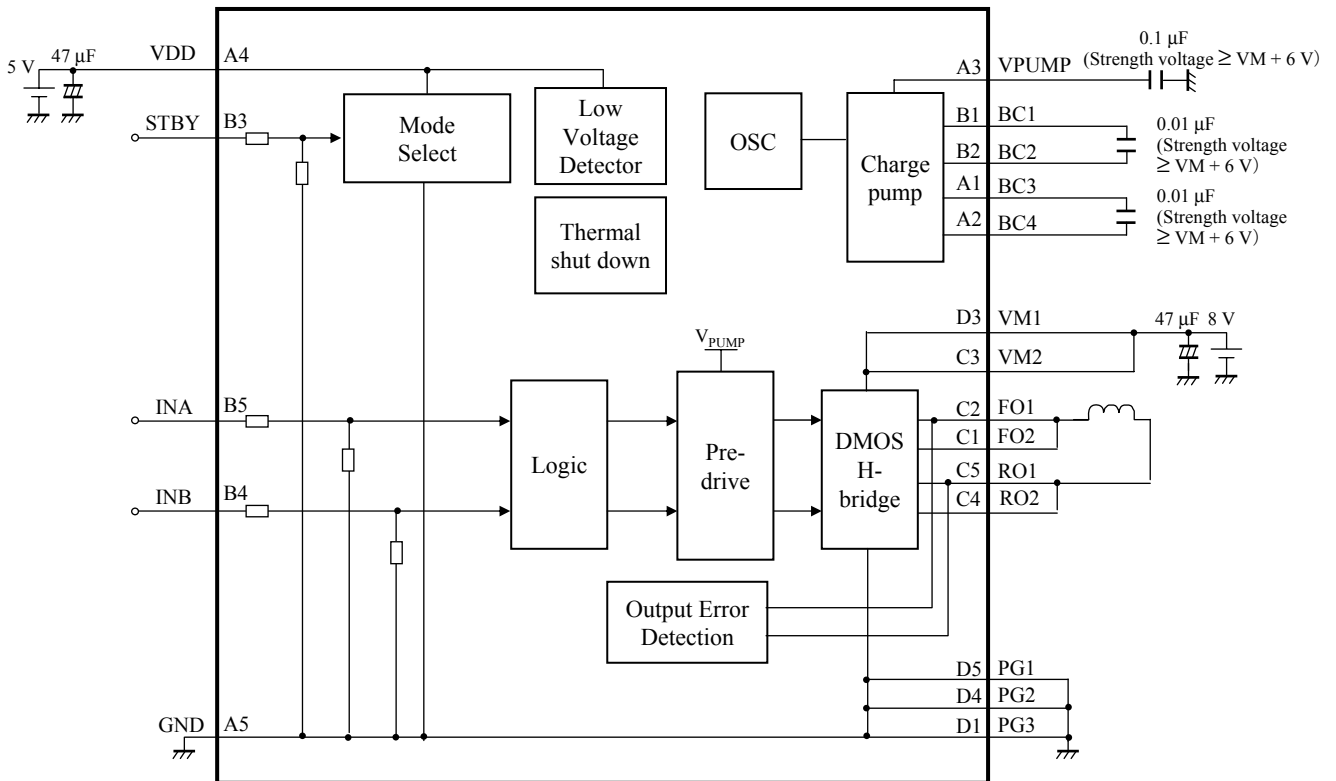
■ Package

- 19 pin Wafer Level Chip Size Package (WLCSP) (Size : 2.41 mm \times 1.91 mm, 0.5 mm Pitch)

■ Type

- Bi-CDMOS IC

■ Application Circuit Example (Block Diagram)

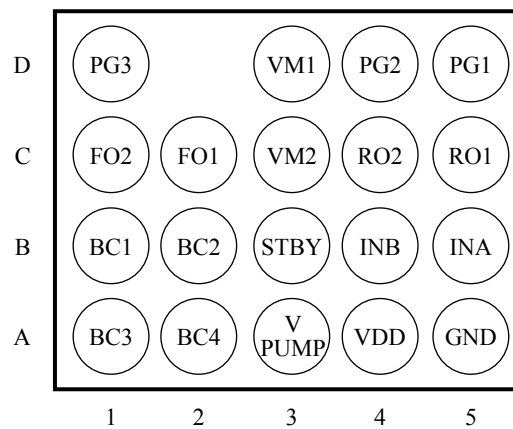


- Notes)
- This block diagram is for explaining functions. Part of the block diagram may be omitted, or it may be simplified.
 - This application circuit is shown as an example but does not guarantee the design for mass production set.

■ Pin Descriptions

| Pin No. | Pin name | Type | Description |
|---------|----------|--------------|------------------------------------|
| A1 | BC3 | Output | Charge pump capacitor connection 3 |
| A2 | BC4 | Output | Charge pump capacitor connection 4 |
| A3 | VPUMP | Output | Charge pump output |
| A4 | VDD | Power supply | Power supply for control circuit |
| A5 | GND | Ground | Ground for control circuit |
| B1 | BC1 | Output | Charge pump capacitor connection 1 |
| B2 | BC2 | Output | Charge pump capacitor connection 2 |
| B3 | STBY | Input | Total shutdown input |
| B4 | INB | Input | Inverting input |
| B5 | INA | Input | Non-inverting input |
| C1 | FO2 | Output | Non-inverting output 2 |
| C2 | FO1 | Output | Non-inverting output 1 |
| C3 | VM2 | Power supply | Power supply 2 for motor drive |
| C4 | RO2 | Output | Inverting output 2 |
| C5 | RO1 | Output | Inverting output 1 |
| D1 | PG3 | Ground | Ground 3 for motor drive |
| D3 | VM1 | Power supply | Power supply 1 for motor drive |
| D4 | PG2 | Ground | Ground 2 for motor drive |
| D5 | PG1 | Ground | Ground 1 for motor drive |

■ Pin Configuration (Bottom View)



■ Absolute Maximum Ratings

| A No. | Parameter | Symbol | Rating | A No. | Appropriate Pin No. | Notes |
|-------|-------------------------------|-----------------|-----------------|-------|---------------------|----------|
| 1 | Supply voltage | V_{DD} | 6.0 | V | — | *1 |
| | | V_M | 14.0 | | | |
| 2 | Supply current | I_{DD} | 100 | mA | — | — |
| | | I_M | 1 200 | | | |
| 3 | Power dissipation | P_D | 92 | mW | — | *2 |
| 4 | Operating ambient temperature | T_{opr} | -30 to +85 | °C | — | *3 |
| 5 | Storage temperature | T_{stg} | -55 to +150 | °C | — | *3 |
| 6 | Drive output current | $I_{(p) DC}$ | ±1 200 (DC) | mA | p = C1, C2, C4, C5 | *4 *5 |
| | | $I_{(p) peak1}$ | ±6 000 (1 ms) | mA | | |
| | | $I_{(p) peak2}$ | ±3 500 (10 ms) | mA | | |
| | | $I_{(p) peak3}$ | ±2 000 (100 ms) | mA | | |
| 7 | Drive output voltage | $V_{(m)}$ | 14.7 | V | m = C1, C2, C4, C5 | *5 |
| 8 | Control signal input voltage | $V_{(n)}$ | GND to V_{DD} | V | n = B3, B4, B5 | *5 |

Notes) *1: The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

The voltage of the charge pump circuit voltage will exceed the supply voltage. The limit voltage of the charge pump is shown on page 8.

*2: The power dissipation shown is the value at $T_a = 85^\circ\text{C}$ for the independent (unmounted) IC package without a heat sink.

When using this IC, refer to the $\bullet P_D-T_a$ diagram in the ■ Technical Data and design the heat radiation with sufficient margin so that the allowable value might not be exceeded based on the conditions of power supply voltage, load, and ambient temperature.

*3: Except for the power dissipation, operating ambient temperature, and storage temperature, all ratings are for $T_a = 25^\circ\text{C}$.

*4: Design the heat radiation with sufficient margin so that the allowable value might not be exceeded based on the time conditions which the drive output current $\pm 6\,000$ mA is allowed within 1 ms and $\pm 3\,500$ mA is allowed within 10 ms and $\pm 2\,000$ mA is allowed within 100 ms. However, the output frequency f requires that $f \leq 5$ Hz.

*5: Do not apply voltage or current from outside to these pin. The setting not exceeding the rating, even transiently, is required. For the circuit currents, '+' denotes current flowing into the IC, and '-' denotes current flowing out of the IC.

■ Operating Supply Voltage Range

| Parameter | Symbol | Min | Typ | Max | Unit | Notes |
|----------------------|----------|-----|-----|------|------|-------|
| Supply voltage range | V_{DD} | 2.7 | 3.3 | 5.5 | V | *1 |
| | V_M | 2.0 | 7.4 | 13.8 | | |

Note) *1 : The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

■ Allowable Current and Voltage Range

- Notes)
- Allowable current and voltage ranges are limit ranges which do not result in damages to this IC, and IC operation is not guaranteed within these limit ranges.
 - Voltage values, unless otherwise specified, are with respect to GND. GND is voltage for GND, PG1, PG2 and PG3. GND = PG1 = PG2 = PG3
 - V_{DD} is voltage for VDD.
 - V_M is voltage for VM1 and VM2. $V_M = V_{M1} = V_{M2}$
 - Do not apply external currents or voltages to any pin not specifically mentioned.
 - For the circuit currents, "+" denotes current flowing into the IC, and "-" denotes current flowing out of the IC.

| Pin No. | Pin name | Rating | Unit | Note |
|---------|----------|-----------------|------|------|
| A1 | BC3 | GND to V_M | V | *1 |
| A2 | BC4 | GND to 19.5 | V | *1 |
| A3 | VPUMP | GND to 19 | V | *1 |
| B1 | BC1 | GND to V_M | V | *1 |
| B2 | BC2 | GND to 19.5 | V | *1 |
| B3 | STBY | GND to V_{DD} | V | — |
| B4 | INB | GND to V_{DD} | V | — |
| B5 | INA | GND to V_{DD} | V | — |
| C1 | FO2 | -1.0 to 14.7 | V | *1 |
| C2 | FO1 | -1.0 to 14.7 | V | *1 |
| C4 | RO2 | -1.0 to 14.7 | V | *1 |
| C5 | RO1 | -1.0 to 14.7 | V | *1 |

Note) *1 : Do not apply external voltage to this pin. The setting not exceeding the rating, even transiently, is required.

■ Electrical Characteristics at $V_{DD} = 3.3\text{ V}$, $V_M = 7.4\text{ V}$, $STBY = 3.3\text{ V}$

Note) $T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$ unless otherwise specified.

| B No. | Parameter | Symbol | Conditions | Limits | | | Unit | Notes |
|----------------------------|--|-------------|--------------------------------|--------|------|------|---------------|-------|
| | | | | Min | Typ | Max | | |
| COMMON BLOCK | | | | | | | | |
| Supply Current | | | | | | | | |
| 1 | Drive power supply current in standby mode | I_{VM} | STBY = Low | — | — | 50 | μA | — |
| 2 | Control power supply current in standby mode | I_{DDS} | STBY = Low | — | — | 10 | μA | — |
| 3 | Control power supply current under no input | I_{DDA} | INA = INB = Low | — | 1.4 | 2.0 | mA | — |
| Standby Operation | | | | | | | | |
| 4 | STBY high level input voltage | V_{SBH} | — | 2.2 | — | — | V | *1 |
| 5 | STBY low level input voltage | V_{SBL} | — | — | — | 0.5 | V | *1 |
| 6 | STBY pull-down resistance | R_{STBY} | — | 100 | 200 | 300 | k Ω | — |
| Charge Pump Circuit | | | | | | | | |
| 7 | Charge pump voltage | V_{PUMP} | $I_{PUMP} = 0\text{ A}$ | 11.4 | 12.4 | 13.4 | V | — |
| 8 | Charge pump current capability | V_{PUMPL} | $I_{PUMP} = -500\ \mu\text{A}$ | 10.4 | 11.4 | 13.4 | V | — |
| Driver Block | | | | | | | | |
| 9 | INA, INB high level input voltage | V_{INH} | — | 2.2 | — | — | V | *1 |
| 10 | INA, INB low level input voltage | V_{INL} | — | — | — | 0.5 | V | *1 |
| 11 | INA, INB high level input current | I_{INH} | INA = INB = 3.3 V | 8.3 | 16.5 | 33 | μA | — |
| 12 | INA, INB low level input current | I_{INL} | — | -1.0 | — | — | μA | — |
| 13 | Output ON resistance (Upper and Lower) | R_{ON} | $I_{out} = \pm 500\text{ mA}$ | — | 0.19 | 0.24 | Ω | — |
| 14 | Rise time | T_R | — | — | 0.1 | 0.2 | μs | — |
| 15 | Fall time | T_F | — | — | 0.1 | 0.2 | μs | — |
| 16 | Turn on time | T_{PLH} | — | — | 0.4 | 1.0 | μs | — |
| 17 | Turn off time | T_{PHL} | — | — | 0.2 | 0.5 | μs | — |

Nots) *1 : Refer to page 10 for the mode setting.

■ Electrical Characteristics (Reference values for design) at $V_{DD} = 3.3\text{ V}$, $V_M = 7.4\text{ V}$, $STBY = 3.3\text{ V}$

Notes) $T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$ unless otherwise specified.

The characteristics listed below are reference values derived from the design of the IC and are not guaranteed by inspection.

If a problem does occur related to these characteristics, we will respond in good faith to user concerns.

| B No. | Parameter | Symbol | Conditions | Reference values | | | Unit | Notes |
|------------------------------------|--|------------------|------------|------------------|-----|-----|------------------|-------|
| | | | | Min | Typ | Max | | |
| 18 | Input signal frequency | f_{\max} | — | — | — | 300 | kHz | — |
| Operation of low voltage detection | | | | | | | | |
| 19 | Operating voltage of low voltage detection | V_{LVD} | — | — | 2.4 | — | V | — |
| 20 | Hysteresis width | ΔV_{LVD} | — | — | 0.2 | — | V | — |
| Thermal Shutdown | | | | | | | | |
| 21 | Thermal shutdown operating temperature | T_{TSD} | — | — | 160 | — | $^\circ\text{C}$ | — |
| 22 | Thermal shutdown hysteresis temperature | ΔT_{TSD} | — | — | 35 | — | $^\circ\text{C}$ | — |

■ Control Pin Mode Table

| STBY | VDD | Temp. | Input Logic | | Output State | | Charge Pump Circuit | Mode |
|------|---------|---------|-------------|-----------------------|--------------|------|---------------------|------------------|
| | | | INA | INB | FO | RO | | |
| High | > 2.4 V | < 160°C | High | High | Low | Low | Active | Brake |
| | | | High | Low | High | Low | | Normal rotation |
| | | | Low | High | Low | High | | Reverse rotation |
| | | | Low | Low | Z * | Z * | | Mute |
| | ≤ 2.4 V | — | — | Low voltage detection | | | | |
| | > 2.4 V | | | ≥ 160°C | | | | Thermal shutdown |
| Low | — | — | — | — | — | Mute | Standby | |

Note) * : Z means that output is Hi-Z.

■ Technical Data

- I/O block circuit diagrams and pin function descriptions

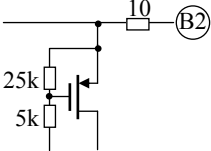
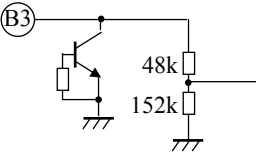
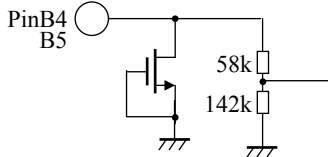
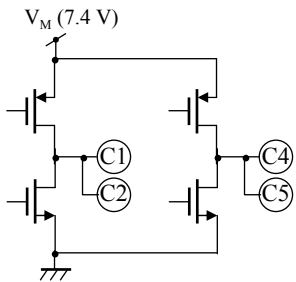
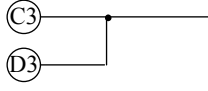
Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

| Pin No. | Waveform and voltage | Internal circuit | Impedance | Description |
|-------------------------|----------------------|------------------|-----------|--|
| A1 | — | | — | Charge pump capacitor connection 3 |
| A2 | — | | — | Charge pump capacitor connection 4 |
| A3 | DC approx. 12.4 V | | — | Charge pump output |
| A4 | DC (Typ. 3.3 V) | | — | Power supply for control circuit |
| A5, D1, D4, D5 | DC 0 V | | — | A5 : Ground for control circuit D1 : Ground 3 for motor drive D4 : Ground 2 for motor drive D5 : Ground 1 for motor drive |
| B1 | — | | — | Charge pump capacitor connection 1 |

■ Technical Data (continued)

- I/O block circuit diagrams and pin function descriptions (continued)

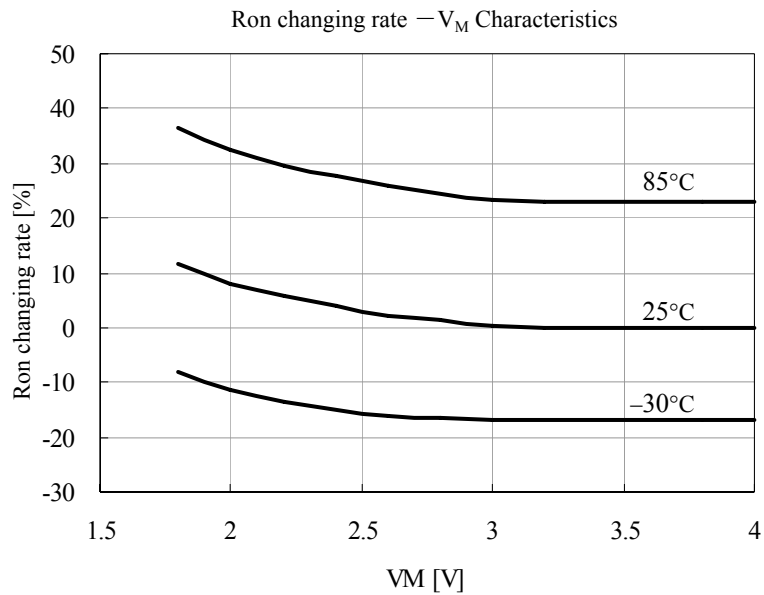
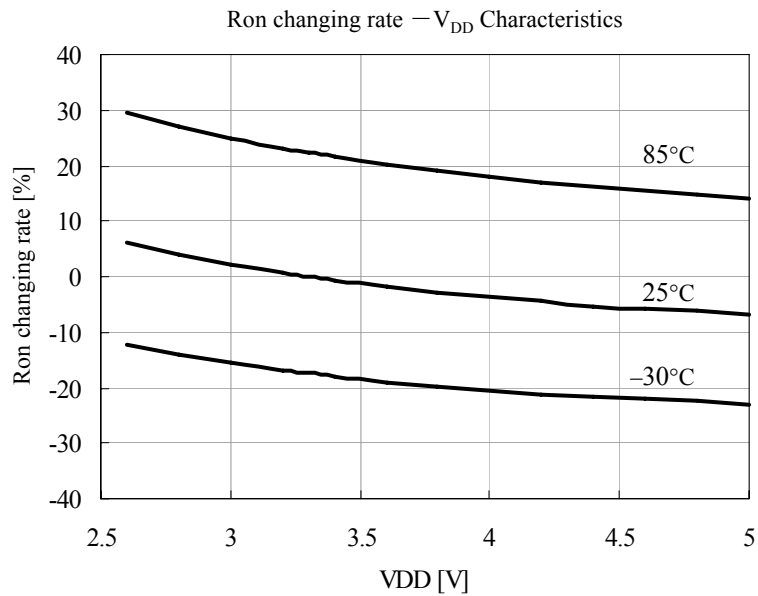
Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

| Pin No. | Waveform and voltage | Internal circuit | Impedance | Description |
|----------------|----------------------|---|-----------|--|
| B2 | — |  | — | Charge pump capacitor connection 2 |
| B3 | — |  | 200 kΩ | Total shutdown input |
| B4, B5 | — |  | 200 kΩ | B4 : Inverting input B5 : Non-inverting input |
| C1, C2, C4, C5 | — |  | — | C1 : Non-inverting output 2 C2 : Non-inverting output 1 C4 : Inverting output 2 C5 : Inverting output 1 |
| C3, D3 | DC (Typ. 7.4 V) |  | — | C3 : Power supply 2 for motor drive D3 : Power supply 1 for motor drive |

■ Technical Data (continued)

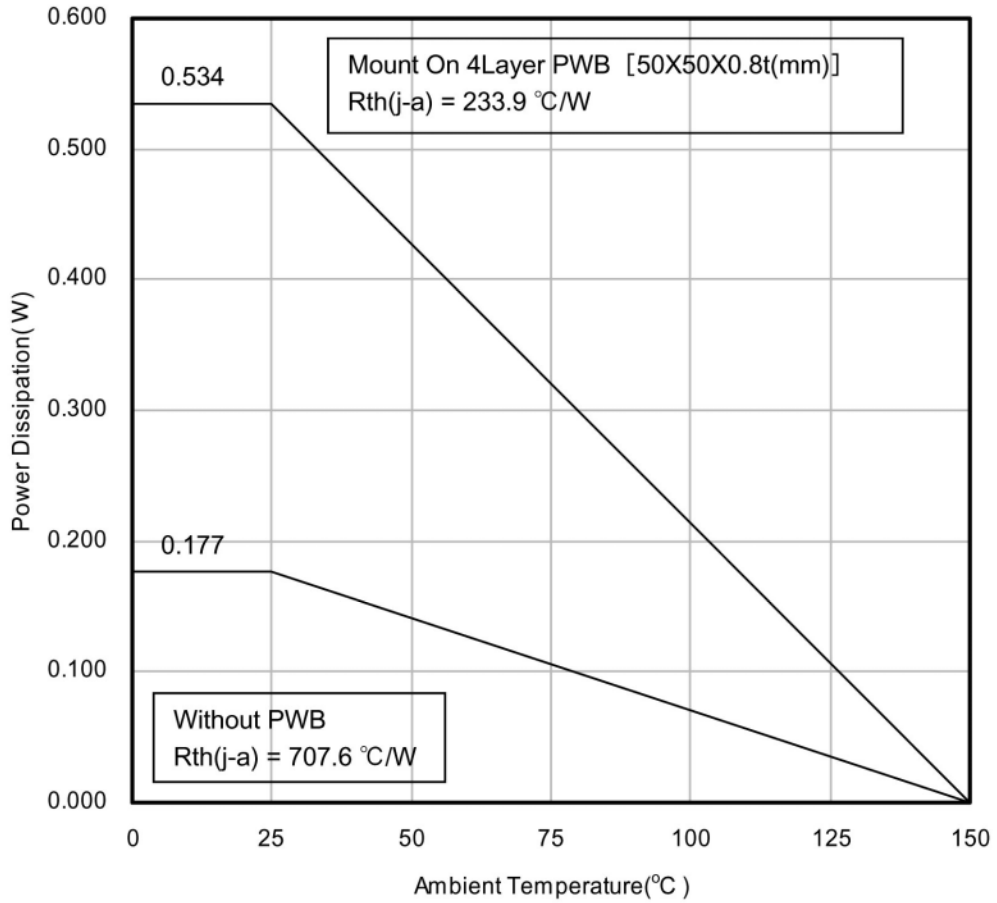
• Reference data

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.



■ Technical Data (continued)

- $P_D - T_a$ diagram



■ Usage Notes

• Special attention and precaution in using

1. This IC is intended to be used for general electronic equipment [For shutter, mirror, and lens of camera].
Consult our sales staff in advance for information on the following applications:
 - Special applications in which exceptional quality and reliability are required, or if the failure or malfunction of this IC may directly jeopardize life or harm the human body.
 - Any applications other than the standard applications intended.
 - (1) Space appliance (such as artificial satellite, and rocket)
 - (2) Traffic control equipment (such as for automobile, airplane, train, and ship)
 - (3) Medical equipment for life support
 - (4) Submarine transponder
 - (5) Control equipment for power plant
 - (6) Disaster prevention and security device
 - (7) Weapon
 - (8) Others : Applications of which reliability equivalent to (1) to (7) is required
2. Pay attention to the direction of LSI. When mounting it in the wrong direction onto the PCB (printed-circuit-board), it might smoke or ignite.
3. Pay attention in the PCB (printed-circuit-board) pattern layout in order to prevent damage due to short circuit between pins. In addition, refer to the Pin Description for the pin configuration.
4. Perform a visual inspection on the PCB before applying power, otherwise damage might happen due to problems such as a solder-bridge between the pins of the semiconductor device. Also, perform a full technical verification on the assembly quality, because the same damage possibly can happen due to conductive substances, such as solder ball, that adhere to the LSI during transportation.
5. Take notice in the use of this product that it might break or occasionally smoke when an abnormal state occurs such as output pin- V_{CC} short (Power supply fault), output pin-GND short (Ground fault), or output-to-output-pin short (load short) .
And, safety measures such as an installation of fuses are recommended because the extent of the above-mentioned damage and smoke emission will depend on the current capability of the power supply.
6. When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
7. When using the LSI for new models, verify the safety including the long-term reliability for each product.
8. When the application system is designed by using this LSI, be sure to confirm notes in this book.
Be sure to read the notes to descriptions and the usage notes in the book.
9. Due to unshielded structure of this IC, under exposure of light, function and characteristic of the product cannot be guaranteed.
During normal operation or even under testing condition, please ensure that IC is not exposed to light.
10. Basically, chip surface is ground potential. Please design to ensure no contact between chip surface and metal shielding.

■ Usage Notes (continued)

• Notes of Power LSI

1. The protection circuit is for maintaining safety against abnormal operation. Therefore, the protection circuit should not work during normal operation.
Especially for the thermal protection circuit, if the area of safe operation or the absolute maximum rating is momentarily exceeded due to output pin to V_{CC} short (Power supply fault), or output pin to GND short (Ground fault), the LSI might be damaged before the thermal protection circuit could operate.
2. Unless specified in the product specifications, make sure that negative voltage or excessive voltage are not applied to the pins because the device might be damaged, which could happen due to negative voltage or excessive voltage generated during the ON and OFF timing when the inductive load of a motor coil or actuator coils of optical pick-up is being driven.
3. The product which has specified ASO (Area of Safe Operation) should be operated in ASO.
4. Verify the risks which might be caused by the malfunctions of external components.

■ Usage Notes (continued)

• Notes of This LSI

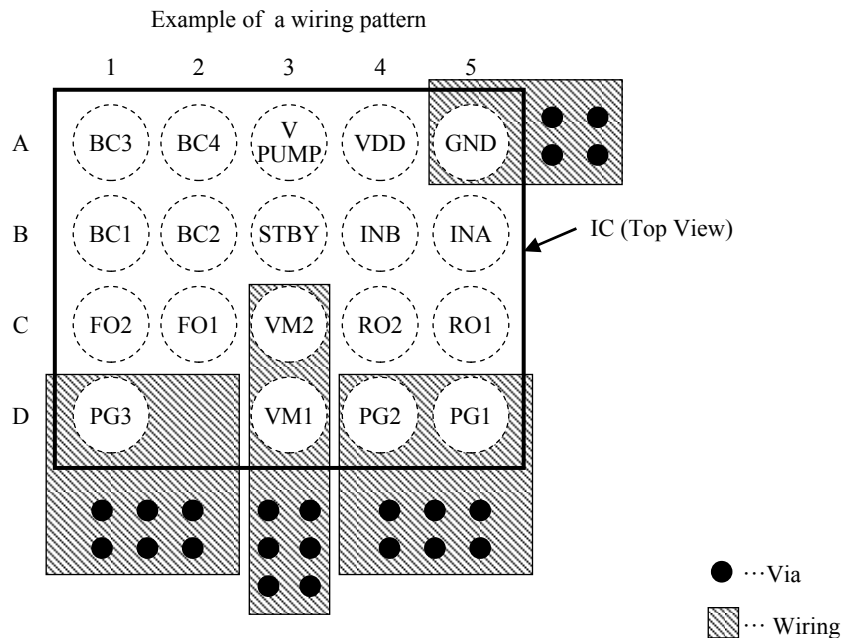
1. Make sure to power on, off, and switching under the standby mode (STBY = Low).
2. Apply voltage from a low-impedance source to VDD and VM. Connect a bypass capacitor to each as near the IC as possible.
3. In case of that the output is changed into Hi-Z (INA = INB = Low) in the rotation of motor, due to the motor current to flow back into a power source, the supply voltage might rise.
4. If the error detection circuit is active, all outputs are fixed in Hi-Z during the specified time (470 μs ±30%).
The function is for safety improvements and is not guaranteed nondestructive control.
5. Check the characteristics carefully before using this IC.
Preserve sufficient margin in consideration of dispersion of external components and our ICs including not only static characteristics but transition characteristics when using this IC changing external circuit constants.
6. Prohibit mounting with solder dipping and mounting to a flexible cable.
7. The heat thermal resistance is variable due to the mounted status of this IC. To reduce the heat thermal resistance, it is recommended that the power supply and GND pins are connected to a wide metal layer as short as possible.
Refer to the following figure shown an example of a wiring pattern.

<Reference value>

The heat thermal resistance value (for simulation) in case of the following wiring pattern example

$$R_{th(j-a)} = 97^{\circ}C / W$$

Condition : Glass-epoxy PWB, 50 × 50 × 0.8t (mm), 4-ply



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Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



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