## -30V Pch+Pch Middle Power MOSFET

$V_{DSS}$	-30V
R <sub>DS(on)</sub> (Max.)	70mΩ
I <sub>D</sub>	±4.0A
P <sub>D</sub>	2.0W

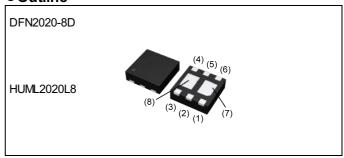
#### Features

- 1) Low on resistance.
- 2) Small Surface Mount Package.
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

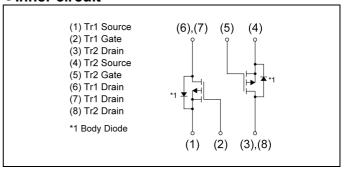
# Application

Switching

## Outline



## •Inner circuit



Packaging specifications

	Jing opcomoditorio	
	Packing	Embossed Tape
	Reel size (mm)	180
Type	Tape width (mm)	8
••	Quantity (pcs)	3000
	Taping code	TCR
	Marking	JA2

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-30	V
Continuous drain current	I <sub>D</sub>	±4.0	А
Pulsed drain current	I <sub>DP</sub> *1	±12	А
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Avalanche current, single pulse	I <sub>AS</sub> *2	-3.0	Α
Avalanche energy, single pulse	E <sub>AS</sub> *2	6.5	mJ
Power dissipation	P <sub>D</sub> *3	2.0	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Parameter	Symbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	1	-	62.5	°C/W

## ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Damanadan	0	0 - 1141 - 11 -	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-30	-	-	V
Breakdown voltage temperature coefficient		I <sub>D</sub> = -1mA	-	-22	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	$\Delta T_j$ referenced to 25°C $I_{DSS}$ $V_{DS} = -30V, V_{GS} = 0V$		-	-1	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{DS} = 0V, V_{GS} = \pm 20V$		-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -1 \text{mA}$		-	-2.5	V
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{\text{GS(th)}}}{\DeltaT_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	2.9	-	mV/°C
Static drain - source	D *4	V <sub>GS</sub> = -10V, I <sub>D</sub> = -4.0A	-	55	70	0
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.0A	-	80	103	mΩ
Gate resistance	$R_{G}$	f=1MHz, open drain	-	13	-	Ω
Forward Transfer Admittance	Y <sub>fs</sub>  *4	$V_{DS} = -5V, I_{D} = -3A$	2.5	-	-	S

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> L  $\simeq$  1mH, V<sub>DD</sub> = -15V, R<sub>G</sub> = 25 $\Omega$ , STARTING T $_{j}$  = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*3</sup> Mounted on a Cu board (40×40×0.8mm)

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Darameter	Symbol	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	305	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -15V	1	55	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	1	43	-	
Turn - on delay time	t <sub>d(on)</sub> *4	V <sub>DD</sub> ≃ -15V,V <sub>GS</sub> = -10V	1	7.4	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -2A	1	9.1	-	no
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 7.5\Omega$		26	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	13	-	

## ● Gate charge characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Darameter	Cumbal	Conditions -		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total meta abanna	O *4		V <sub>GS</sub> = -10V	-	6.7	-	
Total gate charge	$Q_g^{*4}$	V <sub>DD</sub> ≃ -15V		-	3.4	-	<b>~</b> C
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = -4A	V <sub>GS</sub> = -4.5V	-	1.1	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4			-	1.3	-	

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

## <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Continuous forward current	I <sub>S</sub>	T = 25°C	-	-	-1.67		
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	-12	А	
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1.67A	-	-	-1.2	V	



Fig.1 Power Dissipation Derating Curve

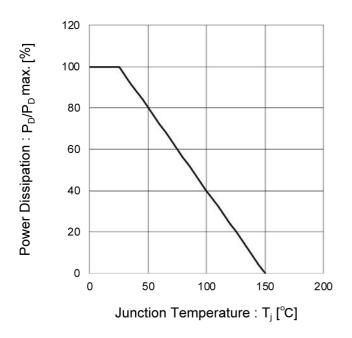
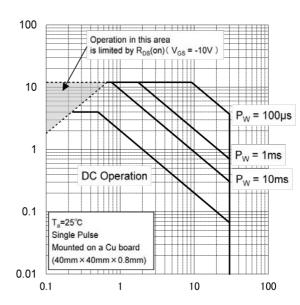


Fig.2 Maximum Safe Operating Area



Drain Current: -l<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

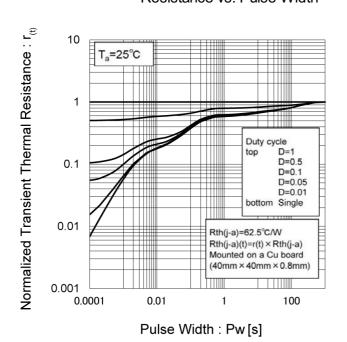
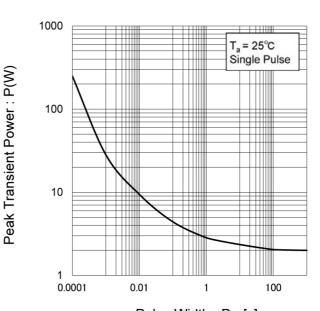


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

Fig.5 Typical Output Characteristics(I)

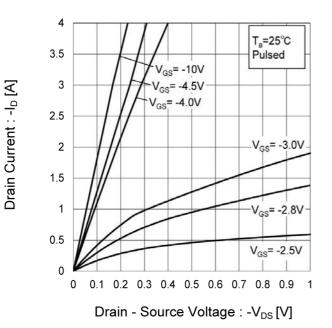
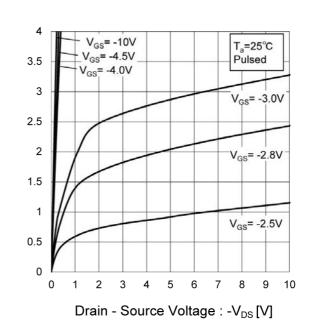


Fig.6 Typical Output Characteristics(II)



Drain Current : -I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs. Junction Temperature

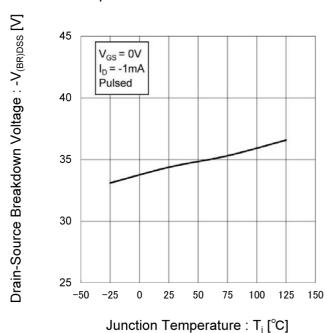


Fig.8 Typical Transfer Characteristics

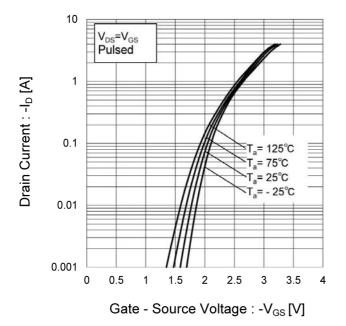
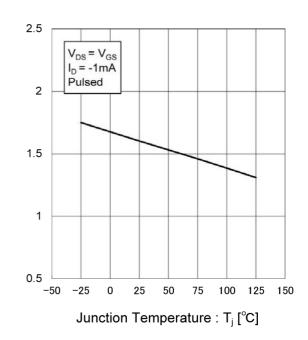


Fig.9 Gate Threshold Voltage vs. Junction Temperature



Gate Threshold Voltage : - $V_{GS(th)}$  [V]

Fig.10 Forward Transfer Admittance vs. Drain Current

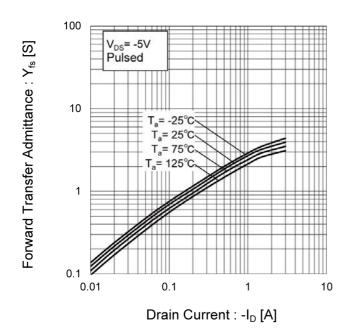


Fig.11 Drain Current Derating Curve

Drain Current Dissipation

100

(%) 80

60

20

20

20

25 50 75 100 125 150

Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

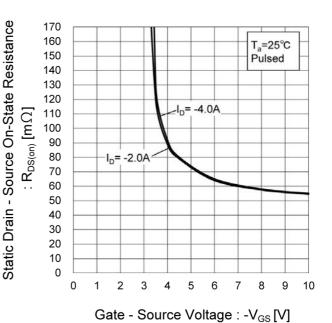
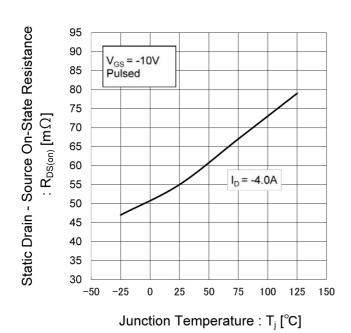


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



ROHM

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

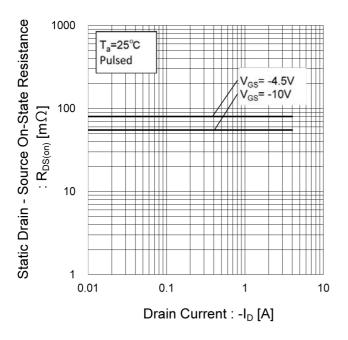


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

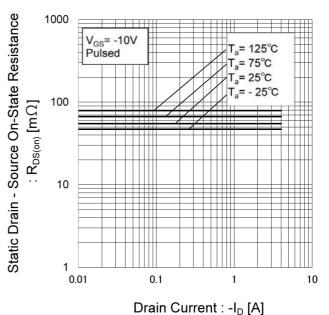


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

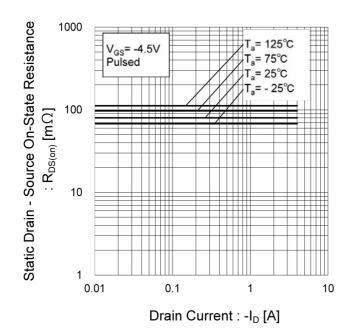


Fig.17 Typical Capacitance vs. Drain - Source Voltage

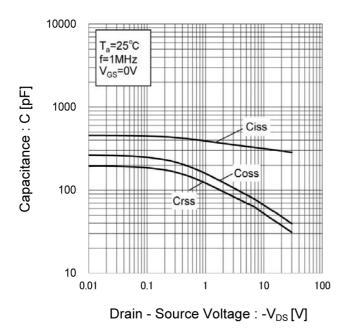


Fig.18 Switching Characteristics

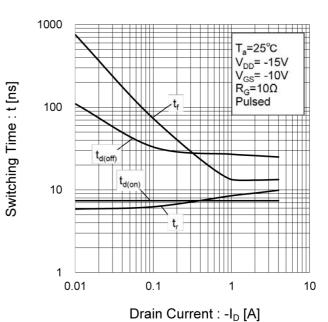


Fig.19 Dynamic Input Characteristics

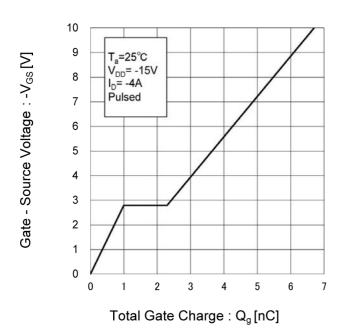
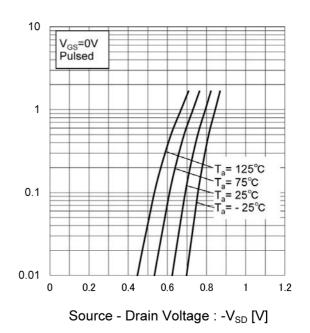


Fig.20 Source Current vs. Source Drain Voltage



Source Current : -I<sub>s</sub> [A]

## • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

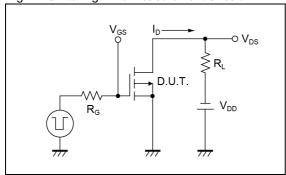


Fig.2-1 Gate Charge Measurement Circuit

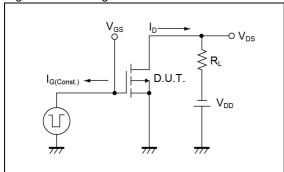


Fig.3-1 Avalanche Measurement Circuit

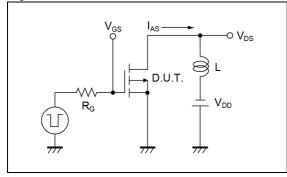


Fig.1-2 Switching Waveforms

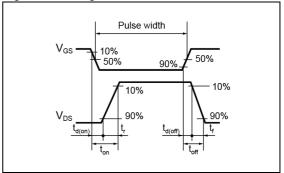


Fig.2-2 Gate Charge Waveform

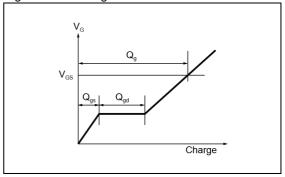
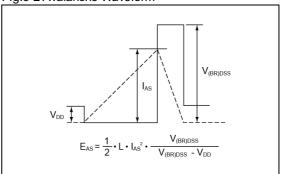


Fig.3-2 Avalanche Waveform

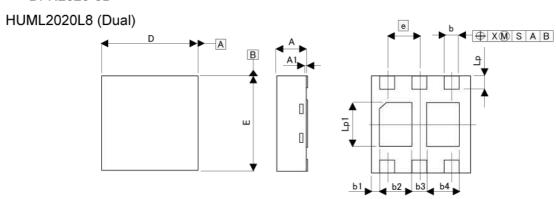


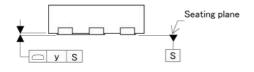
### Notice

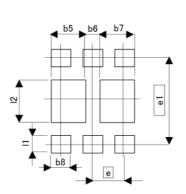
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## Dimensions

### DFN2020-8D







Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	0.3	25	0.0	10
b2	0.60	0.70	0.024	0.028
b3	0.	.3	0.0	)12
b4	0.60	0.70	0.024	0.028
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.0	0.65		26
Lp	0.225	0.325	0.009	0.013
Lp1	0.80	1.00	0.031	0.039
x	-	0.10	0H)	0.004
٧	-	0.10	1350	0.004

DIM -	MILIME	ETERS	INC	HES
DIIVI F	MIN	MAX	MIN	MAX
b5	-	0.70	5.51	0.028
b6	0.20	0.30	0.008	0.012
b7	-	0.70	0#0	0.028
b8	-	0.45	14	0.018
e1	1.7	725	0.068	
11	-	0.425	9.50	0.017
12	- 2	1.00	(E)	0.039

Dimension in mm/inches



# **Notice**

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1. Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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ИНН 7805602321 КПП 780501001 P/C 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

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С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

#### Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



Тел: +7 (812) 336 43 04 (многоканальный) Email: org@lifeelectronics.ru