

# (PNP) 2N6034, 2N6035, 2N6036; (NPN) 2N6038, 2N6039



ON Semiconductor®

<http://onsemi.com>

## Plastic Darlington Complementary Silicon Power Transistors

Plastic Darlington complementary silicon power transistors are designed for general purpose amplifier and low-speed switching applications.

### Features

- ESD Ratings: Machine Model, C; > 400 V  
Human Body Model, 3B; > 8000 V
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Pb-Free Packages are Available\*

### MAXIMUM RATINGS

| Rating   | Symbol         | Value          | Unit                      |
|--|----------------|----------------|---------------------------|
| Collector-Emitter Voltage<br>2N6034<br>2N6035, 2N6038<br>2N6036, 2N6039                | $V_{CEO}$      | 40<br>60<br>80 | Vdc                       |
| Collector-Base Voltage<br>2N6034<br>2N6035, 2N6038<br>2N6036, 2N6039                   | $V_{CBO}$      | 40<br>60<br>80 | Vdc                       |
| Emitter-Base Voltage   | $V_{EBO}$      | 5.0            | Vdc                       |
| Collector Current<br>Continuous<br>Peak  | $I_C$          | 4.0<br>8.0     | Adc<br>Apk                |
| Base Current   | $I_B$          | 100            | mAdc                      |
| Total Device Dissipation @ $T_C = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$          | 40<br>320      | W<br>mW/ $^\circ\text{C}$ |
| Total Device Dissipation @ $T_C = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$          | 1.5<br>12      | W<br>mW/ $^\circ\text{C}$ |
| Operating and Storage Junction<br>Temperature Range                                    | $T_J, T_{stg}$ | -65 to<br>+150 | $^\circ\text{C}$          |

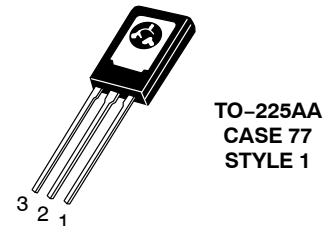
### THERMAL CHARACTERISTICS

| Characteristic                          | Symbol          | Max  | Unit                      |
|---|-----------------|------|---------------------------|
| Thermal Resistance, Junction-to-Case    | $R_{\theta JC}$ | 3.12 | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 83.3 | $^\circ\text{C}/\text{W}$ |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## 4.0 AMPERES DARLINGTON COMPLEMENTARY SILICON POWER TRANSISTORS 40, 60, 80 VOLTS, 40 WATTS



### MARKING DIAGRAM



Y = Year  
WW = Work Week  
2N603x = Device Code  
x = 4, 5, 6, 8, 9  
G = Pb-Free Package

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

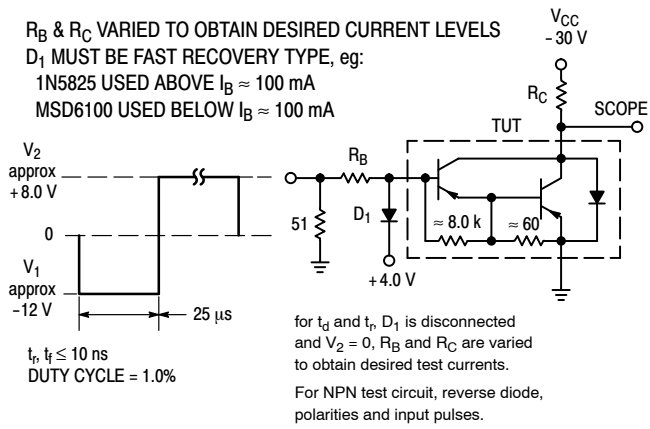
## (PNP) 2N6034, 2N6035, 2N6036; (NPN) 2N6038, 2N6039

### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

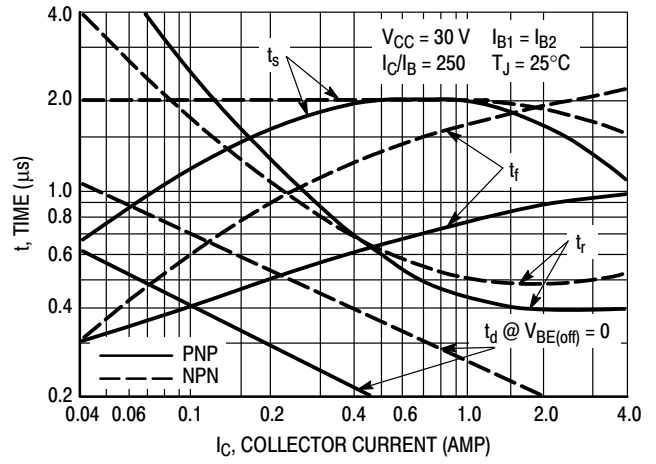
| Characteristic   | Symbol   | Min                        | Max                                    | Unit |
|--|--|----------------------------|--|------|
| <b>OFF CHARACTERISTICS</b>   |  |                            |  |      |
| Collector–Emitter Sustaining Voltage<br>(I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)  | V <sub>CE(sus)</sub>   | 40<br>60<br>80             | –<br>–<br>–                            | Vdc  |
|  | 2N6034<br>2N6035, 2N6038<br>2N6036, 2N6039   |                            |  |      |
| Collector–Cutoff Current<br>(V <sub>CE</sub> = 40 Vdc, I <sub>B</sub> = 0)<br>(V <sub>CE</sub> = 60 Vdc, I <sub>B</sub> = 0)<br>(V <sub>CE</sub> = 80 Vdc, I <sub>B</sub> = 0)   | I <sub>CEO</sub>   | –<br>–<br>–                | 100<br>100<br>100                      | μA   |
|  | 2N6034<br>2N6035, 2N6038<br>2N6036, 2N6039   |                            |  |      |
| Collector–Cutoff Current<br>(V <sub>CE</sub> = 40 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc)<br>(V <sub>CE</sub> = 60 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc)<br>(V <sub>CE</sub> = 80 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc)<br>(V <sub>CE</sub> = 40 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc, T <sub>C</sub> = 125°C)<br>(V <sub>CE</sub> = 60 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc, T <sub>C</sub> = 125°C)<br>(V <sub>CE</sub> = 80 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc, T <sub>C</sub> = 125°C) | I <sub>CEX</sub>   | –<br>–<br>–<br>–<br>–<br>– | 100<br>100<br>100<br>500<br>500<br>500 | μA   |
|  | 2N6034<br>2N6035, 2N6038<br>2N6036, 2N6039<br>2N6034<br>2N6035, 2N6038<br>2N6036, 2N6039 |                            |  |      |
| Collector–Cutoff Current<br>(V <sub>CB</sub> = 40 Vdc, I <sub>E</sub> = 0)<br>(V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0)<br>(V <sub>CB</sub> = 80 Vdc, I <sub>E</sub> = 0)   | I <sub>CBO</sub>   | –<br>–<br>–                | 0.5<br>0.5<br>0.5                      | mAdc |
|  | 2N6034<br>2N6035, 2N6038<br>2N6036, 2N6039   |                            |  |      |
| Emitter–Cutoff Current (V <sub>BE</sub> = 5.0 Vdc, I <sub>C</sub> = 0)   | I <sub>EBO</sub>   | –                          | 2.0                                    | mAdc |
| <b>ON CHARACTERISTICS</b>  |  |                            |  |      |
| DC Current Gain<br>(I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 3.0 Vdc)<br>(I <sub>C</sub> = 2.0 Adc, V <sub>CE</sub> = 3.0 Vdc)<br>(I <sub>C</sub> = 4.0 Adc, V <sub>CE</sub> = 3.0 Vdc)   | h <sub>FE</sub>  | 500<br>750<br>100          | –<br>15,000<br>–                       | –    |
| Collector–Emitter Saturation Voltage<br>(I <sub>C</sub> = 2.0 Adc, I <sub>B</sub> = 8.0 mAdc)<br>(I <sub>C</sub> = 4.0 Adc, I <sub>B</sub> = 40 mAdc)  | V <sub>CE(sat)</sub>   | –<br>–                     | 2.0<br>3.0                             | Vdc  |
| Base–Emitter Saturation Voltage<br>(I <sub>C</sub> = 4.0 Adc, I <sub>B</sub> = 40 mAdc)  | V <sub>BE(sat)</sub>   | –                          | 4.0                                    | Vdc  |
| Base–Emitter On Voltage<br>(I <sub>C</sub> = 2.0 Adc, V <sub>CE</sub> = 3.0 Vdc)   | V <sub>BE(on)</sub>  | –                          | 2.8                                    | Vdc  |
| <b>DYNAMIC CHARACTERISTICS</b>   |  |                            |  |      |
| Small–Signal Current–Gain<br>(I <sub>C</sub> = 0.75 Adc, V <sub>CE</sub> = 10 Vdc, f = 1.0 MHz)  | h <sub>fe</sub>  | 25                         | –                                      | –    |
| Output Capacitance<br>(V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 0.1 MHz)  | C <sub>ob</sub>  | –<br>–                     | 200<br>100                             | pF   |
|  | 2N6034, 2N6035, 2N6036<br>2N6038, 2N6039   |                            |  |      |

\*Indicates JEDEC Registered Data.

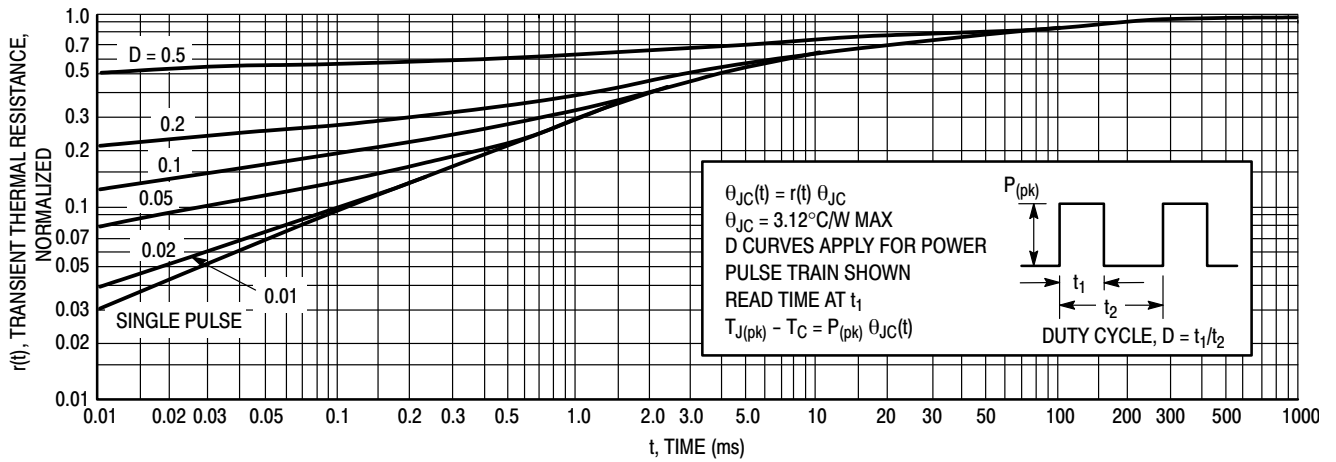
**(PNP) 2N6034, 2N6035, 2N6036; (NPN) 2N6038, 2N6039**



**Figure 1. Switching Times Test Circuit**



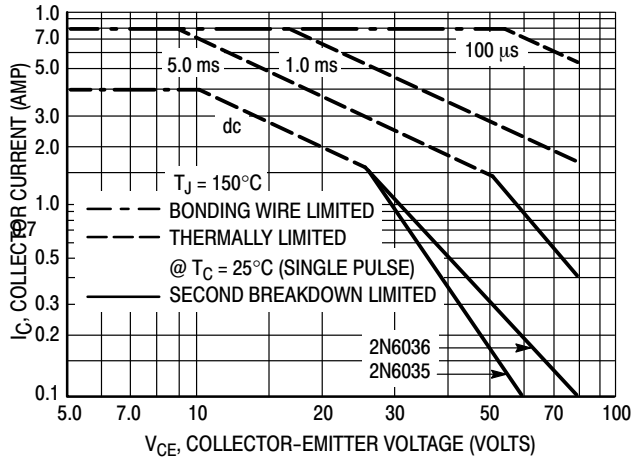
**Figure 2. Switching Times**



**Figure 3. Thermal Response**

**(PNP) 2N6034, 2N6035, 2N6036; (NPN) 2N6038, 2N6039**

**ACTIVE-REGION SAFE-OPERATING AREA**



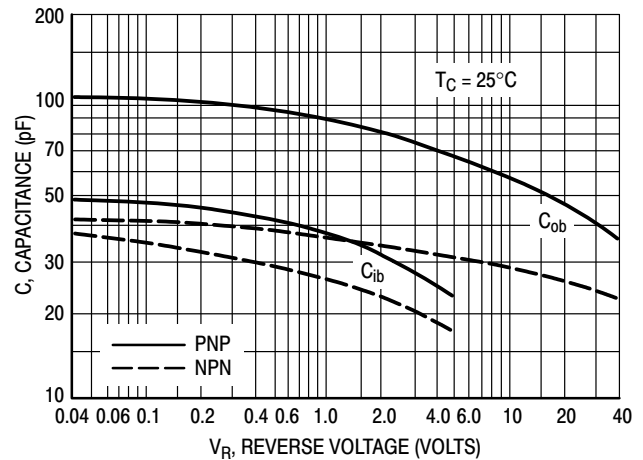
**Figure 4. 2N6035, 2N6036**



**Figure 5. 2N6038, 2N6039**

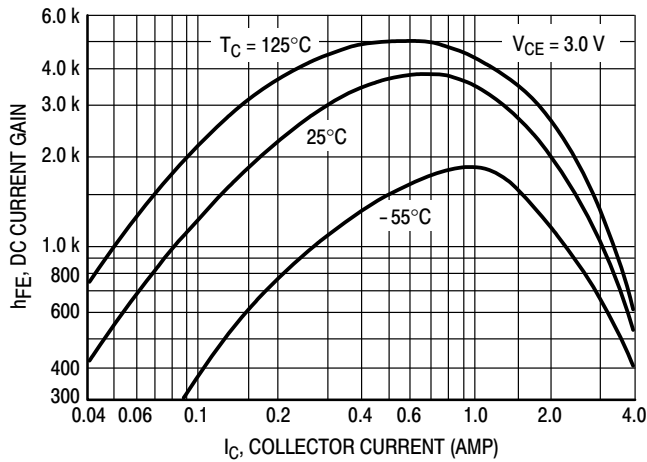
There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 4 and 5 is based on  $T_{J(pk)} = 150^\circ\text{C}$ ;  $T_C$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} < 150^\circ\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 3. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



**Figure 6. Capacitance**

**PNP**  
**2N6034, 2N6035, 2N6036**



**NPN**  
**2N6038, 2N6039**

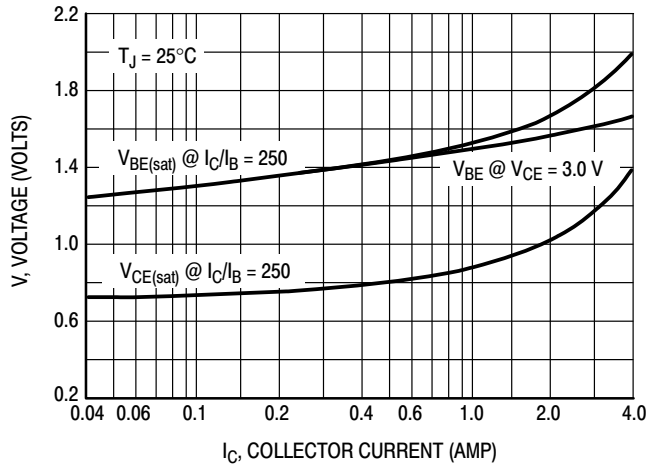
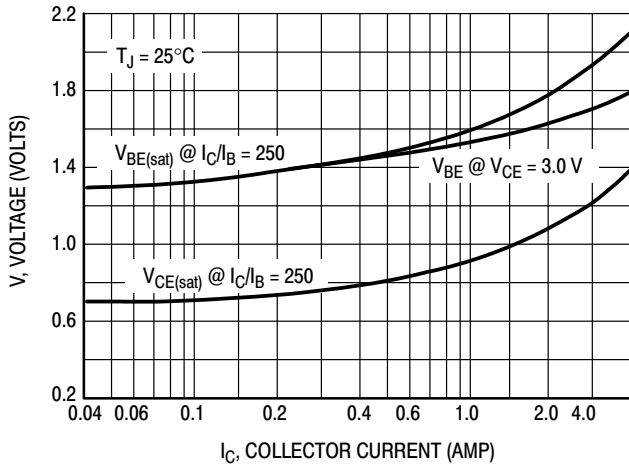


**Figure 7. DC Current Gain**

**(PNP) 2N6034, 2N6035, 2N6036; (NPN) 2N6038, 2N6039**



**Figure 8. Collector Saturation Region**



**Figure 9. "On" Voltages**

**ORDERING INFORMATION**

| Device  | Package               | Shipping        |
|---------|-----------------------|-----------------|
| 2N6034  | TO-225AA              | 500 Units / Box |
| 2N6034G | TO-225AA<br>(Pb-Free) |                 |
| 2N6035  | TO-225AA              |                 |
| 2N6035G | TO-225AA<br>(Pb-Free) |                 |
| 2N6036  | TO-225AA              |                 |
| 2N6036G | TO-225AA<br>(Pb-Free) |                 |
| 2N6038  | TO-225AA              |                 |
| 2N6038G | TO-225AA<br>(Pb-Free) |                 |
| 2N6039  | TO-225AA              |                 |
| 2N6039G | TO-225AA<br>(Pb-Free) |                 |

(PNP) 2N6034, 2N6035, 2N6036; (NPN) 2N6038, 2N6039

PACKAGE DIMENSIONS

TO-225AA  
CASE 77-09  
ISSUE Z



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 077-01 THRU -08 OBSOLETE, NEW STANDARD 077-09.

| DIM | INCHES    |       | MILLIMETERS |       |
|-----|-----------|-------|-------------|-------|
|     | MIN       | MAX   | MIN         | MAX   |
| A   | 0.425     | 0.435 | 10.80       | 11.04 |
| B   | 0.295     | 0.305 | 7.50        | 7.74  |
| C   | 0.095     | 0.105 | 2.42        | 2.66  |
| D   | 0.020     | 0.026 | 0.51        | 0.66  |
| F   | 0.115     | 0.130 | 2.93        | 3.30  |
| G   | 0.094 BSC |       | 2.39 BSC    |       |
| H   | 0.050     | 0.095 | 1.27        | 2.41  |
| J   | 0.015     | 0.025 | 0.39        | 0.63  |
| K   | 0.575     | 0.655 | 14.61       | 16.63 |
| M   | 5° TYP    |       | 5° TYP      |       |
| Q   | 0.148     | 0.158 | 3.76        | 4.01  |
| R   | 0.045     | 0.065 | 1.15        | 1.65  |
| S   | 0.025     | 0.035 | 0.64        | 0.88  |
| U   | 0.145     | 0.155 | 3.69        | 3.93  |
| V   | 0.040     | ---   | 1.02        | ---   |

STYLE 1:

1. EMITTER
2. COLLECTOR
3. BASE

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- Защиту от снятия компонента с производства.
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