

#### Is Now Part of



## ON Semiconductor®

## To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="general-regarding-numbers-n

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



## FSB50325A / FSB50325AT Motion SPM<sup>®</sup> 5 Series

# January 2014

#### **Features**

- UL Certified No. E209204 (UL1557)
- 250 V R<sub>DS(on)</sub> = 1.7 Ω(Max) FRFET MOSFET 3-Phase Inverter with Gate Drivers and Protection
- Built-In Bootstrap Diodes Simplify PCB Layout
- Separate Open-Source Pins from Low-Side MOSFETs for Three-Phase Current-Sensing
- Active-HIGH Interface, Works with 3.3 / 5 V Logic, Schmitt-trigger Input
- · Optimized for Low Electromagnetic Interference
- HVIC Temperature-Sensing Built-In for Temperature Monitoring
- HVIC for Gate Driving and Under-Voltage Protection
- Isolation Rating: 1500 V<sub>rms</sub> / min.
- · RoHS Compliant

#### **Applications**

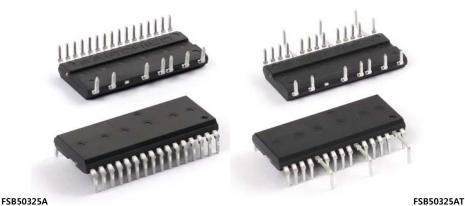
 3-Phase Inverter Driver for Small Power AC Motor Drives

#### **Related Source**

- RD-FSB50450A Reference Design for Motion SPM\_5 Series Ver.2
- AN-9082 Motion SPM5 Series Thermal Performance by Contact Pressure
- AN-9080 User's Guide for Motion SPM 5 Series V2

#### **General Description**

The FSB50325A/AT is an advanced Motion SPM® 5 module providing a fully-featured, high-performance inverter output stage for AC Induction, BLDC and PMSM motors. These modules integrate optimized gate drive of the built-in MOSFETs (FRFET® technology) to minimize EMI and losses, while also providing multiple on-module protection features including under-voltage lockouts and The built-in thermal monitoring. high-speed HVIC requires only a single supply voltage and translates the incoming logic-level gate inputs to the high-voltage, high-current drive signals required to properly drive the module's internal MOSFETs. Separate open-source MOSFET terminals are available for each phase to support the widest variety of control algorithms.



#### Package Marking & Ordering Information

Device	Device Marking	Package	Packing Type	Quantity
FSB50325A	FSB50325A	SPM5P-023	Rail	15
FSB50325AT	FSB50325AT	SPM5N-023	Rail	15

## **Absolute Maximum Ratings**

Inverter Part (each MOSFET unless otherwise specified.)

Symbol	Parameter	Conditions	Rating	Unit
V <sub>DSS</sub>	Drain-Source Voltage of Each MOSFET		250	V
*I <sub>D 25</sub>	Each MOSFET Drain Current, Continuous	T <sub>C</sub> = 25°C	1.7	Α
*I <sub>D 80</sub>	Each MOSFET Drain Current, Continuous	T <sub>C</sub> = 80°C	1.3	Α
*I <sub>DP</sub>	Each MOSFET Drain Current, Peak	$T_C = 25^{\circ}C$ , PW < 100 $\mu$ s	4.4	Α
*I <sub>DRMS</sub>	Each MOSFET Drain Current, Rms	$T_C = 80$ °C, $F_{PWM} < 20 \text{ kHz}$	0.9	A <sub>rms</sub>
*P <sub>D</sub>	Maximum Power Dissipation	T <sub>C</sub> = 25°C, For Each MOSFET	12.3	W

#### Control Part (each HVIC unless otherwise specified.)

Symbol	Parameter	Conditions	Rating	Unit
V <sub>CC</sub>	Control Supply Voltage	Applied between V <sub>CC</sub> and COM	20	V
V <sub>BS</sub>	High-side Bias Voltage	Applied between V <sub>B</sub> and V <sub>S</sub>	20	V
V <sub>IN</sub>	Input Signal Voltage	Applied between V <sub>IN</sub> and COM	-0.3 ~ V <sub>CC</sub> + 0.3	V

#### Bootstrap Diode Part (each bootstrap diode unless otherwise specified.)

Symbol	bol Parameter Conditions		Rating	Unit
V <sub>RRMB</sub>	Maximum Repetitive Reverse Voltage		250	V
* I <sub>FB</sub>	Forward Current	T <sub>C</sub> = 25°C	0.5	А
* I <sub>FPB</sub>	Forward Current (Peak)	T <sub>C</sub> = 25°C, Under 1ms Pulse Width	1.5	А

#### **Thermal Resistance**

Symbol	Parameter	Conditions	Rating	Unit
$R_{ heta JC}$	Junction to Case Thermal Resistance	Each MOSFET under Inverter Operating Condition (1st Note 1)	10.2	°C/W

#### **Total System**

Symbol	Parameter	Parameter Conditions		Unit
T <sub>J</sub>	Operating Junction Temperature		-40 ~ 150	°C
T <sub>STG</sub>	Storage Temperature		-40 ~ 125	°C
V <sub>ISO</sub>	Isolation Voltage	60 Hz, Sinusoidal, 1 Minute, Connect Pins to Heat Sink Plate	1500	$V_{rms}$

#### 1st Notes:

- 1. For the measurement point of case temperature  $\mathsf{T}_\mathsf{C},$  please refer to Figure 4.
- 2. Marking " \* " is calculation value or design factor.

### Pin descriptions

Pin Number	Pin Name	Pin Description
1	СОМ	IC Common Supply Ground
2	V <sub>B(U)</sub>	Bias Voltage for U-Phase High-Side MOSFET Driving
3	V <sub>CC(U)</sub>	Bias Voltage for U-Phase IC and Low-Side MOSFET Driving
4	IN <sub>(UH)</sub>	Signal Input for U-Phase High-Side
5	IN <sub>(UL)</sub>	Signal Input for U-Phase Low-Side
6	N.C	No Connection
7	V <sub>B(V)</sub>	Bias Voltage for V-Phase High Side MOSFET Driving
8	V <sub>CC(V)</sub>	Bias Voltage for V-Phase IC and Low Side MOSFET Driving
9	IN <sub>(VH)</sub>	Signal Input for V-Phase High-Side
10	IN <sub>(VL)</sub>	Signal Input for V-Phase Low-Side
11	V <sub>TS</sub>	Output for HVIC Temperature Sensing
12	V <sub>B(W)</sub>	Bias Voltage for W-Phase High-Side MOSFET Driving
13	V <sub>CC(W)</sub>	Bias Voltage for W-Phase IC and Low-Side MOSFET Driving
14	IN <sub>(WH)</sub>	Signal Input for W-Phase High-Side
15	IN <sub>(WL)</sub>	Signal Input for W-Phase Low-Side
16	N.C	No Connection
17	Р	Positive DC-Link Input
18	U, V <sub>S(U)</sub>	Output for U-Phase & Bias Voltage Ground for High-Side MOSFET Driving
19	N <sub>U</sub>	Negative DC-Link Input for U-Phase
20	N <sub>V</sub>	Negative DC-Link Input for V-Phase
21	V, V <sub>S(V)</sub>	Output for V-Phase & Bias Voltage Ground for High-Side MOSFET Driving
22	N <sub>W</sub>	Negative DC-Link Input for W-Phase
23	W, V <sub>S(W)</sub>	Output for W Phase & Bias Voltage Ground for High-Side MOSFET Driving

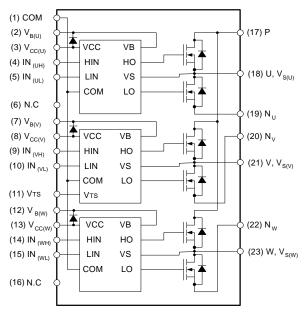


Figure 1. Pin Configuration and Internal Block Diagram (Bottom View)

#### 1st Notes:

<sup>3.</sup> Source terminal of each low-side MOSFET is not connected to supply ground or bias voltage ground inside Motion SPM® 5 product. External connections should be made as indicated in Figure 3.

## **Electrical Characteristics** ( $T_J = 25^{\circ}C$ , $V_{CC} = V_{BS} = 15 \text{ V}$ unless otherwise specified.)

Inverter Part (each MOSFET unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
BV <sub>DSS</sub>	Drain - Source Breakdown Voltage	V <sub>IN</sub> = 0 V, I <sub>D</sub> = 1 mA (2nd Note 1)	250	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>IN</sub> = 0 V, V <sub>DS</sub> = 250 V	-	-	1	mA
R <sub>DS(on)</sub>	Static Drain - Source Turn-On Resistance	V <sub>CC</sub> = V <sub>BS</sub> = 15 V, V <sub>IN</sub> = 5 V, I <sub>D</sub> = 1.0 A	-	1.1	1.7	Ω
V <sub>SD</sub>	Drain - Source Diode Forward Voltage	$V_{CC} = V_{BS} = 15V, V_{IN} = 0 V, I_D = -1.0 A$	-	-	1.2	V
t <sub>ON</sub>			-	810	-	ns
t <sub>OFF</sub>		$V_{PN}$ = 150 V, $V_{CC}$ = $V_{BS}$ = 15 V, $I_{D}$ = 1.0 A $V_{IN}$ = 0 V $\leftrightarrow$ 5 V, Inductive Load L = 3 mH High- and Low-Side MOSFET Switching (2nd Note 2)	-	600	-	ns
t <sub>rr</sub>	Switching Times		-	140	-	ns
E <sub>ON</sub>			-	40	-	μJ
E <sub>OFF</sub>			-	10	-	μJ
RBSOA	Reverse Bias Safe Operating Area	$V_{PN}$ = 200 V, $V_{CC}$ = $V_{BS}$ = 15 V, $I_D$ = $I_{DP}$ , $V_{DS}$ = $BV_{DSS}$ , $T_J$ = 150°C High- and Low-Side MOSFET Switching (2nd Note 3)		Full	Square	

#### Control Part (each HVIC unless otherwise specified.)

Symbol	Parameter		Conditions	Min	Тур	Max	Unit
I <sub>QCC</sub>	Quiescent V <sub>CC</sub> Current	V <sub>CC</sub> = 15 V, V <sub>IN</sub> = 0 V	Applied between V <sub>CC</sub> and COM	-	-	200	μА
I <sub>QBS</sub>	Quiescent V <sub>BS</sub> Current	V <sub>BS</sub> = 15 V, V <sub>IN</sub> = 0 V	Applied between $V_{B(U)}$ - U, $V_{B(V)}$ - V, $V_{B(W)}$ - W	-	-	100	μА
UV <sub>CCD</sub>	Low-Side Under-Voltage	V <sub>CC</sub> Under-Voltage	V <sub>CC</sub> Under-Voltage Protection Detection Level		8.0	9.4	V
UV <sub>CCR</sub>	Protection (Figure 8)	V <sub>CC</sub> Under-Voltage Protection Reset Level		8.0	8.9	9.8	V
UV <sub>BSD</sub>	High-Side Under-Voltage	V <sub>BS</sub> Under-Voltage	Protection Detection Level	7.4	8.0	9.4	V
UV <sub>BSR</sub>	Protection (Figure 9)	V <sub>BS</sub> Under-Voltage	V <sub>BS</sub> Under-Voltage Protection Reset Level		8.9	9.8	V
V <sub>TS</sub>	HVIC Temperature Sens- ing Voltage Output	V <sub>CC</sub> = 15 V, T <sub>HVIC</sub> = 25°C (2nd Note 4)		600	790	980	mV
V <sub>IH</sub>	ON Threshold Voltage	Logic HIGH Level		-	-	2.9	V
V <sub>IL</sub>	OFF Threshold Voltage	Logic LOW Level	gic LOW Level Applied between V <sub>IN</sub> and COM		-	-	V

#### Bootstrap Diode Part (each bootstrap diode unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{FB}$	Forward Voltage	I <sub>F</sub> = 0.1 A, T <sub>C</sub> = 25°C (2nd Note 5)	-	2.5	-	V
t <sub>rrB</sub>	Reverse Recovery Time	$I_F = 0.1 \text{ A}, T_C = 25^{\circ}\text{C}$	-	80	-	ns

#### 2nd Notes:

- 1. BV<sub>DSS</sub> is the absolute maximum voltage rating between drain and source terminal of each MOSFET inside Motion SPM<sup>®</sup> 5 product. V<sub>PN</sub> should be sufficiently less than this value considering the effect of the stray inductance so that V<sub>PN</sub> should not exceed BV<sub>DSS</sub> in any case.
- 2. toN and toFF include the propagation delay of the internal drive IC. Listed values are measured at the laboratory test condition, and they can be different according to the field applications due to the effect of different printed circuit boards and wirings. Please see Figure 6 for the switching time definition with the switching test circuit of Figure 7.
- 3. The peak current and voltage of each MOSFET during the switching operation should be included in the Safe Operating Area (SOA). Please see Figure 7 for the RBSOA test circuit that is same as the switching test circuit.
- 4.  $V_{ts}$  is only for sensing-temperature of module and cannot shutdown MOSFETs automatically.
- 5. Built-in bootstrap diode includes around 15  $\,\Omega$  resistance characteristic. Please refer to Figure 2.

## **Recommended Operating Condition**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>PN</sub>	Supply Voltage	Applied between P and N	-	150	200	V
V <sub>CC</sub>	Control Supply Voltage	Applied between V <sub>CC</sub> and COM	13.5	15.0	16.5	V
V <sub>BS</sub>	High-Side Bias Voltage	Applied between V <sub>B</sub> and V <sub>S</sub>	13.5	15.0	16.5	V
V <sub>IN(ON)</sub>	Input ON Threshold Voltage	Applied between V <sub>IN</sub> and COM	3.0	-	V <sub>CC</sub>	V
V <sub>IN(OFF)</sub>	Input OFF Threshold Voltage	"	0	-	0.6	V
t <sub>dead</sub>	Blanking Time for Preventing Arm-Short	$V_{CC} = V_{BS} = 13.5 \sim 16.5 \text{ V}, T_{J} \le 150^{\circ}\text{C}$	1.0	-	-	μS
f <sub>PWM</sub>	PWM Switching Frequency	T <sub>J</sub> ≤ 150°C	-	15	=	kHz

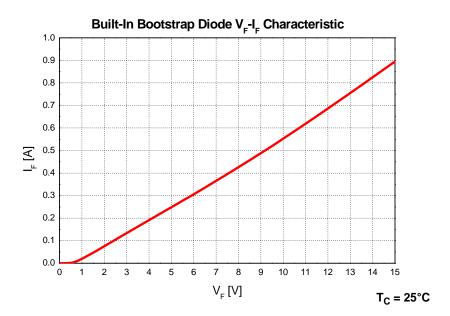


Figure 2. Built-In Bootstrap Diode Characteristics (Typical)

#### These values depend on PWM control algorithm

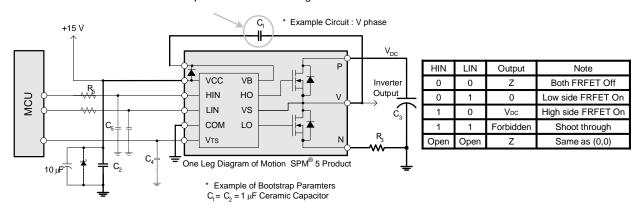
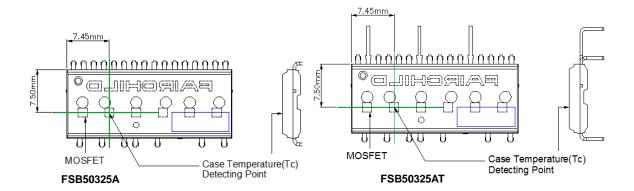


Figure 3. Recommended MCU Interface and Bootstrap Circuit with Parameters

#### 3rd Notes

- 1. Parameters for bootstrap circuit elements are dependent on PWM algorithm. For 15 kHz of switching frequency, typical example of parameters is shown above.
- 2. RC-coupling (R<sub>5</sub> and C<sub>5</sub>) and C<sub>4</sub> at each input of Motion SPM 5 product and MCU (Indicated as Dotted Lines) may be used to prevent improper signal due to surge-noise.
- Bold lines should be short and thick in PCB pattern to have small stray inductance of circuit, which results in the reduction of surge-voltage. Bypass capacitors such as C<sub>1</sub>, C<sub>2</sub> and C<sub>3</sub> should have good high-frequency characteristics to absorb high-frequency ripple-current.



**Figure 4. Case Temperature Measurement** 

#### 3rd Notes:

4. Attach the thermocouple on top of the heat-sink of SPM 5 package (between SPM 5 package and heatsink if applied) to get the correct temperature measurement.

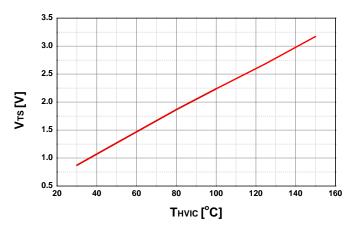
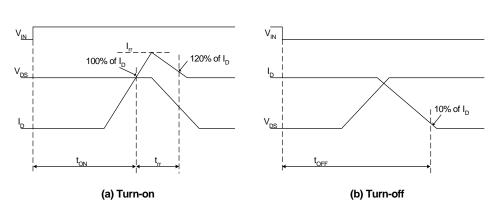


Figure 5. Temperature Profile of V<sub>TS</sub> (Typical)



**Figure 6. Switching Time Definitions** 

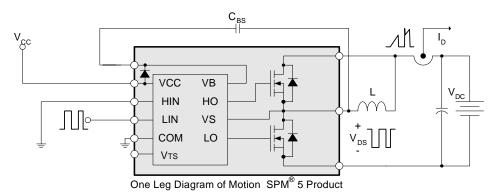


Figure 7. Switching and RBSOA (Single-Pulse) Test Circuit (Low-side)

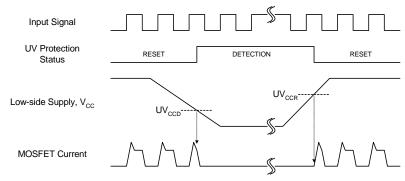


Figure 8. Under-Voltage Protection (Low-Side)

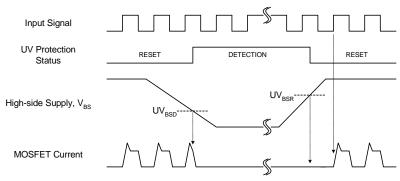


Figure 9. Under-Voltage Protection (High-Side)

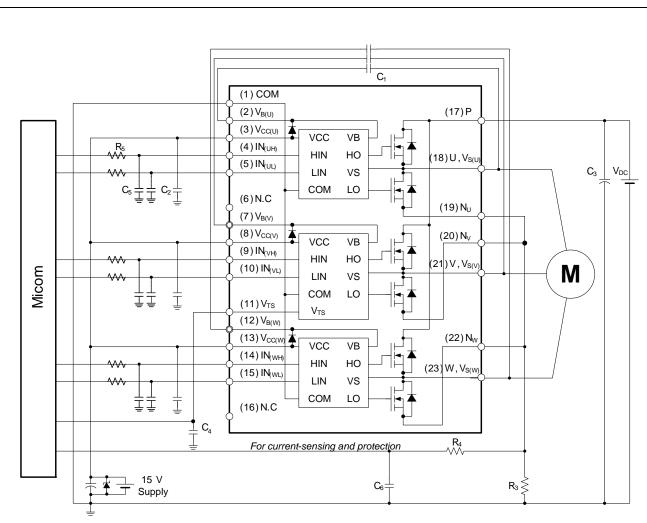
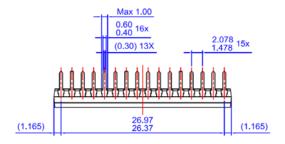


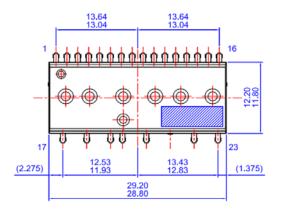
Figure 10. Example of Application Circuit

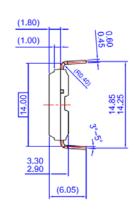
#### 4th Notes:

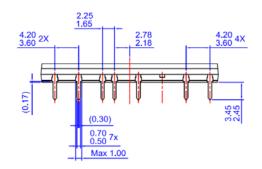
- 1. About pin position, refer to Figure 1.
- 2. RC-coupling (R<sub>5</sub> and C<sub>5</sub>, R<sub>4</sub> and C<sub>6</sub>) and C<sub>4</sub> at each input of Motion SPM<sup>®</sup> 5 product and MCU are useful to prevent improper input signal caused by surge-noise.
- 3. The voltage-drop across R<sub>3</sub> affects the low-side switching performance and the bootstrap characteristics since it is placed between COM and the source terminal of the low-side MOSFET. For this reason, the voltage-drop across R<sub>3</sub> should be less than 1 V in the steady-state.
- 4. Ground-wires and output terminals, should be thick and short in order to avoid surge-voltage and malfunction of HVIC.
- 5. All the filter capacitors should be connected close to Motion SPM 5 product, and they should have good characteristics for rejecting high-frequency ripple current.

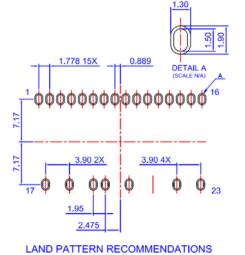
#### **Detailed Package Outline Drawings (FSB50325A)**











NOTES: UNLESS OTHERWISE SPECIFIED

A) THIS PACKAGE DOES NOT COMPLY

A) THIS PACKAGE DOES NOT COMPLY TO ANY CURRENT PACKAGING STANDARD

- B) ALL DIMENSIONS ARE IN MILLIMETERS
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS
- D) ( ) IS REFERENCE
- E) [ ] IS ASS'Y QUALITY
- F) DRAWING FILENAME: MOD23DCREV2.0
- G) FAIRCHILD SEMICONDUCTOR

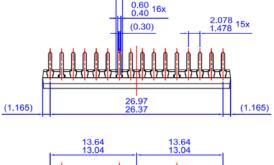
Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or data on the drawing and contact a FairchildSemiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide therm and conditions, specifically the the warranty therein, which covers Fairchild products.

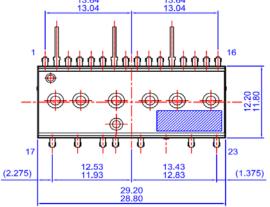
Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

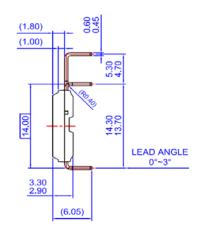
http://www.fairchildsemi.com/dwg/MO/MOD23DC.pdf

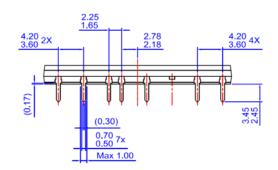
#### **Detailed Package Outline Drawings (FSB50325AT)**

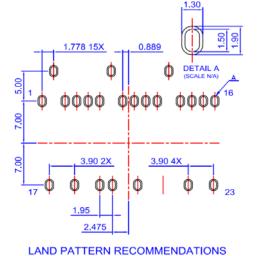
Max 1.00











NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE DOES NOT COMPLY TO ANY CURRENT PACKAGING STANDARD
- B) ALL DIMENSIONS ARE IN MILLIMETERS
- C) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS
- D) ( ) IS REFERENCE
- E) [ ] IS ASS'Y QUALITY
- F) DRAWING FILENAME: MOD23DFREV2.0
- G) FAIRCHILD SEMICONDUCTOR

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or data on the drawing and contact a FairchildSemiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide therm and conditions, specifically the the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/dwg/MO/MOD23DF.pdf





#### **TRADEMARKS**

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

F-PFS" AX-CAP FRFET® BitSiC™ Global Power Resource SM

GreenBridge<sup>™</sup> Build it Now™ CorePLUS™ Green FPS™ CorePOWER™ Green FPS™ e-Series™

**CROSSVOLT™** Gmax™ CTL™ **GTO™** IntelliMAXTM Current Transfer Logic™ DEUXPEED<sup>®</sup> ISOPLANAR™

Dual Cool™ Making Small Speakers Sound Louder

EcoSPARK® and Better™ EfficientMax™ MegaBuck<sup>TI</sup> MICROCOUPLER™ ESBC<sup>TM</sup>

MicroFET<sup>11</sup> MicroPak™ Fairchild<sup>®</sup> MicroPak2™ Fairchild Semiconductor® MillerDrive™ FACT Quiet Series™

MotionMax™ mWSaver FAST® OptoHiT™ FastvCore™ **OPTOLOGIC®** FETBench™ **OPTOPLANAR**<sup>®</sup> **FPS™** 

PowerTrench® PowerXS™

Programmable Active Droop™

OFFT OSTM Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™

SignalWise<sup>TI</sup> SmartMax™ SMART START™

Solutions for Your Success™

SPM® STEALTH\* SuperFET® SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™

Sync-Lock™ SYSTEM SERVERALS

TinyBoost<sup>®</sup> TinyBuck TinyCalc™ TinyLogic<sup>®</sup> TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®\*

UHC Ultra FRFET™ UniFET™ VCX\*\* VisualMax™ VoltagePlus™ XS™

uSerDes™

\* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

#### As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- 2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts, Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full fraceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

#### PRODUCT STATUS DEFINITIONS

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 166

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdt/Patent-Marking.pdf">www.onsemi.com/site/pdt/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

## **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor:

FSB50325AT



OOO «ЛайфЭлектроникс" "LifeElectronics" LLC

ИНН 7805602321 КПП 780501001 P/C 40702810122510004610 ФАКБ "АБСОЛЮТ БАНК" (ЗАО) в г.Санкт-Петербурге К/С 3010181090000000703 БИК 044030703

Компания «Life Electronics» занимается поставками электронных компонентов импортного и отечественного производства от производителей и со складов крупных дистрибьюторов Европы, Америки и Азии.

С конца 2013 года компания активно расширяет линейку поставок компонентов по направлению коаксиальный кабель, кварцевые генераторы и конденсаторы (керамические, пленочные, электролитические), за счёт заключения дистрибьюторских договоров

#### Мы предлагаем:

- Конкурентоспособные цены и скидки постоянным клиентам.
- Специальные условия для постоянных клиентов.
- Подбор аналогов.
- Поставку компонентов в любых объемах, удовлетворяющих вашим потребностям.
- Приемлемые сроки поставки, возможна ускоренная поставка.
- Доставку товара в любую точку России и стран СНГ.
- Комплексную поставку.
- Работу по проектам и поставку образцов.
- Формирование склада под заказчика.
- Сертификаты соответствия на поставляемую продукцию (по желанию клиента).
- Тестирование поставляемой продукции.
- Поставку компонентов, требующих военную и космическую приемку.
- Входной контроль качества.
- Наличие сертификата ISO.

В составе нашей компании организован Конструкторский отдел, призванный помогать разработчикам, и инженерам.

Конструкторский отдел помогает осуществить:

- Регистрацию проекта у производителя компонентов.
- Техническую поддержку проекта.
- Защиту от снятия компонента с производства.
- Оценку стоимости проекта по компонентам.
- Изготовление тестовой платы монтаж и пусконаладочные работы.



Тел: +7 (812) 336 43 04 (многоканальный) Email: org@lifeelectronics.ru